TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOSVI)

# 2SK3903

### Switching Regulator Applications

• Low drain-source ON resistance:  $R_{DS (ON)} = 0.32 \Omega (typ.)$ 

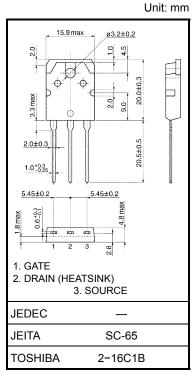
• High forward transfer admittance:  $|Y_{fS}| = 7.5 \text{ S (typ.)}$ 

Low leakage current: I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 600 V)

• Enhancement model:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
Drain-source voltage			$V_{DSS}$	600	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )			$V_{DGR}$	600	V	
Gate-source voltage			V <sub>GSS</sub>	±30	V	
Drain current	DC	(Note 1)	I <sub>D</sub>	14	А	
	Pulse	(Note 1)	$I_{DP}$	56		
Drain power dissipation (Tc = 25°C)			$P_{D}$	150	W	
Single pulse avalanche energy (Note 2)			EAS	806	mJ	
Avalanche current			I <sub>AR</sub>	14	Α	
Repetitive avalanche energy (Note 3)			E <sub>AR</sub>	15	mJ	
Channel temperature			T <sub>ch</sub>	150	°C	
Storage temperature range			T <sub>stg</sub>	-55 to 150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.833	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

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Note 1: Ensure that the channel temperature does not exceed 150°C during use of the device.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$ , L = 7.2 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 14 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by max junction temperature

This transistor is an electrostatic-sensitive device. Handle with care.



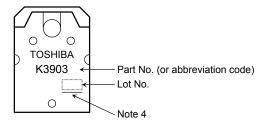
## **Electrical Characteristics (Ta = 25°C)**

Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate-source brea	ıkdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	— 100 µ	
Drain-source brea	rain-source breakdown voltage		$I_D=10~mA,~V_{GS}=0~V$	600	_		V
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	2.0	_	4.0	V
Drain-source on resistance		R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, I_D = 7 \text{ A}$		0.32	0.44	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 7 \text{ A}$	2.1	7.5		S
Input capacitance		C <sub>iss</sub>			3100		pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		20		
Output capacitance		Coss			270		
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 7 \text{ A} \text{ VOUT}$ $V_{GS} = 29 \Omega$ $V_{DD} \approx 200 \text{ V}$ $V_{DD} \approx 200 \text{ V}$	_	70	_	ns
	Turn-on time	t <sub>on</sub>			130		
	Fall time	t <sub>f</sub>			70		
	Turn-off time	t <sub>off</sub>		_	280	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	62	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 14 \text{ A}$		40		nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>		_	22	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	$I_{DR}$	_		_	14	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	-	_	56	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 14 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 14 \text{ A}, V_{GS} = 0 \text{ V},$	-	1300		ns
Reverse recovery charge	Qrr	$dI_{DR}/dt = 100 \text{ A}/\mu\text{s}$		18	_	μС

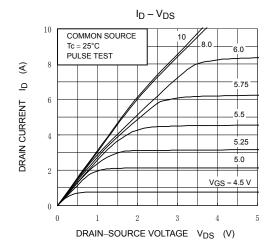
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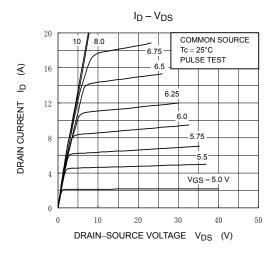


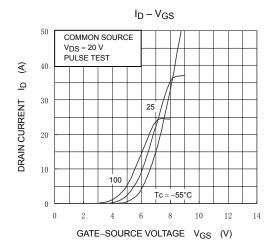
Note 4: A line under a Lot No. identifies the indication of product Labels.

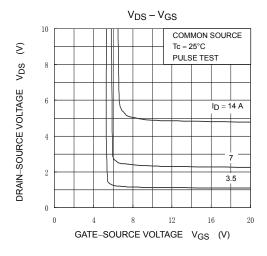
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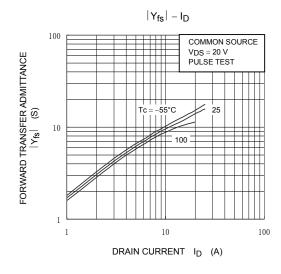
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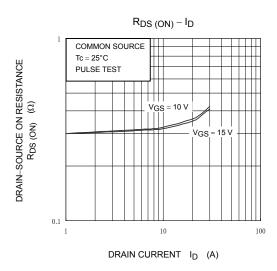


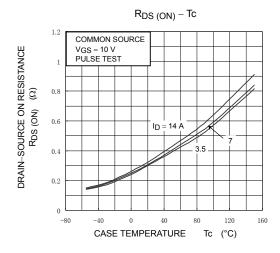


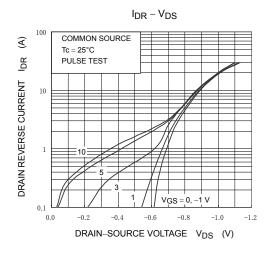


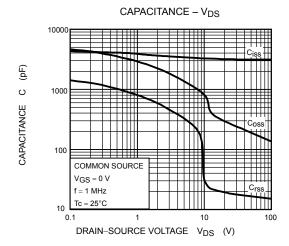


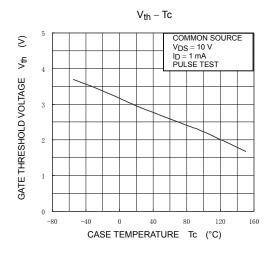


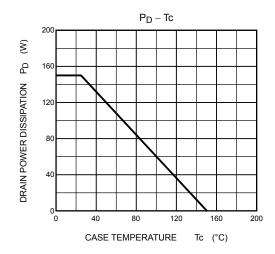


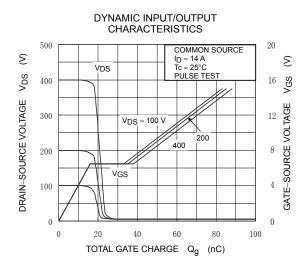


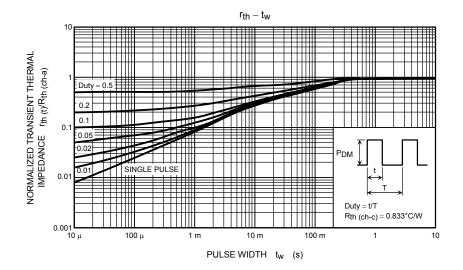


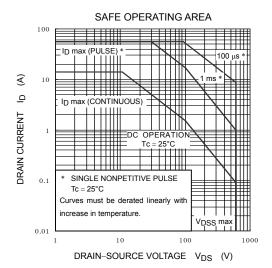


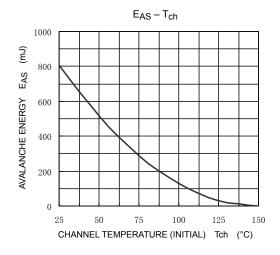


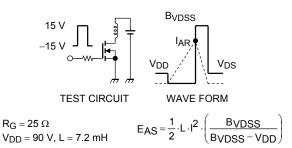












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