Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3471

Switching Regulator and DC-DC Converter Applications

• Low drain-source ON resistance: RDS (ON) = 10Ω (typ.)

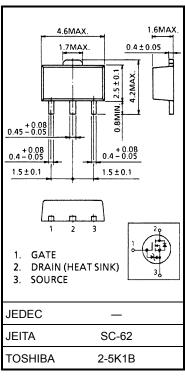
• High forward transfer admittance: $|Y_{fs}| = 0.4 \text{ S (typ.)}$

• Low leakage current: $IDSS = 100 \mu A (max) (VDS = 500 V)$

• Enhancement model: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Drain-gate voltage (RG	S = 20 kΩ)	V_{DGR}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	ID	0.5	А	
	Pulse (Note 1)	I _{DP}	1.5		
Drain power dissipation	1	P _D	0.5	W	
Drain power dissipation	n (Note 2)	P _D	1.5	W	
Single pulse avalanche energy (Note 3)		E _{AS}	14.3	mJ	
Avalanche current		I _{AR}	0.5	Α	
Repetitive avalanche e	nergy (Note 4)	E _{AR}	0.05	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55 to150	°C	



Weight: 0.05 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient	R _{th (ch-a)}	250	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C

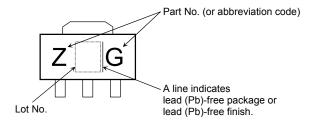
Note 2: Mounted on a ceramic substrate (25.4 mm \times 25.4 mm \times 0.8 mm)

Note 3: $V_{DD}=90~V,~T_{ch}=25^{\circ}C$ (initial), L = 100 mH, R_G = 25 $\Omega,~I_{AR}=0.5~A$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Marking



Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain-source breakdown voltage		V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30		_	V
Drain cut-OFF cเ	ırrent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	100	μА
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500			V
Gate threshold ve	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 0.25 A	_	10	18	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 0.25 A	0.2	0.4	_	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	75	_	pF
Reverse transfer capacitance		C _{rss}		_	7	_	
Output capacitance		Coss		_	24	_	
Switching time	Rise time	t _r	$V_{GS} = 0.25 \text{ A} \\ V_{GS} = 0.25 \text{ A} \\ V_{DD} = 0.25 \text{ A} \\ V_{DD} = 1 \text{ k} \Omega$ $V_{DD} = 250 \text{ V}$ $V_{DD} = 250 \text{ V}$	_	11	_	
	Turn-ON time	t _{on}		_	18	_	
	Fall time	t _f		_	54	_	ns
	Turn-OFF time	t _{off}		_	95	_	
Total gate charge (gate-source plus gate-drain)		Qg			3.8	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$		1.9	_	
Gate-drain ("miller") charge		Q _{gd}			1.9		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	0.5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	1.5	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 0.5 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 0.5 A, V _{GS} = 0 V,	_	190	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs		380	_	nC

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20070701-EN

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