

ZXTC2062E6 20V, SOT23-6, complementary medium power transistors

Summary

$$\begin{split} &\mathsf{BV}_{\mathsf{CEO}} > 20 \; (\text{-}20)\mathsf{V} \\ &\mathsf{BV}_{\mathsf{ECO}} > 5 \; (\text{-}4)\mathsf{V} \\ &\mathsf{I}_{\mathsf{C(cont)}} = 4 \; (\text{-}3.5)\mathsf{A} \\ &\mathsf{V}_{\mathsf{CE(sat)}} < 50 \; (\text{-}65)\mathsf{mV} @ \; \mathsf{1A} \\ &\mathsf{R}_{\mathsf{CE(sat)}} = 35 \; (\mathsf{54})\mathsf{m}\Omega \\ &\mathsf{P}_{\mathsf{D}} = 1.1\mathsf{W} \end{split}$$

Description

Advanced process capability has been used to achieve this high performance device. Combining NPN and PNP transistors in the SOT23-6 package provides a compact solution for the intended applications

Features

- NPN-PNP combination
- Very low saturation voltage
- High gain
- SOT23-6 package

Applications

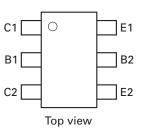
- MOSFET and IGBT gate driving
- Motor drive

Ordering information

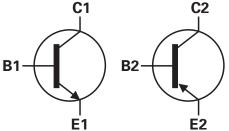
DEVICE	Reel size	Tape width	Quantity
	(inches)	(mm)	per reel
ZXTC2062E6TA	7	8	3000

Device marking

2062







Absolute maximum and thermal ratings

PARAMETER	Symbol	Limit	Unit
Collector-base voltage	V _{CBO}	100(-25)	V
Collector-emitter voltage	V _{CEO}	(-)20	V
Emitter-collector voltage (reverse blocking)	V _{ECO}	5(-4)	V
Emitter-base voltage	V _{EBO}	(-)7	V
Continuous collector current ^{(c)(f)}	Ι _C	4(-3.5)	А
Peak pulse current	I _{CM}	(-)10	А
Base current	I _B	(-)1	А
Power dissipation at $T_A = 25^{\circ}C^{(a)(f)}$	P _D	0.7	W
Linear derating factor		5.6	mW/°C
Power dissipation at $T_A = 25^{\circ}C^{(b)(f)}$	PD	0.9	W
Linear derating factor		7.2	mW/°C
Power dissipation at $T_A = 25^{\circ}C^{(b)(g)}$	PD	1.1	W
Linear derating factor		8.8	mW/°C
Power dissipation at $T_A = 25^{\circ}C^{(c)(f)}$	PD	1.1	W
Linear derating factor		8.8	mW/°C
Power dissipation at $T_A = 25^{\circ}C^{(d)(f)}$	PD	1.7	W
Linear derating factor		13.6	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to +150	°C
Thermal resistance junction to ambient ^{(a)(f)}	R _{θJA}	179	°C/W
Thermal resistance junction to ambient ^{(b)(f)}	R _{θJA}	139	°C/W
Thermal resistance junction to ambient ^{(b)(g)}	R _{θJA}	113	°C/W
Thermal resistance junction to ambient ^{(c)(f)}	R _{θJA}	113	°C/W
Thermal resistance junction to ambient ^{(d)(f)}	R _{θJA}	73	°C/W

NOTES:

(a) For a device surface mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

(b) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

(c) For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.

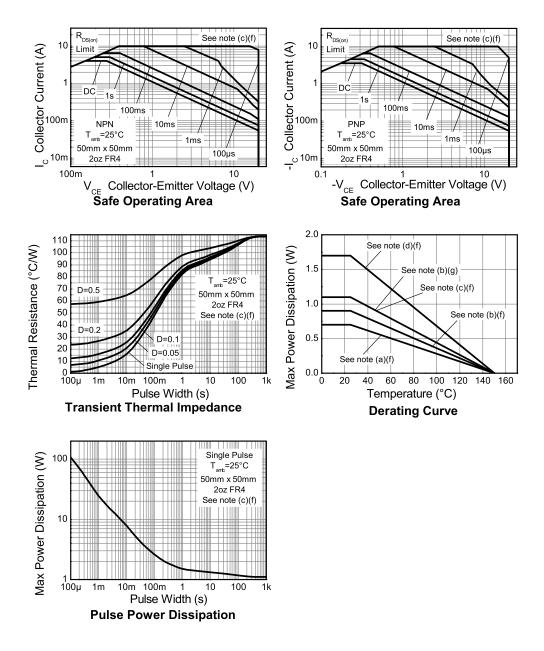
(d) As above measured at t<5 seconds.

(e) Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

(f) For device with one active die, both collectors attached to a common sink.

(g) For device with two active dice running at equal power, split sink 50% to each collector.

Thermal characteristics



ELECTRICAL CHARACTERISTICS (at Tamb = 25°C unless otherwise stated).

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base	BV _{CBO}	100(-25)	140(-55)		V	I _C = (-)100μA
breakdown voltage						
Collector-emitter	BV _{CEO}	(-)20	35(-45)		V	I _C = (-)10mA ^(*)
breakdown voltage						
(base open)						
Emitter-base	BV _{EBO}	(-)7	(-)8.3		V	I _E = (-)100μA
breakdown voltage						
Emitter-collector	BV _{ECO}	5(-4)	6(-8.5)		V	I _E = (-)100μA
breakdown voltage						
(base open)						
Collector-base cut-off	I _{CBO}		<1	(-)50	nA	V _{CB} =100(-25)V
current				(-)0.5	μA	V _{CB} =100(-25)V, T _{amb} = 100°C
Emitter-base cut-off	I _{EBO}		<1	(-)50	nA	V _{EB} = (-)5.6V
current						
Collector-emitter	V _{CE(sat)}		40(-55)	50(-65)	mV	I _C = (-)1A, I _B = (-)100mA ^(*)
saturation voltage			60(-100)	75(-135)	mV	I _C = (-)1A, I _B = (-)20mA ^(*)
			95(-185)	115(-280)	mV	$I_{C} = (-)2A, I_{B} = (-)40mA^{(*)}$
			(-190)	(-250)	mV	(I _C = -3.5A, I _B = -175mA) ^(*)
			140	190	mV	$I_{C} = 4A, I_{B} = 200 mA^{(*)}$
Base-emitter	V _{BE(sat)}		(-925)	(-1000)	mV	(I _C = -3.5A, I _B = -175mA ^(*))
saturation voltage			940	1050	mV	I _C = 4A, I _B = 200mA ^(*)
Base-emitter turn-on	V _{BE(on)}		(-835)	(-900)	mV	(I _C = -3.5A, V _{CE} = -2V ^(*))
voltage			810	900	mV	$I_{C} = 4A, V_{CE} = 2V^{(*)}$
Static forward current	h _{FE}	300(300)	450(450)	900(900)		I _C = (-)10mA, V _{CF} = (-)2V ^(*)
transfer ratio		280(170)	420(300)			$I_{\rm C} = (-)1A, V_{\rm CF} = (-)2V^{(*)}$
		(65)	(100)			$(I_{\rm C} = -3.5 {\rm A}, V_{\rm CF} = -2 {\rm V}^{(*)})$
		140	210			$I_{\rm C} = 4A, V_{\rm CE} = 2V^{(*)}$
			(15)			$(I_{\rm C} = -10A, V_{\rm CE} = -2V^{(*)})$
			15			I _C = 15A, V _{CE} = 2V ^(*)
Transition frequency	f _T		215		MHz	I _C = (-)50mA, V _{CE} = (-)10V
			(290)			f = 100MHz
Output capacitance	C _{OBO}		17(21)	25(30)	pF	V _{CB} = (-)10V, f = 1MHz ^(*)
Delay time	t _d		68(56)		ns	
Rise time	t _r		72(68)		ns	$V_{CC} = (-)10V. I_{C} = (-)1A,$
Storage time	t _s		361(158)		ns	I _{B1} = -I _{B2} = (-)10mA.
Fall time	t _f		64(59)		ns	

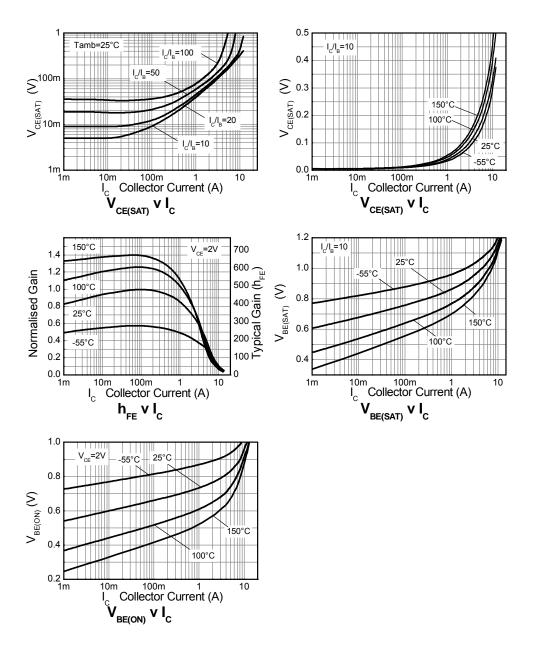
NOTES:

(*) Measured under pulsed conditions. Pulse width \leq 300 $\mu s;$ duty cycle \leq 2%.

() = PNP

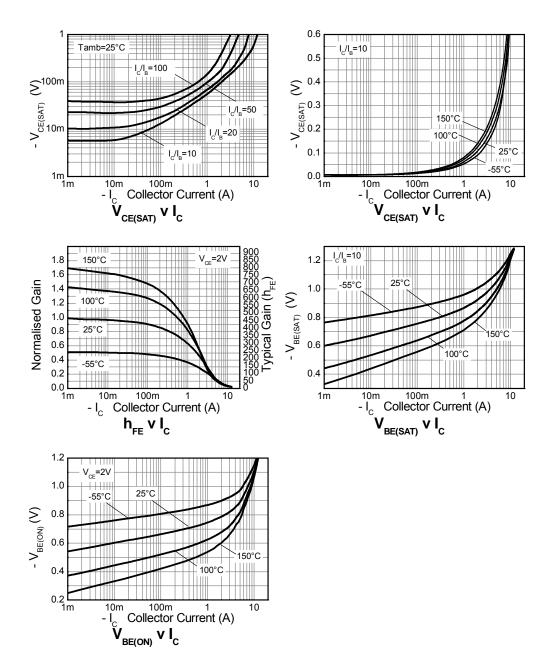
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NPN electrical characteristics



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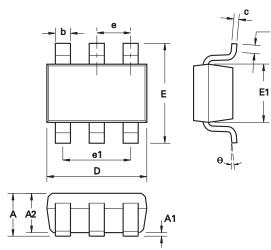
PNP electrical characteristics

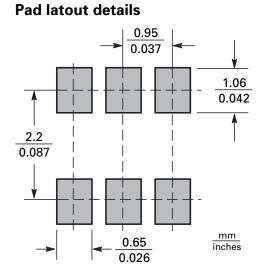


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Package outline SOT23-6

Package outline





DIM	Milli	meters	Inches		
	Min.	Max.	Min.	Max.	
А	0.90	1.45	0.354	0.0570	
A1	0.00	0.15	0.00	0.0059	
A2	0.90	1.30	0.0354	0.0511	
b	0.35	0.50	0.0078	0.0196	
С	0.09	0.26	0.0035	0.0102	
D	2.70	3.10	0.1062	0.1220	
E	2.20	3.20	0.0866	0.1181	
E1	1.30	1.80	0.0511	0.0708	
L	0.10	0.60	0.0039	0.0236	
е	0.9	5 REF	0.0374 REF		
e1	1.90 REF		0.0748 REF		
L	0°	30°	0°	30°	

L

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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