Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

# 2SK3301

#### Switching Regulatorand DC-DC Converter Applications

• Low drain-source ON resistance:  $R_{DS}$  (ON) = 15  $\Omega$  (typ.)

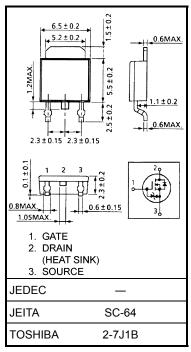
• High forward transfer admittance: |Y<sub>fS</sub>| = 0.65 S (typ.)

• Low leakage current: I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 720 V)

• Enhancement mode:  $V_{th}$  = 2.4 to 3.4 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	900	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	900	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	ID	1	А	
	Pulse (Note 1)	I <sub>DP</sub>	2		
Drain power dissipation (Tc = 25°C)		P <sub>D</sub>	20	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	140	mJ	
Avalanche current		I <sub>AR</sub>	1	А	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	2.0	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	6.25	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 257 mH,  $R_G = 25 \Omega$ ,

Note 3: Repetitive rating: pulse width limited by max junction temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

2010-02-05

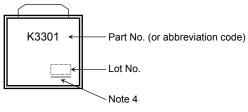
#### **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Gate-source brea	akdown voltage	V <sub>(BR)</sub> GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	900	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	2.4	_	3.4	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	$V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$	_	15	20	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 0.5 \text{ A}$	0.3	0.65	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	165	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	6	_	pF
Output capacitance		C <sub>oss</sub>		_	21	_	pF
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 0.5 \text{ A} \\ V_{GS} = 0.5 \text{ A} \\ V_{DD} = 0.5 \text{ A} \\ V_{OUT} = 0.5 \text{ A} \\ V$	_	15	_	
	Turn-on time	t <sub>on</sub>		_	60	_	
	Fall time	t <sub>f</sub>			40	_	ns
	Turn-off time	t <sub>off</sub>			110	_	
Total gate charge (gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≃ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1 A		6	_	nC
Gate-source charge		$Q_{gs}$	V     - +00 V, V		3	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	3	_	nC

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	1	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	2	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 1 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 1 A, V <sub>GS</sub> = 0 V	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	_	1.95	_	μС

### Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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