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Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

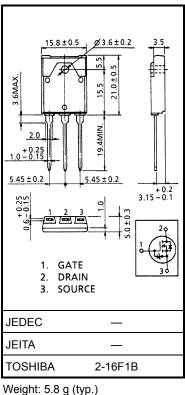
2SK3017

DC-DC Converter, Relay Drive and Motor Drive Applications

- Low drain-source ON resistance $: RDS(ON) = 1.05 \Omega(typ.)$
- High forward transfer admittance : $|Y_{fs}| = 7.0 S (typ.)$ •
- $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 720 \ V)$ • Low leakage current
- : $V_{th} = 2.0 \sim 4.0 V (V_{DS} = 10 V, I_D = 1 mA)$ Enhancement mode

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	900	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	900	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	8.5	А	
	Pulse (Note 1)	I _{DP}	25.5	А	
Drain power dissipation (Tc = 25°C)		PD	90	W	
Single pulse avalanche energy (Note 2)		EAS	966	mJ	
Avalanche current		I _{AR}	8.5	А	
Repetitive avalanche energy (Note 3)		E _{AR}	9	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

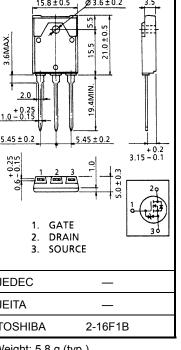
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.39	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	41.6	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 24.5 mH, R_G = 25 Ω , I_{AR} = 8.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



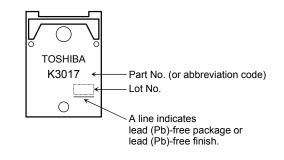
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	age current I_{GSS} $V_{GS} = \pm 30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$		_	—	±10	μA	
Gate-source bro	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V		_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900	—	—	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 4 A		1.05	1.25	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 15 V, I _D = 4 A	3.5	7.0	—	S
Input capacitand	e	C _{iss}			2150	—	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	35	_	рF
Output capacitance		C _{oss}			220	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10 \text{ V}}{_{0 \text{ V}}} \stackrel{I_{D} = 4 \text{ A}}{_{0 \text{ V}}} \stackrel{V_{OUT}}{_{0 \text{ V}}} \stackrel{R_{L} =}{_{100 \Omega}} \stackrel{R_{L} =}{_{0 \text{ V}}} R_{L$		25		- ns
	Turn-on time	t _{on}		_	60	_	
	Fall time	t _f		_	25	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w = 10 µs	_	120	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	70	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 8 A		37	_	nC
Gate-drain ("miller") charge		Q _{gd}			33	—	

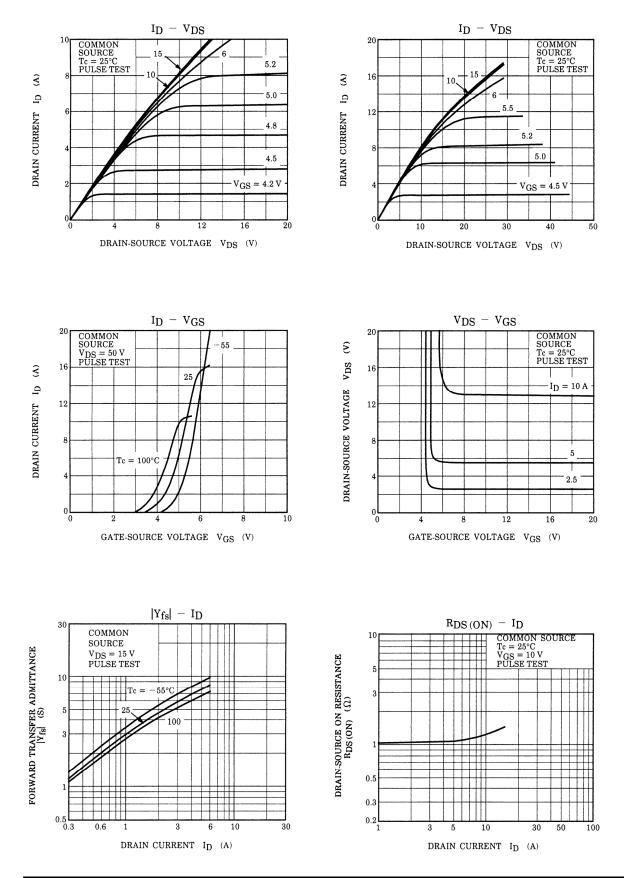
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	8.5	А
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	25.5	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 8.5 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	t _{rr}	I _{DR} = 8.5 A, V _{GS} = 0 V		1300		ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 Å / µs	_	14.5	_	μC

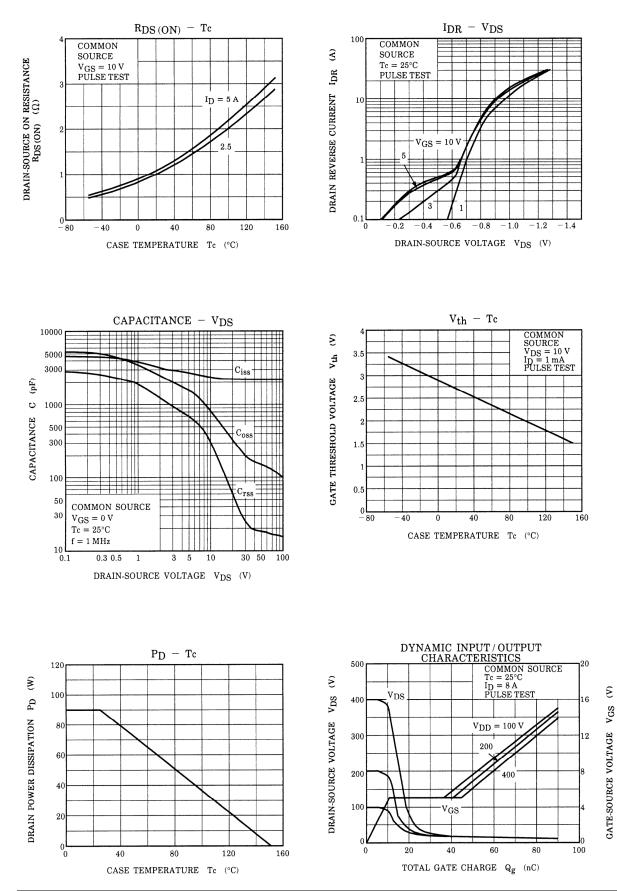
Marking

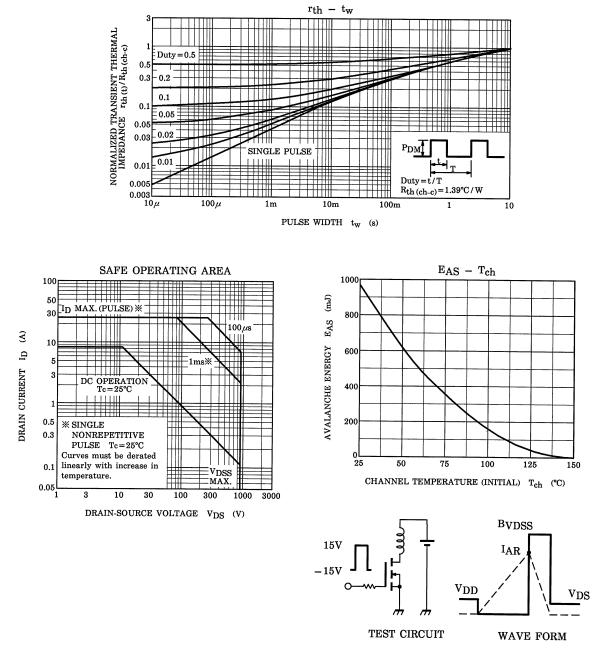


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$$\begin{array}{l} \mathrm{R_{G}=25\ \Omega} \\ \mathrm{V_{DD}=90\ V,\ L=24.5\ mH} \end{array} \qquad \mathrm{E_{AS}=\frac{1}{2}\cdot L\cdot I^{2}\cdot \left(\frac{\mathrm{B_{VDSS}}}{\mathrm{B_{VDSS}-V_{DD}}}\right)} \end{array}$$

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