

< IGBT MODULES >

CM200DY-34A

HIGH POWER SWITCHING USE
INSULATED TYPE



Dual (Half-Bridge)

Collector current I_C **200 A**
 Collector-emitter voltage V_{CES} **1700 V**
 Maximum junction temperature T_{jmax} **150 °C**

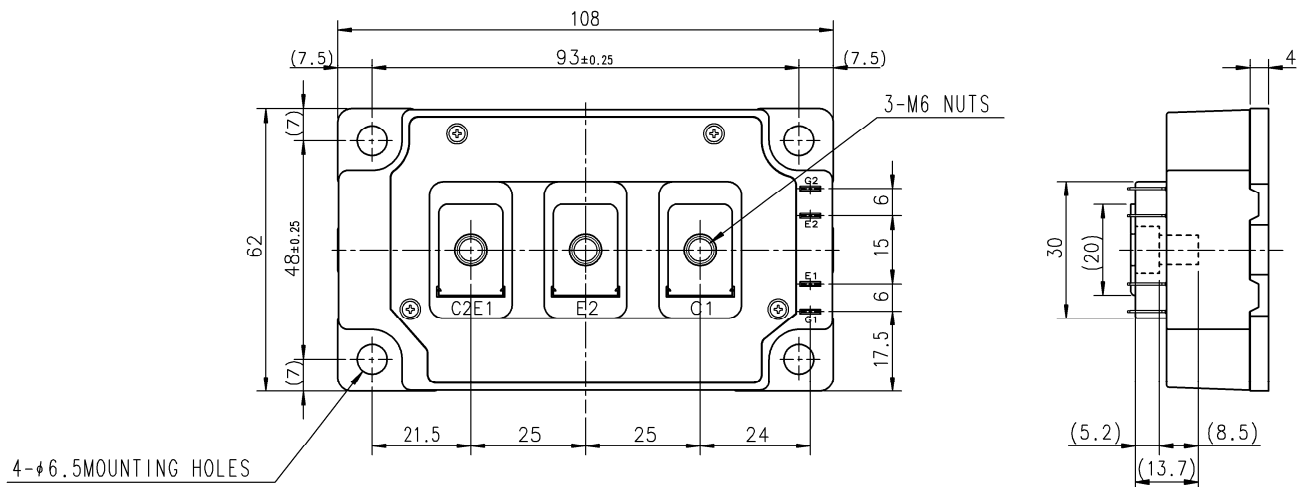
- Flat base Type
- Copper base plate
- RoHS Directive compliant
- UL Recognized under UL1557, File E323585

APPLICATION

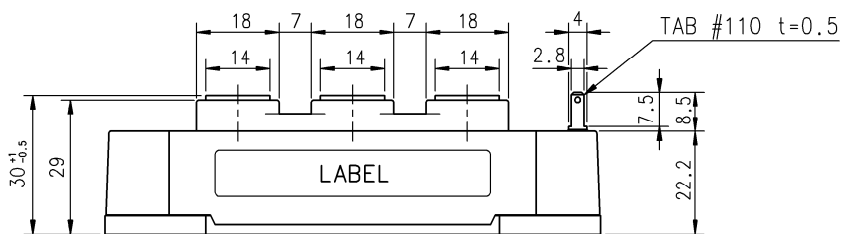
AC Motor Control, Motion/Servo Control, Power supply, etc.

OUTLINE DRAWING & INTERNAL CONNECTION

Dimension in mm



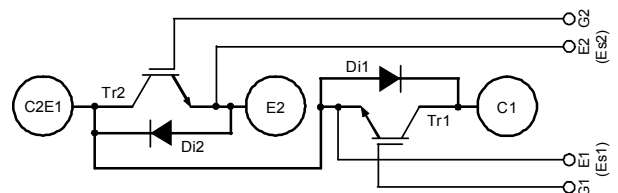
4-φ6.5 MOUNTING HOLES



Tolerance otherwise specified

Division of Dimension	Tolerance
0.5 to 3	±0.2
over 3 to 6	±0.3
over 6 to 30	±0.5
over 30 to 120	±0.8
over 120 to 400	±1.2

INTERNAL CONNECTION



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ABSOLUTE MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V _{CEs}	Collector-emitter voltage	G-E short-circuited	1700	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	±20	V
I _c	Collector current	DC, T _c =109 °C (Note.2, 4)	200	A
I _{CRM}		Pulse, Repetitive (Note.3)	400	
P _{tot}	Total power dissipation	T _c =25 °C (Note.2, 4)	1980	W
I _E (Note.1)	Emitter current	T _c =25 °C (Note.2, 4)	200	A
I _{ERM} (Note.1)		Pulse, Repetitive (Note.3)	400	
T _j	Junction temperature	-	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	3500	V

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Limits			Unit	
			Min.	Typ.	Max.		
I _{CEs}	Collector-emitter cut-off current	V _{CE} =V _{CEs} , G-E short-circuited	-	-	1.0	mA	
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited	-	-	2.0	µA	
V _{GE(th)}	Gate-emitter threshold voltage	I _c =20 mA, V _{CE} =10 V	5.5	7.0	8.5	V	
V _{CEsat}	Collector-emitter saturation voltage	I _c =200 A (Note.5), V _{GE} =15 V	T _j =25 °C	-	2.2	2.8	V
			T _j =125 °C	-	2.45	-	
C _{ies}	Input capacitance	V _{CE} =10 V, G-E short-circuited	-	-	49.4	nF	
C _{oes}	Output capacitance		-	-	5.6		
C _{res}	Reverse transfer capacitance		-	-	1.06		
Q _G	Gate charge	V _{CC} =1000 V, I _c =200 A, V _{GE} =15 V	-	1330	-	nC	
t _{d(on)}	Turn-on delay time	V _{CC} =1000 V, I _c =200 A, V _{GE} =±15 V, R _G =2.4 Ω, Inductive load	-	-	550	ns	
t _r	Rise time		-	-	190		
t _{d(off)}	Turn-off delay time		-	-	750		
t _f	Fall time		-	-	350		
V _{EC} (Note.1)	Emitter-collector voltage	I _E =200 A (Note.5), G-E short-circuited	-	2.3	3.0	V	
t _{rr} (Note.1)	Reverse recovery time	V _{CC} =1000 V, I _E =200 A, V _{GE} =±15 V, R _G =2.4 Ω, Inductive load	-	-	450	ns	
Q _{rr} (Note.1)	Reverse recovery charge	R _G =2.4 Ω, Inductive load	-	20	-	µC	
E _{on}	Turn-on switching energy per pulse	V _{CC} =1000 V, I _c =200 A,	-	94.5	-	mJ	
E _{off}	Turn-off switching energy per pulse	V _{GE} =±15 V, R _G =2.4 Ω, T _j =125 °C, Inductive load	-	58.7	-		
E _{rr} (Note.1)	Reverse recovery energy per pulse	Inductive load	-	50.7	-	mJ	
r _g	Internal gate resistance	Per switch, T _c =25 °C	-	3.0	-	Ω	

THERMAL RESISTANCE CHARACTERISTICS

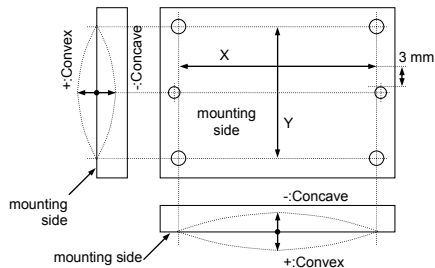
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R _{th(j-c)Q}	Thermal resistance (Note.2)	Junction to case, per IGBT	-	-	0.063	K/W
R _{th(j-c)D}		Junction to case, per FWDi	-	-	0.11	
R _{th(c-s)}	Contact thermal resistance (Note.2)	Case to heat sink, per 1/2 module, Thermal grease applied (Note.6)	-	0.02	-	K/W

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M _t	Mounting torque	Main terminals M 6 screw	3.5	4.0	4.5	N·m
M _s		Mounting to heat sink M 6 screw	3.5	4.0	4.5	
m	Weight	-	-	400	-	g
e _c	Flatness of base plate	On the centerline X, Y (Note.7)	-100	-	+100	µm

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- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).
- Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - The heat sink thermal resistance should measure just under the chips.
 - Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.
 - Junction temperature (T_j) should not increase beyond T_{jmax} rating.
 - Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
 - Typical value is measured by using thermally conductive grease of $\lambda=0.9$ W/(m·K).
 - Base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

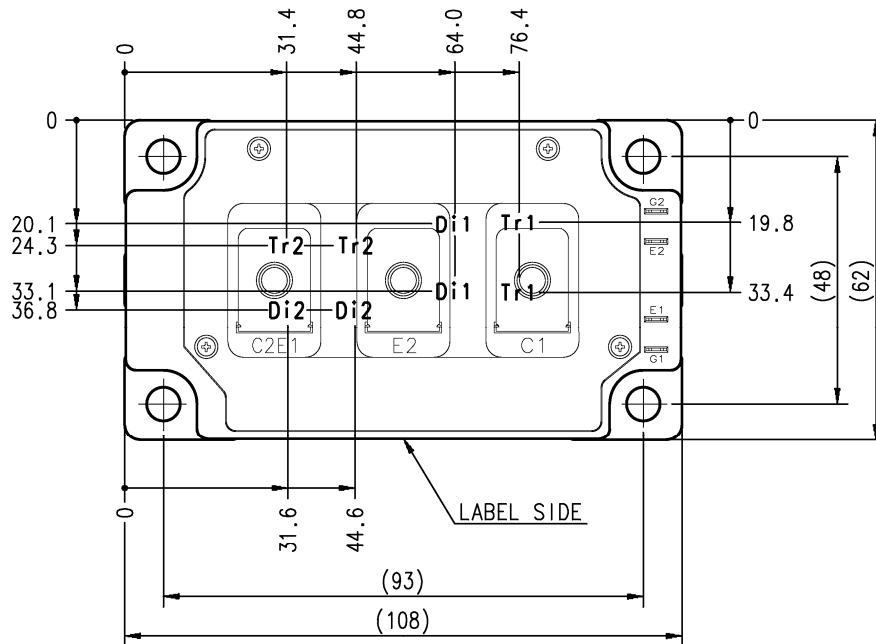


RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across C1-E2	-	1000	1100	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	2.4	-	24	Ω

CHIP LOCATION (Top view)

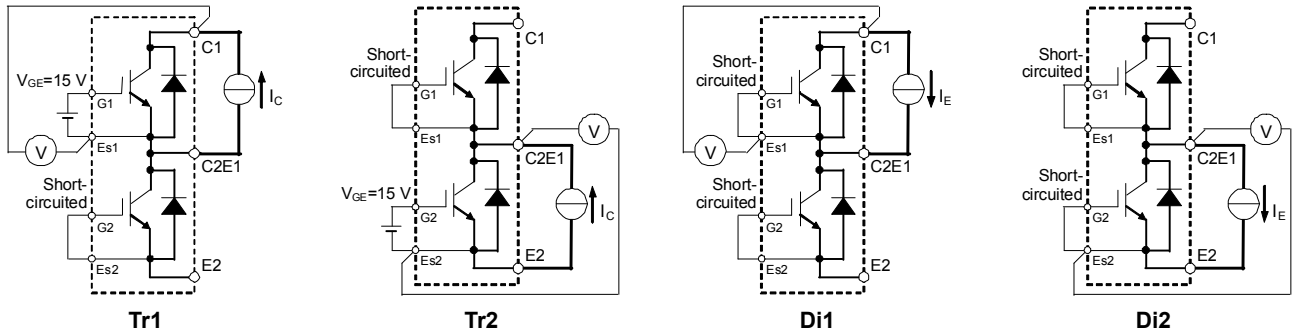
Dimension in mm, tolerance: ± 1 mm



Tr1/Tr2: IGBT, Di1/Di2: FWDi

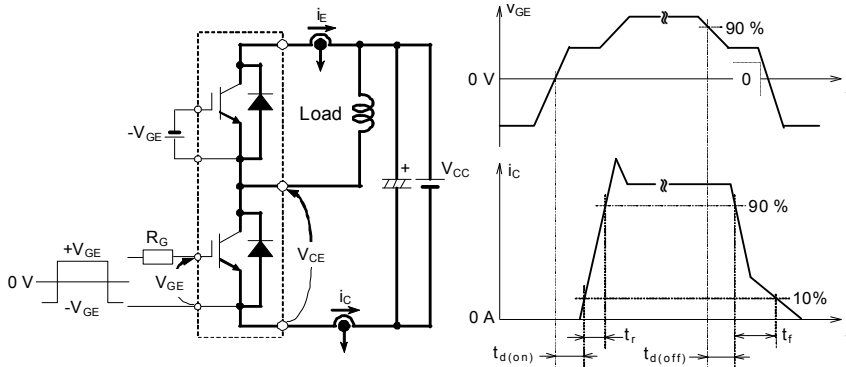
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TEST CIRCUIT AND WAVEFORMS

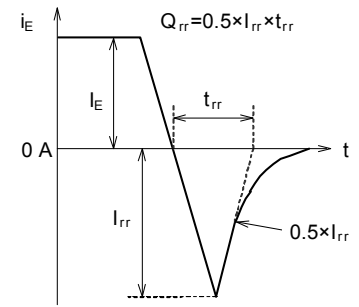


V_{CEsat} test circuit

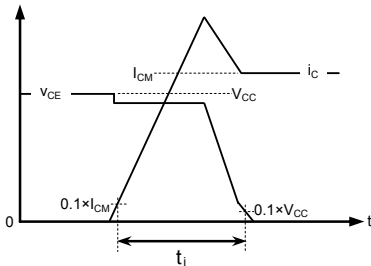
V_{EC} test circuit



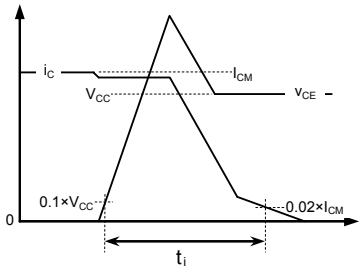
Switching characteristics test circuit and waveforms



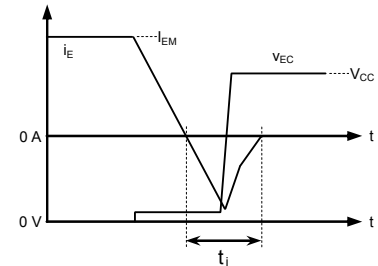
t_{rr} , Q_{rr} test waveform



IGBT Turn-on switching energy



IGBT Turn-off switching energy



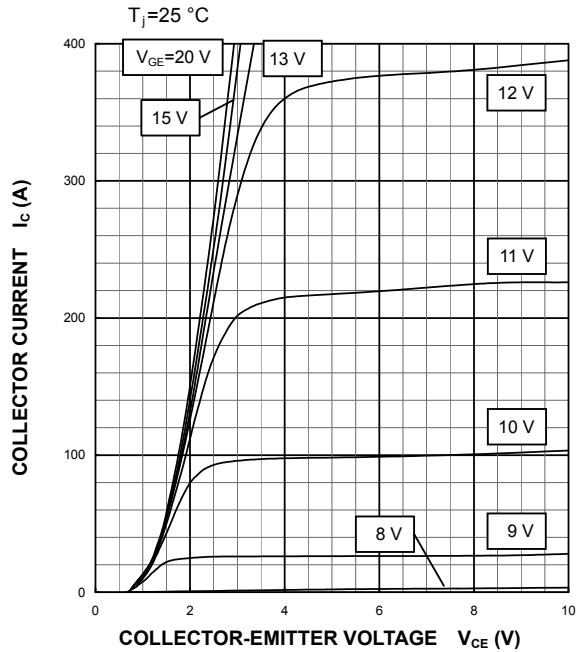
FWDi Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

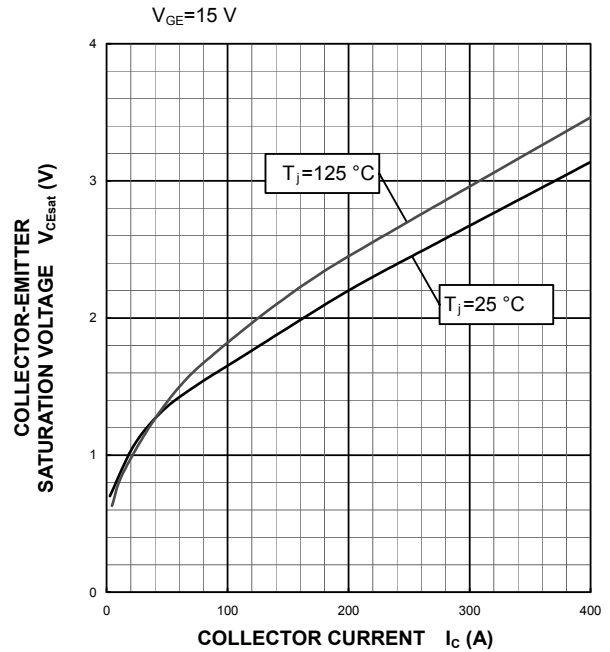
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PERFORMANCE CURVES

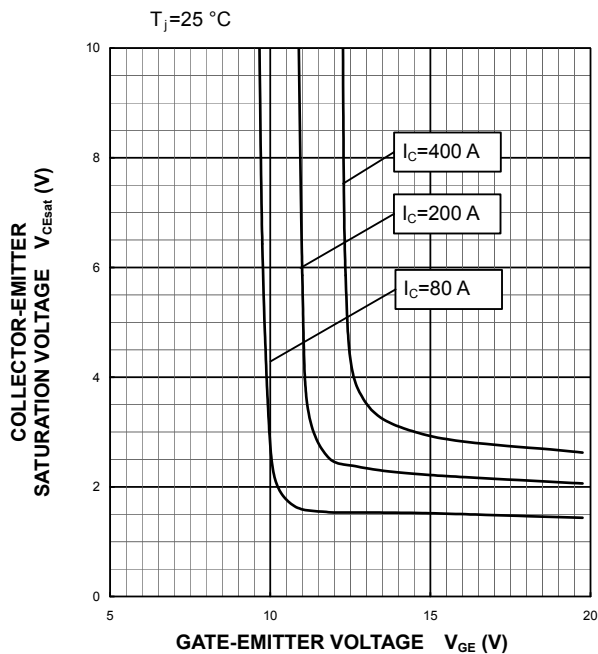
OUTPUT CHARACTERISTICS
 (TYPICAL)



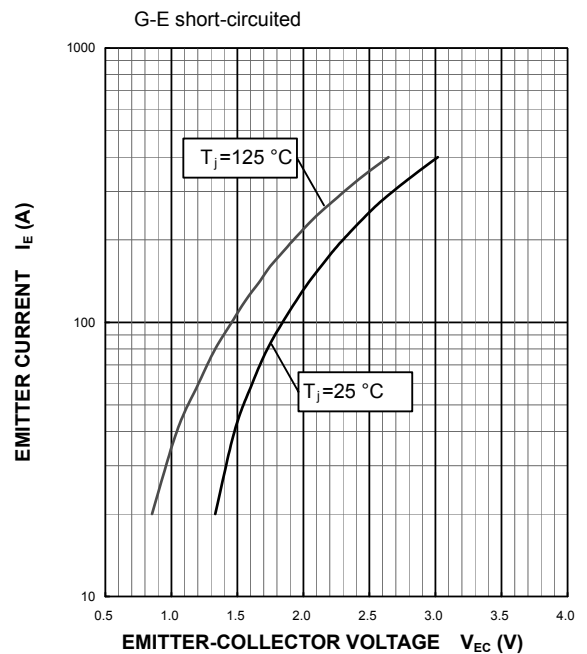
COLLECTOR-EMITTER SATURATION
 VOLTAGE CHARACTERISTICS
 (TYPICAL)



COLLECTOR-EMITTER SATURATION
 VOLTAGE CHARACTERISTICS
 (TYPICAL)



FREE WHEELING DIODE
 FORWARD CHARACTERISTICS
 (TYPICAL)

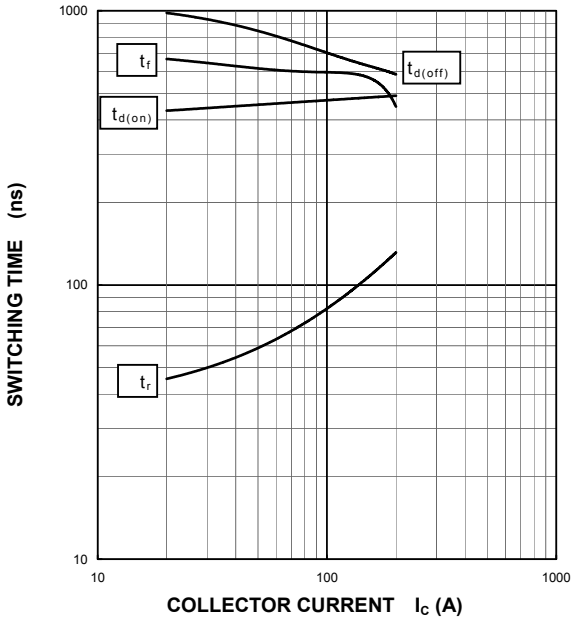


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PERFORMANCE CURVES

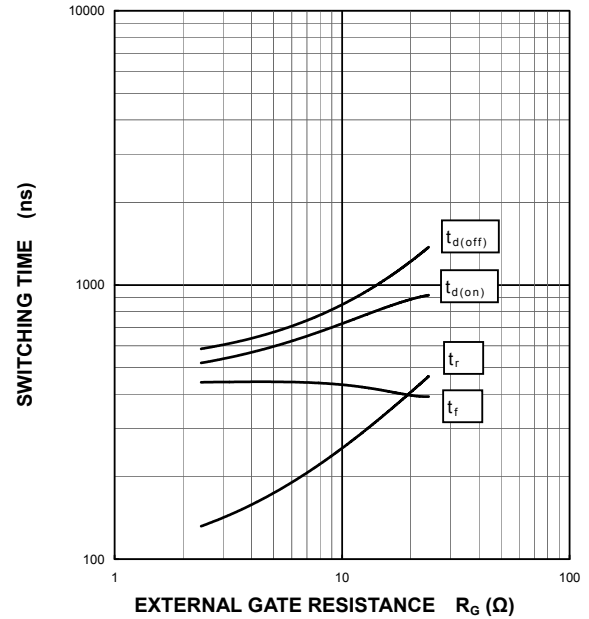
HALF-BRIDGE
 SWITCHING CHARACTERISTICS
 (TYPICAL)

$V_{CC}=1000\text{ V}$, $V_{GE}=\pm 15\text{ V}$, $R_G=2.4\ \Omega$,
 $T_J=125\text{ }^\circ\text{C}$, INDUCTIVE LOAD



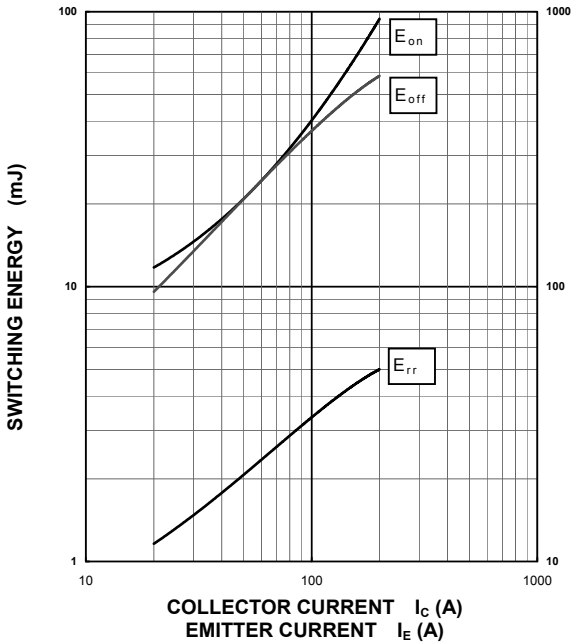
HALF-BRIDGE
 SWITCHING CHARACTERISTICS
 (TYPICAL)

$V_{CC}=1000\text{ V}$, $I_C=200\text{ A}$, $V_{GE}=\pm 15\text{ V}$,
 $T_J=125\text{ }^\circ\text{C}$, INDUCTIVE LOAD



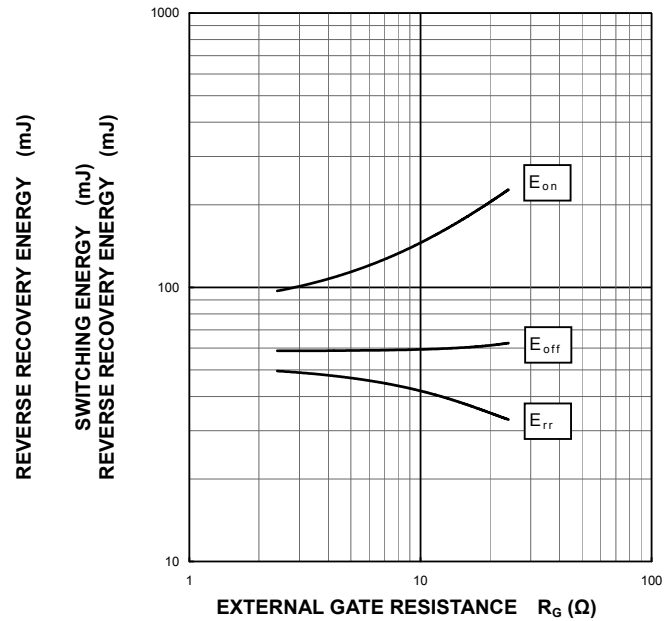
HALF-BRIDGE
 SWITCHING CHARACTERISTICS
 (TYPICAL)

$V_{CC}=1000\text{ V}$, $V_{GE}=\pm 15\text{ V}$, $R_G=2.4\ \Omega$, $T_J=125\text{ }^\circ\text{C}$
 INDUCTIVE LOAD, PER PULSE



HALF-BRIDGE
 SWITCHING CHARACTERISTICS
 (TYPICAL)

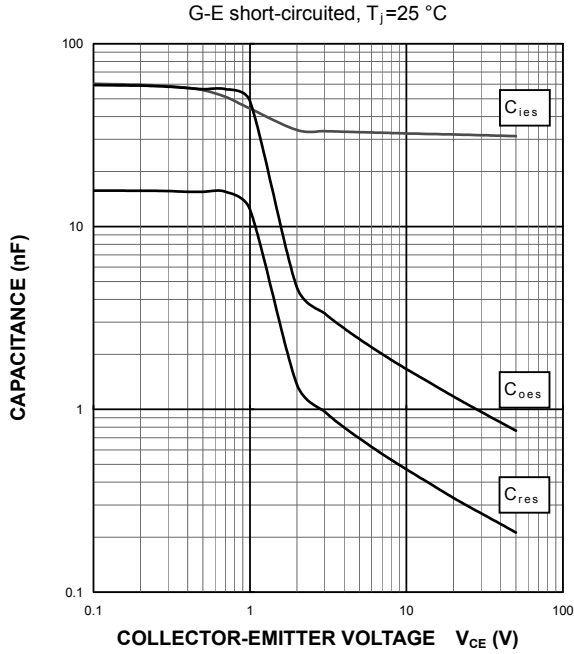
$V_{CC}=1000\text{ V}$, $I_C/I_E=200\text{ A}$, $V_{GE}=\pm 15\text{ V}$, $T_J=125\text{ }^\circ\text{C}$
 INDUCTIVE LOAD, PER PULSE



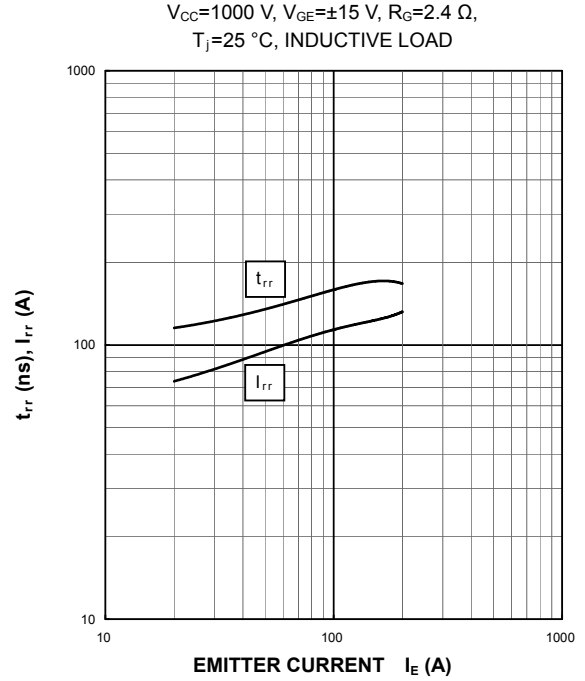
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PERFORMANCE CURVES

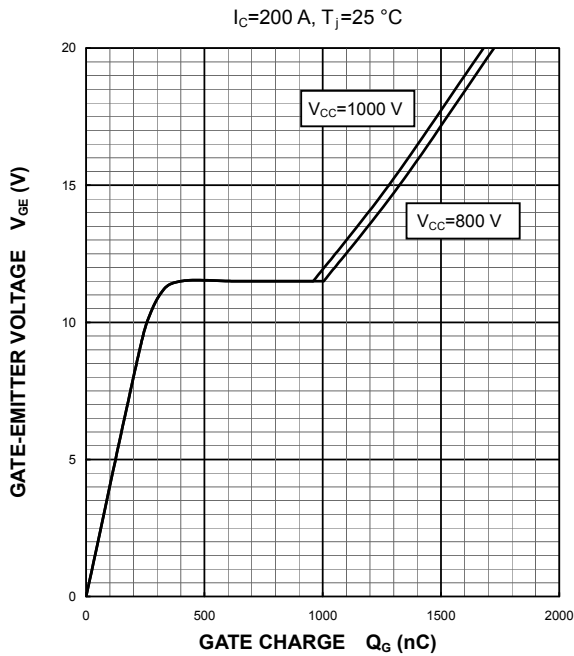
CAPACITANCE CHARACTERISTICS (TYPICAL)



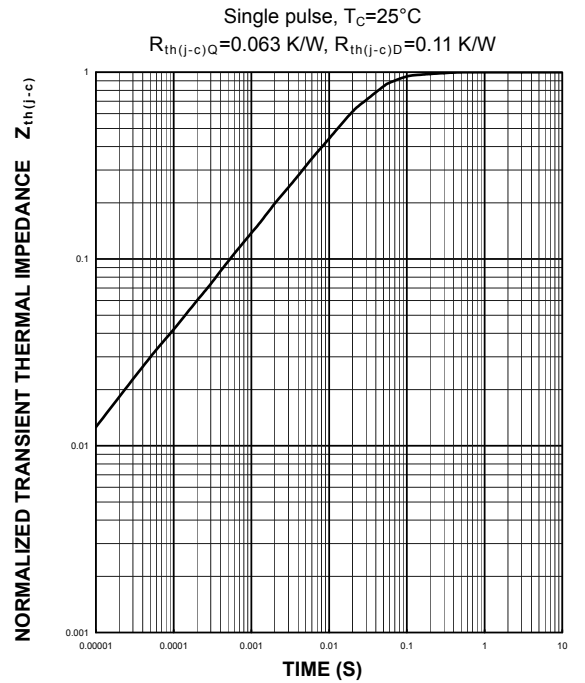
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



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