9301/DM9301 1-of-10 Decoders

General Description

These BCD-to-decimal decoders consist of eight inverters and ten 4-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of valid input logic ensures that all outputs remain "OFF" for all invalid input conditions.

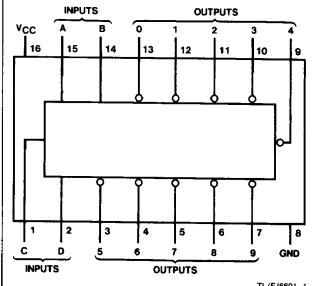
These circuits provide familiar TTL inputs and outputs which are compatible for use with other TTL and DTL circuits. DC noise margins are typically 1V and power dissipation is typically 125 mW. The diode-clamped, buffered inputs represent only one normalized Series 54/74 load.

Features

- Direct replacement for Signetics 8252
- Diode-clamped inputs
- All outputs are high for invalid BCD input conditions
- Typical power dissipation 125 mW
- Typical propagation delay 20 ns

Connection Diagram

Dual-In-Line Package



Order Number 9301DMQB, 9301FMQB or DM9301N See NS Package Number J16A, N16E or W16A

Function Table

No.	BCD Inputs			Decimal Outputs										
	D	С	В	A	0	1	2	3	4	5	6	7	8	9
0	L	L	L	L	L	Н	Н	Н	Н	н	н	Н	Н	н
1	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	н
2	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	н
3	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	H	Н	н
4	L	Н	L	Ļ	Н	Н	Н	Н	L	Н	Н	H	Н	Н
5	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	н
6	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	н
7	L	Н	Н	Н	H	Н	Н	Н	Н	Н	Н	L	Н	н
8	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	н
9	Η	L	L	Н	Ι	Н	Н	Н	Н	Н	Н	Н	Н	ᅵᅵ
ı	Н	L	Н	٦	I	Н	Н	Н	Н	Н	Н	н	Н	Н
N	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	н
V	Н	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	н
Α,	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	н
L	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	н
ı	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	н
D														

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 5.5V

Operating Free Air Temperature Range

Military -55°C to +125°C Commercial 0°C to 70°C

Storage Temperature Range -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter		Military			Units		
	Farameter	Min	Nom	Max	Min	Nom	Max	Oilles
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
Юн	High Level Output Current			-0.8			-0.8	mA
loL	Low Level Output Current			16			16	mA
TA	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V _{CC} = Min, I ₁ = -12 mA				-1.5	٧
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{IL} = Max, V_{IH} = Min$		2.4			٧
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$				0.4	٧
Ι _Ι	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 5.5V				1	mA
l _{iH}	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
1 _{IL}	Low Level Input Current	V _{CC} = Max, V _I	= 0.4V			-1.6	mA
los	Short Circuit Output Current	V _{CC} = Max	MIL	-20		-70	mA.
		(Note 2)	СОМ	-20		-55] ""
lcc	Supply Current	V _{CC} = Max	MIL			44	mA
		(Note 3)	СОМ		25	41] '''^

$\textbf{Switching Characteristics} \ \ \text{at V}_{CC} = 5 \text{V and T}_{A} = 25 ^{\circ} \text{C (See Section 1 for Test Waveforms and Output Load)}$

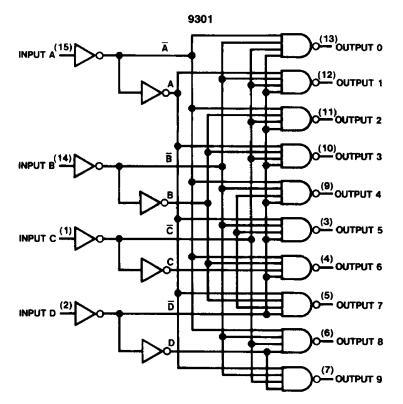
Symbol	Parameter	Conditions	Mil	itary	Comr	Units		
Cyllibol	T di dilictoi	Conditions	Min	Max	Min	Max		
t _{PLH}	Propagation Delay Time Low to High Level Output	$C_L = 15 pF$ $R_L = 400\Omega$		35		30	ns	
t _{PHL}	Propagation Delay Time High to Low Level Output			30		30	ns	

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25$ °C.

Note 2: Not more than one output should be shorted at a time.

Note 3: I_{CC} is measured with the outputs open and all inputs grounded.

Logic Diagram



TL/F/6601-2