

BCR2AS-14A

Triac Low Power Use R07DS0257EJ0100 Rev.1.00 Feb 28, 2011

Features

• $I_{T (RMS)}$: 2 A $V_{DRM} : 700 \text{ V}$

 $\bullet \quad I_{FGTI},\,I_{RGTI},\,I_{RGT}\ : 10\;mA$

- Non-Insulated Type
- Planar Passivation Type

Outline

RENESAS Package code: PRSS0004ZG-A

(Package name : MP-3A)





- T₁ Terminal
 T₂ Terminal
- 3. Gate Terminal
- 4. T₂ Terminal

Applications

Small motor control, heater control, and other general purpose AC power control applications

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
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Repetitive peak off-state voltage ^{Note1}	V_{DRM}	700	V	
Non-repetitive peak off-state voltage ^{Note1}	V_{DSM}	840	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	2	А	Commercial frequency, sine full wave 360°conduction
Surge on-state current	I _{TSM}	9	А	50Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.41	A ² s	Value corresponding to 1 cycle of half wave 50Hz, surge on-state current
Peak gate power dissipation	P_{GM}	1	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate voltage	V_{GM}	6	V	
Peak gate current	I_{GM}	1	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.26	g	Typical value

Notes: 1. Gate open.

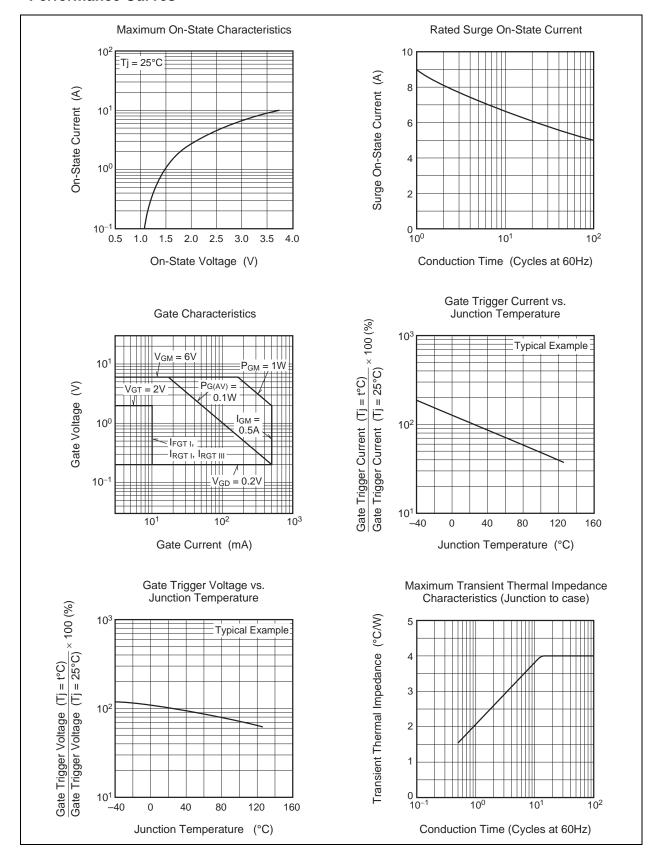
Electrical Characteristics

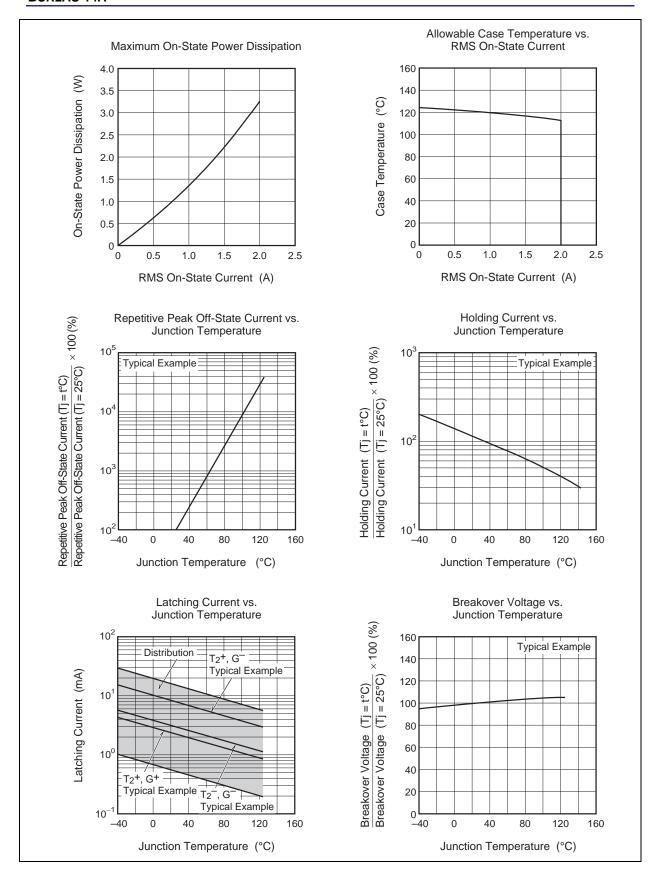
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state of	current	I_{DRM}	_	_	1.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V _{TM}	_	_	2.1	V	Tc = 25°C, I _{TM} = 3A, instantaneous measurement
Gate trigger voltage ^{Note2}	I	V_{FGTI}		_	2.0	V	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	$V_{RGT_{\mathrm{I}}}$	_	_	2.0	V	$R_G = 330 \Omega$
	III	$V_{RGT_{III}}$	_	_	2.0	V	
Gate trigger curent ^{Note2}	I	I_{FGTI}	_	_	10	mA	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	$I_{RGT_{\mathrm{I}}}$		_	10	mA	$R_G = 330 \Omega$
	III	I_{RGTIII}	_	_	10	mA	
Gate non-trigger voltage		V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$
Thermal resistance		R _{th (j-c)}	_	_	4.0	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off-s commutation voltage ^{Note4}	tate	(dv/dt)c	0.5	_	_	V/µs	Tj = 125°C

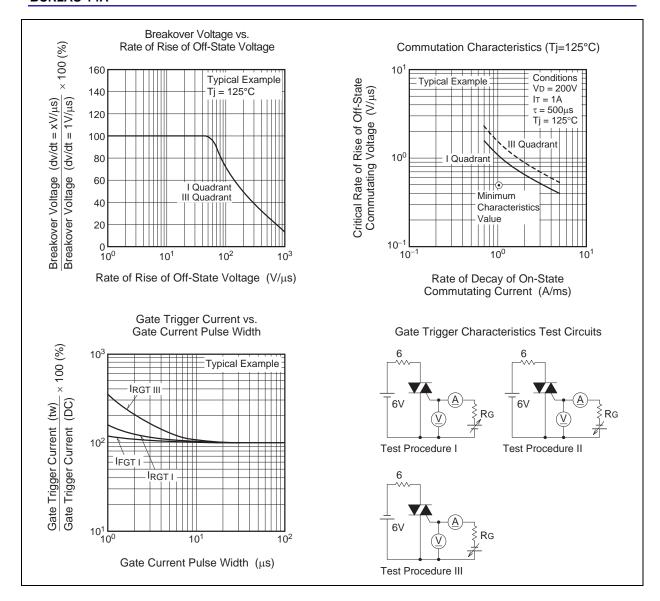
- Notes: 2. Measurement using the gate trigger characteristics measurement circuit.
 - 3. Case temperature is measured on the T_2 tab.
 - 4. Test conditions of the critical-rate of rise of off-state commutation voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C 2. Rate of decay of on-state commutating current (di/dt)c = -1.0 A/ms 3. Peak off-state voltage V _D = 400 V	Supply Voltage Main Current Main Voltage (di/dt)c Time Main Voltage

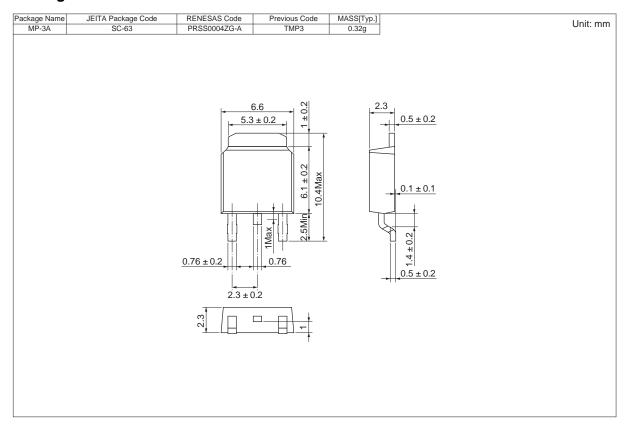
Performance Curves







Package Dimensions



Ordering Information

Orderable Part Number	Packing	Quantity	Remark
BCR2AS-14A#B00	Tube	75 pcs.	_
BCR2AS-14A-T13#B00	Embossed Tape	3000 pcs.	Taping direction "T1"

Note: Please confirm the specification about the shipping in detail.

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