## FEATURES

- 3.3V, 5.0V, 12V, 15V, and Adjustable Output Versions
- Adjustable Version Output Voltage Range, 1.23 to 37 V +/- 4\% AG10 Maximum Over Line and Load Conditions
- Guaranteed 1A Output Current
- Wide Input Voltage Range
- Requires Only 4 External Components
- 52kHz Fixed Frequency Internal Oscillator
- TTL Shutdown Capability, Low Power Standby Mode
- High Efficiency
- Uses Readily Available Standard Inductors
- Thermal Shutdown and Current Limit Protection
- Moisture Sensitivity Level 3


## Applications

- Simple High-Efficiency Step-Down(Buck) Regulator
- Efficient Pre-Regulator for Linear Regulators
- On-Card Switching Regulators
- Positive to Negative Converter(Buck-Boost)
- Negative Step-Up Converters
- Power Supply for Battery Chargers


## DESCRIPTION

The LM2575 series of regulators are monolithic integrated circuits ideally suited for easy and convenient design of a step-down switching regualtor(buck converter).
All circuits of this series are capable of driving a 1A load with excellent line and load regulation. These devices are available in fixed output voltages of $3.3 \mathrm{~V}, 5.0 \mathrm{~V}, 12 \mathrm{~V}, 15 \mathrm{~V}$, and an adjustable output version.
These regulatiors were designed to minimize the number of externalcomponents to simplify the power supply design. Standard series of inductors optimized for use with the LM2575 are offered by several different inductor manufacturers.
Since the LM2575 converter is a switch-mode power supply, its efficiency is significantly higher in comparison with popular three-terminal limear reguators, especially with higher input voltages. In many cases, the power dissipated is so low that no heatsink is required or its size could be reduced dramatically. A standard series of inductors optimized for use with the LM2575 are available from several different manufacturers. This feature greatly simplifies the design of switch-mode power supplies. The LM2575 features include a guaranteed $+/-4 \%$ tolerance on output voltage within specified input voltages and output load conditions, and $+/-10 \%$ on the oscillator frequency ( $+/-2 \%$ over $0^{\circ} \mathrm{C}$ to $125{ }^{\circ} \mathrm{C}$ ).
External shutdown is included, featuring $80 \mu \mathrm{~A}$ (typical) standby current. The output switch includes cycle-bycycle current limiting, as well as thermal shutdown for full protection under fault conditions.

## Typical Application (Fixed Output Voltage Versions)



Figure 1. Block Diagram and Typical Application

## ABSOLUTE MAXIMUM RATINGS

(Absolute Maximum Ratings indicate limits beyond which damage to the device may occur)

| Rating | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Maximum Supply Voltage | Vin | 45 | V |
| On/Off Pin Input Voltage | - | $-0.3 \mathrm{~V} \leq \mathrm{V} \leq+\mathrm{Vin}$ | V |
| Output Voltage to Ground (Steady-State) | - | -1.0 | V |
| Power Dissipation TO-220 5Lead <br> Thermal Resistance, Juntion to Ambient Thermal Resistance, Juntion to Case TO-263 5Lead <br> Thermal Resistance, Juntion to Ambient Thermal Resistance, Juntion to Case | Pd <br> Pөja <br> Pөлс <br> Pd <br> Peja <br> Pөлс | Internally Limited <br> 65 <br> 5.0 <br> Internally Limited <br> 70 <br> 5.0 | $\begin{gathered} W \\ { }^{\circ} \mathrm{C} / \mathrm{W} \\ { }^{\circ} \mathrm{C} / \mathrm{W} \\ \mathrm{~W} \\ { }^{\circ} \mathrm{C} / \mathrm{W} \\ { }^{\circ} \mathrm{C} / \mathrm{W} \\ \hline \end{gathered}$ |
| Storage Temperature Range | Tstg | -60 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Minimum ESD Rating(Human Body Model : $\mathrm{C}=100 \mathrm{pF}, \mathrm{R}=1.5 \mathrm{k} \Omega)$ | _ | 2.0 | kV |
| Lead Temperature (Soldering,10seconds) | - | 260 | C |
| Maximum Junction Temperature | TJ | 150 | ${ }^{\circ} \mathrm{C}$ |

OPERATING RATINGS(Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications, see the Electrical Characteristics.)

| Rating | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Operating Junction Temperature Range | TJ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Supply Voltage | Vin | 40 | V |

ELECTRICAL CHARACTERISTICS / SYSTEM PARAMETERS ([Note 1] Test Circuit Figure 15) (Unless otherwise specified, Vin $=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5.0 \mathrm{~V}$, and Adjustable version, Vin $=25 \mathrm{~V}$ for the 12 V version, and Vin $=30 \mathrm{~V}$ for the 15 V version. ILoad $=500 \mathrm{~mA}$. For typical values $\mathrm{TJ}=25^{\circ} \mathrm{C}$, for min/max values TJ is the operating junction temperature range that applies [Note 2], unless otherwise noted.)

| Characteristics | Symbol | Min | TYP | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| LM2575-3.3V ([Note 1] Test Circuit Figure 2) |  |  |  |  |  |
| Output Voltage (Vin = 12V, ILOAD $=0.5 \mathrm{~A}, \mathrm{TJ}=25^{\circ} \mathrm{C}$ ) | Vout | 3.234 | 3.3 | 3.366 | V |
| $\begin{aligned} & \text { Output Voltage }(6.0 \mathrm{~V} \leq \mathrm{Vin} \leq 40 \mathrm{~V}, 0.5 \mathrm{~A} \leq \operatorname{LLOAD} \leq 1 \mathrm{~A} \\ & \mathrm{TJ}=25^{\circ} \mathrm{C} \\ & \mathrm{TJ}=-40^{\circ} \mathrm{C} \sim+125^{\circ} \mathrm{C} \end{aligned}$ | Vout | $\begin{aligned} & 3.168 \\ & 3.135 \\ & \hline \end{aligned}$ |  | $\begin{aligned} & 3.432 \\ & 3.465 \end{aligned}$ | V |
| Efficiency (Vin=12V, lıoad=1A) | $\eta$ | - | 75 | - | \% |


| LM2575-5.0V ([Note 1] Test Circuit Figure 2) |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Output Voltage (Vin = 12V, ILOAD $\left.=0.5 \mathrm{~A}, \mathrm{TJ}=25^{\circ} \mathrm{C}\right)$ | Vout | 4.9 | 5.0 | 5.1 | V |
| Output VoItage ( $8.0 \mathrm{~V} \leq \operatorname{Vin} \leq 40 \mathrm{~V}, 0.5 \mathrm{~A} \leq \mathrm{ILOAD} \leq 1 \mathrm{~A}$ |  |  |  |  |  |
| $\mathrm{TJ}=25^{\circ} \mathrm{C}$ | Vout | 4.8 | 5.0 | 5.2 | V |
| $\mathrm{TJ}=-40^{\circ} \mathrm{C} \sim+125^{\circ} \mathrm{C}$ |  | 4.75 | - | 5.25 |  |
| Efficiency (Vin=12V, ILOAD=1A) | n | - | 77 | - | $\%$ |


| LM2575-12V ([Note 1] Test Circuit Figure 2) |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Output Voltage (Vin $\left.=25 \mathrm{~V}, \operatorname{ILOAD}=0.5 \mathrm{~A}, \mathrm{TJ}=25^{\circ} \mathrm{C}\right)$ | Vout | 11.76 | 12 | 12.24 | V |
| Output Voltage ( $15 \mathrm{~V} \leq \mathrm{Vin} \leq 40 \mathrm{~V}, 0.5 \mathrm{~A} \leq \mathrm{I}$ LOAD $\leq 1 \mathrm{~A}$ |  |  |  |  |  |
| $\mathrm{TJ}=25^{\circ} \mathrm{C}$ | Vout | 11.52 | 12 | 12.48 | V |
| $\mathrm{TJ}=-40^{\circ} \mathrm{C} \sim+125^{\circ} \mathrm{C}$ |  | 11.4 | - | 12.6 |  |
| Efficiency (Vin=12V, ILOAD=1A) | n | - | 88 | - | $\%$ |


| LM2575-ADJ ([Note 1] Test Circuit Figure 2) |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Feedback Voltage (Vin=12V, LLOAD=0.5A, TJ=25 $\left.{ }^{\circ} \mathrm{C}\right)$ | Vout | 1.217 | 1.23 | 1.243 | V |
| Feedback Voltage (8.0V $\leq$ Vin $\leq 40 \mathrm{~V}, 0.5 \mathrm{~A} \leq \operatorname{ILOAD} \leq 1 \mathrm{~A}$, Vout=5.0V) |  |  |  |  |  |
| $\mathrm{TJ}=25^{\circ} \mathrm{C}$ | Vout | 1.193 | 1.23 | 1.267 | V |
| $\mathrm{TJ}=-40^{\circ} \mathrm{C} \sim+125^{\circ} \mathrm{C}$ |  | 1.18 | - | 1.28 |  |
| Efficiency (Vin=12V, ILOAD=1A, Vout=5.0V) | n | - | 77 | - | $\%$ |

1. External components such as the catch diode, inductor, input and output capacitors can affect switching regulator system performance.
When the LM2575 is used as shown in the Figure 15 test circuit, system performance will be as shown in system parameters section.
2. Tested junction temperature range for the LM 2575 : Tlow $=-40^{\circ} \mathrm{C}$ Thigh $=+125^{\circ} \mathrm{C}$

## ELECTRICAL CHARACTERISTICS / Device Parameters

(Unless otherwise specified, Vin = 12 V for the $3.3 \mathrm{~V}, 5.0 \mathrm{~V}$, and Adjustable version, Vin $=25 \mathrm{~V}$ for the 12 V version, and $\mathrm{Vin}=30 \mathrm{~V}$ for the 15 V version. $\mathrm{ILoad}=500 \mathrm{~mA}$. For typical values $\mathrm{TJ}=25^{\circ} \mathrm{C}$, for min $/ \mathrm{max}$ values TJ is the operating junction temperature range that applies [Note 2], unless otherwise noted.)

| Characteristics | Symbol | Min | TYP | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| All Output Voltage Versions |  |  |  |  |  |
| $\begin{aligned} & \text { Feedback Bias Current (Vout=5.0V [Adjustable Version Only ] }) \\ & \mathrm{T} J^{\mathrm{N}}=25^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{J}}=-40 \text { to }+125^{\circ} \mathrm{C} \end{aligned}$ | 1 b | - | 25 | $\begin{aligned} & 100 \\ & 200 \end{aligned}$ | nA |
| $\begin{aligned} & \text { Oscillator Frequency [Note 3] } \\ & \mathrm{TJ}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ & \mathrm{TJ}_{\mathrm{J}}=0 \text { to }+125^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{J}}=-40 \text { to }+125^{\circ} \mathrm{C} \\ & \hline \end{aligned}$ | Fosc | $\begin{aligned} & 47 \\ & 47 \end{aligned}$ | $52$ | $\begin{aligned} & 58 \\ & 63 \\ & \hline \end{aligned}$ | kHz |
| $\begin{aligned} & \text { Saturation Voltage (lout=1A [note 4]) } \\ & {\mathrm{TJ}=25^{\circ} \mathrm{C}}_{\mathrm{TJ}=-40 \text { to }+125^{\circ} \mathrm{C}} \end{aligned}$ | Vsat | - | 1.2 | $\begin{array}{r} 1.4 \\ 1.7 \\ \hline \end{array}$ | V |
| Max Duty Cycle ("0") [ Note 5] | DC | 94 | 98 | - | \% |
| $\begin{aligned} & \text { Current Limit (Peak Current [Note 3 and 4]) } \\ & \mathrm{TJ}=25^{\circ} \mathrm{C} \\ & \mathrm{TJ}=-40 \text { to }+125^{\circ} \mathrm{C} \\ & \hline \end{aligned}$ | ICL | $\begin{aligned} & 4.2 \\ & 3.5 \\ & \hline \end{aligned}$ | 5.8 | $\begin{array}{r} 6.9 \\ 7.5 \\ \hline \end{array}$ | A |
| $\begin{aligned} & \text { Output Leakage Current [Note } 6 \text { and } 7], \mathrm{TJ}=25^{\circ} \mathrm{C} \\ & \text { Output }=0 \mathrm{~V} \\ & \text { Output }=-1.0 \mathrm{~V} \\ & \hline \end{aligned}$ | IL | - | $\begin{gathered} 0.8 \\ 6 \\ \hline \end{gathered}$ | $\begin{aligned} & 50 \\ & 30 \\ & \hline \end{aligned}$ | mA |
| $\begin{aligned} & \text { Quiescent Current [Note 6] } \\ & \mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{J}}=-40 \text { to }+125^{\circ} \mathrm{C} \end{aligned}$ | lQ | - |  | $\begin{gathered} 9 \\ 11 \\ \hline \end{gathered}$ | mA |
| $\begin{aligned} & \text { Standby Quiescent Current (ON/OFF Pin = 5.0V ("off")) } \\ & \mathrm{TJ}=25^{\circ} \mathrm{C} \\ & \mathrm{TJ}=-40 \text { to }+125^{\circ} \mathrm{C} \\ & \hline \end{aligned}$ | Istby | - | 80 | $\begin{aligned} & 200 \\ & 400 \\ & \hline \end{aligned}$ | $\mu \mathrm{A}$ |
| ON/OFF Pin Logic Input Level (Test circuit Figure 15) Vout $=0 \mathrm{~V}$ $\mathrm{TJ}=25^{\circ} \mathrm{C}$ $\mathrm{T} \jmath=-40$ to $+125^{\circ} \mathrm{C}$ Vout=0V $\mathrm{T}=25^{\circ} \mathrm{C}$ TJ= -40 to $+125^{\circ} \mathrm{C}$ | VIH | 2.2 2.4 | 1.4 | - | V |
| $\begin{aligned} & \text { Vout=Nominal Output Voltage } \\ & \mathrm{TJ}=25^{\circ} \mathrm{C} \\ & \mathrm{TJ}=-40 \text { to }+125^{\circ} \mathrm{C} \\ & \hline \end{aligned}$ | VIL | - | 1.2 | $\begin{gathered} 1 \\ 0.8 \\ \hline \end{gathered}$ | V |
| ON/OFF Pin Input Current (Test Circuit Figure 15) ON/OFF Pin $=5.0 \mathrm{~V}$ (Regulator OFF), $\mathrm{TJ}=25^{\circ} \mathrm{C}$ ON/OFF Pin $=0 \mathrm{~V}$ (Regulator ON), TJ=25 ${ }^{\circ} \mathrm{C}$ | $\begin{aligned} & \mathrm{I}_{\mathrm{H}} \\ & \mathrm{I}_{\mathrm{L}} \end{aligned}$ | - | 15 0 | $\begin{array}{r} 30 \\ 5.0 \end{array}$ | $\mu \mathrm{A}$ |

3. The oscillator frequency reduces to approximately 18 kHz in the event of an output short or an overload which causes the regulated output voltage to drop approximately $40 \%$ from the nominal output voltage. This self protection feature lowers the average dissipation of the IC by lowering the minimum duty cycle from $5 \%$ down to approximately $2 \%$.
4. Output (Pin 2) sourcing current. No diode, inductor or capacitor connected to output pin.
5. Feedback (Pin 4) removed from output and connected to 0 V .
6. Feedback (Pin 4) removed from output and connected to +12 V for the Adjustable, 3.3 V, and 5.0V ersions, and +25 V for the 12 V and 15 V versions, to force the output transistor "off".
7. $\mathrm{Vin}=40 \mathrm{~V}$.

## TYPICAL PERFORMANCE CHARACTERISTICS (Circuit of Figure 15)




Current Limit


Standby Quiescent Current


## TYPICAL PERFORMANCE CHARACTERISTICS (Circuit of Figure 15)



JUNCTION TENPERATURE ( ${ }^{\circ} \mathrm{C}$ )




Minimum Operating Voltage



Fixed Output Voltage Versions


Adjustable Output Voltage Versions


Where $\mathrm{V}_{\text {ref }}=1.23 \mathrm{~V}$, R1 between 1.0 k and 5.0 k
< Figure 15. Typical Test Circuit >

## PIN FUNCTION DESCRIPTION

|  | Symbol | Description |
| :---: | :---: | :--- |
| 1 | Vin | This pin is the positive input supply for the LM2575 step-down switching <br> regulator.In order to minimize voltage transients and to supply the switching <br> currents needed by the regulator, a suitable input bypass capacitor must be <br> present .(Cin in Figure 1). |
| 2 | Output | This is the emitter of the internal switch. The saturation voltage Vsat of this <br> output switch is typically 1.5 V. It should be kept in mind that the PCB area <br> connected to this pin should be kept to a minimum in order to minimize <br> coupling to sensitive circuitry. |
| 3 | Gnd | Circuit ground pin. See the information about the printed circuit board layout. |
| 4 | Feedback | This pin senses regulated output voltage to complete the feedback loop. <br> The signal is divided by the internal resistor divider network R2, R1 and <br> applied to the non-inverting input of the internal error amplifier. In the |
| Adjustable version of the LM2575 switching regulator this pin is the direct |  |  |
| input of the error amplifier and the resistor network R2, R1 is connected |  |  |
| externally to allow programming of the output voltage. |  |  |

Procedure (Fixed Output Voltage Version) In order to simplify the switching regulator design, a step-by-step design procedure and some examples are provided.

| Procedure | Example |
| :---: | :---: |
| Given Parameters: <br> $\mathrm{V}_{\text {out }}=$ Regulated Output Voltage ( $3.3 \mathrm{~V}, 5.0 \mathrm{~V}, 12 \mathrm{~V}$ or 15 V ) <br> $V_{\text {in(max) }}=$ Maximum Input Voltage <br> $I_{\text {Load }}(\max )=$ Maximum Load Current | Given Parameters: $\begin{aligned} & V_{\text {out }}=5.0 \mathrm{~V} \\ & V_{\text {inn(max) }}=15 \mathrm{~V} \\ & \mathrm{~L}_{\mathrm{Lasd}(\max )}=3.0 \mathrm{~A} \end{aligned}$ |
| 1. Controller IC Selection According to the required input voltage, output voltage and current, select the appropriate type of the controller IC output voltage version. | 1. Controller IC Selection According to the required input voltage, output voltage, current polarity and current value, use the LM2575-5.0 controller IC |
| 2. Input Capacitor Selection ( $\mathrm{C}_{\text {in }}$ ) <br> To prevent large voltage transients from appearing at the input and for stable operation of the converter, an aluminium or tantalum electrolytic bypass capacitor is needed between the input pin $+V_{\text {in }}$ and ground pin GND. This capacitor should be located close to the IC using short leads. This capacitor should have a low ESR (Equivalent Series Resistance) value. | 2. Input Capacitor Selection ( $\mathrm{C}_{\text {in }}$ ) A $100 \mu \mathrm{~F}, 25 \mathrm{~V}$ aluminium electrolytic capacitor located near to the input and ground pins provides sufficient bypassing. |
| 3. Catch Diode Selection (D1) <br> A. Since the diode maximum peak current exceeds the regulator maximum load current the catch diode current rating must be at least 1.2 times greater than the maximum load current. For a robust design the diode should have a current rating equal to the maximum current limit of the LM2576 to be able to withstand a continuous output short <br> B. The reverse voltage rating of the diode should be at least 1.25 times the maximum input voltage. | 3. Catch Diode Selection (D1) <br> A. For this example the current rating of the diode is 3.0 A . <br> B. Use a 20 V 1 N 5820 Schottky diode, or any of the suggested fast recovery diodes |
| 4. Inductor Selection (L1) <br> A. According to the required working conditions, select the correct inductor value using the selection guide from <br> B. From the appropriate inductor selection guide, identify the inductance region intersected by the Maximum Input Voltage line and the Maximum Load Current line. Each region is identified by an inductance value and an inductor code. <br> C. Select an appropriate inductor from the several different manufacturers part numbers The designer must realize that the inductor current rating must be higher than the maximum peak current flowing through the inductor. This maximum peak current can be calculated as follows: $\mathrm{I}_{\mathrm{p}(\max )}=\mathrm{I}_{\mathrm{Load}(\max )}+\frac{\left(\mathrm{V}_{\text {in }}-\mathrm{V}_{\text {out }}\right) \mathrm{t}_{\text {on }}}{2 \mathrm{~L}}$ <br> where $\mathrm{t}_{\text {on }}$ is the "on" time of the power switch and $t_{\text {on }}=\frac{V_{\text {out }}}{V_{\text {in }}} \times \frac{1.0}{f_{\text {osc }}}$ <br> For additional information about the inductor, see the inductor section in the "Application Hints" section of this data sheet. | 4. Inductor Selection (L1) <br> A. From the selection guide, the inductance area intersected by the 15 V line and 3.0 A line is L 100 . <br> B. Inductor value required is $100 \mu \mathrm{H}$. choose an inductor from any of the listed manufacturers. |

Procedure (Fixed Output Voltage Version) (continued)In order to simplify the switching regulator design, a step-by-step design procedure and some examples are provided.

| Procedure | Example |
| :--- | :--- |
| 5. Output Capacitor Selection ( $\mathrm{C}_{\text {out }}$ ) | . Output Capacitor Selection ( $\mathrm{C}_{\text {out }}$ ) |
| A. Since the I LM2575 is a forward-mode switching regulator |  |
| with voltage mode control, its open loop 2-pole-1-zero | A. $\mathrm{C}_{\text {out }}=680 \mu \mathrm{~F}$ to $2000 \mu \mathrm{~F}$ standard aluminium electrolytic. |
| frequency characteristic has the dominant pole-pair |  |
| determined by the output capacitor and inductor values. For |  |
| stable operation and an acceptable ripple voltage, | B. Capacitor voltage rating $=20 \mathrm{~V}$. |
| (approximately $1 \%$ of the output voltage) a value between |  |
| $680 \mu \mathrm{~F}$ and $2000 \mu \mathrm{~F}$ is recommended. |  |
| B. Due to the fact that the higher voltage electrolytic capacitors |  |
| generally have lower ESR (Equivalent Series Resistance) |  |
| numbers, the output capacitor's voltage rating should be at |  |
| least 1.5 times greater than the output voltage. For a 5.0 V |  |
| regulator, a rating at least 8.0 V is appropriate, and a 10 V or |  |
| 16 V rating is recommended. |  |

Procedure (Adjustable Output Version: LM2575-ADJ

| Procedure | Example |
| :---: | :---: |
| Given Parameters: <br> $V_{\text {out }}=$ Regulated Output Voltage <br> $V_{\text {in(max })}=$ Maximum DC Input Voltage <br> $I_{\text {Load }(\max )}=$ Maximum Load Current | Given Parameters: $\begin{aligned} & V_{\text {out }}=8.0 \mathrm{~V} \\ & V_{\text {in }(\max )}=25 \mathrm{~V} \\ & \mathrm{~L}_{\text {Load }(\max )}=2.5 \mathrm{~A} \end{aligned}$ |
| 1. Programming Output Voltage <br> To select the right programming resistor R1 and R2 value use the following formula: $V_{\text {out }}=V_{\text {ref }}\left(1.0+\frac{R 2}{R 1}\right) \text { where } V_{\text {ref }}=1.23 \mathrm{~V}$ <br> Resistor R1 can be between 1.0 k and 5.0 k . (For best temperature coefficient and stability with time, use $1 \%$ metal film resistors). $R 2=R 1\left(\frac{V_{\text {out }}}{V_{\text {ref }}}-1.0\right)$ | 1. Programming Output Voltage (selecting R1 and R2) Select R1 and R2: $\begin{aligned} & V_{\text {out }}=1.23\left(1.0+\frac{R 2}{R 1}\right) \text { Select } R 1=1.8 \mathrm{k} \Omega \\ & R 2=R 1\left(\frac{V_{\text {out }}}{V_{\text {ref }}}-1.0\right)=1.8 \mathrm{k}\left(\frac{8.0 \mathrm{~V}}{1.23 \mathrm{~V}}-1.0\right) \end{aligned}$ <br> $\mathrm{R} 2=9.91 \mathrm{k}$, choose a 9.88 k metal film resistor. |
| 2. Input Capacitor Selection ( $\mathrm{C}_{\mathrm{in}}$ ) <br> To prevent large voltage transients from appearing at the input and for stable operation of the converter, an aluminium or tantalum electrolytic bypass capacitor is needed between the input pin $+\mathrm{V}_{\text {in }}$ and ground pin GND This capacitor should be located close to the IC using short leads. This capacitor should have a low ESR (Equivalent Series Resistance) value. <br> For additional information see input capacitor section in the "Application Hints" section of this data sheet. | 2. Input Capacitor Selection ( $\mathrm{C}_{\mathrm{in}}$ ) <br> A $100 \mu \mathrm{~F}, 150 \mathrm{~V}$ aluminium electrolytic capacitor located near the input and ground pin provides sufficient bypassing. |
| 3. Catch Diode Selection (D1) <br> A. Since the diode maximum peak current exceeds the regulator maximum load current the catch diode current rating must be at least 1.2 times greater than the maximum load current. For a robust design, the diode should have a current rating equal to the maximum current limit of the LM2576 to be able to withstand a continuous output short. <br> B. The reverse voltage rating of the diode should be at least 1.25 times the maximum input voltage. | 3. Catch Diode Selection (D1) <br> A. For this example, a 3.0 A current rating is adequate. <br> B. Use a $30 \vee 1$ N5821 Schottky diode or any suggested fast recovery diode |

Procedure (Adjustable Output Version: LM2575-ADJ (continued)

| Procedure | Example |
| :---: | :---: |
| 4. Inductor Selection (L1) <br> A. Use the following formula to calculate the inductor Volt $x$ microsecond [ $\mathrm{V} \mu \mathrm{s}$ ] constant: $E \times T=\left(V_{\text {in }}-V_{\text {out }}\right) \frac{V_{\text {out }}}{V_{\text {in }}} \times \frac{106}{F[H z]}[V \times \mu s]$ <br> B. Match the calculated E $\times T$ value with the corresponding number on the vertical axis This ExT constant is a measure of the energy handling capability of an inductor and is dependent upon the type of core, the core area, the number of turns, and the duty cycle. <br> C. Next step is to identify the inductance region intersected by the $\mathrm{E} \times \mathrm{T}$ value and the maximum load current value on the horizontal axis <br> D. From the inductor code, identify the inductor value. Then select an appropriate inductor The inductor chosen must be rated for a switching frequency of 52 kHz and for a current rating of $1.15 \times \mathrm{L}$ Load. The inductor current rating can also be determined by calculating the inductor peak current: $I_{p(\max )}=I_{\text {Load }(\max )}+\frac{\left(V_{\text {in }}-V_{\text {out }}\right) t_{\text {on }}}{2 L}$ <br> where $\mathrm{t}_{\mathrm{on}}$ is the "on" time of the power switch and $t_{\text {on }}=\frac{V_{\text {out }}}{V_{\text {in }}} \times \frac{1.0}{f_{\text {osc }}}$ <br> For additional information about the inductor, see the inductor section in the "External Components" section of this data sheet. | 4. Inductor Selection (L1) <br> A. Calculate $\mathrm{E} \times \mathrm{T}[\mathrm{V} \times \mu \mathrm{s}]$ constant: $E \times T=(25-8.0) \times \frac{8.0}{25} \times \frac{1000}{52}=80[V \times \mu \mathrm{s}]$ <br> B. $\mathrm{E} \times \mathrm{T}=80[\mathrm{~V} \times \mu \mathrm{s}]$ <br> C. $I_{\text {Load }(\max )}=2.5 \mathrm{~A}$ <br> Inductance Region $=\mathrm{H} 150$ <br> D. Proper inductor value $=150 \mu \mathrm{H}$ <br> Choose the inductor from Table 2. |
| 5. Output Capacitor Selection ( $\mathrm{C}_{\text {out }}$ ) <br> A. Since the LM2576 is a forward-mode switching regulator with voltage mode control, its open loop 2-pole-1-zero frequency characteristic has the dominant pole-pair determined by the output capacitor and inductor values. <br> For stable operation, the capacitor must satisfy the following requirement: $C_{\text {out }} \geq 13,300 \frac{V_{\text {in }(\max )}}{V_{\text {out }} \times L[\mu \mathrm{H}]}[\mu \mathrm{F}]$ <br> B. Capacitor values between $10 \mu \mathrm{~F}$ and $2000 \mu \mathrm{~F}$ will satisfy the loop requirements for stable operation. To achieve an acceptable output ripple voltage and transient response, the output capacitor may need to be several times larger than the above formula yields. <br> C. Due to the fact that the higher voltage electrolytic capacitors generally have lower ESR (Equivalent Series Resistance) numbers, the output capacitor's voltage rating should be at least 1.5 times greater than the output voltage. For a 5.0 V regulator, a rating of at least 8.0 V is appropriate, and a 10 V or 16 V rating is recommended. | 5. Output Capacitor Selection ( $\mathrm{C}_{\text {out }}$ ) <br> A. $C_{\text {out }} \geq 13,300 \times \frac{25}{8 \times 150}=332.5 \mu \mathrm{~F}$ <br> To achieve an acceptable ripple voltage, select $\mathrm{C}_{\text {out }}=680 \mu \mathrm{~F}$ electrolytic capacitor. |

LM2576 Series Buck Regulator Design Procedures (continued)


Table 1. Diode Selection Guide

| $\mathrm{V}_{\mathrm{R}}$ | Schottky |  |  |  | Fast Recovery |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 3.0 A |  | 4.0-6.0 A |  | 3.0 A |  | 4.0-6.0 A |  |
|  | Through Hole | Surface Mount | Through Hole | Surface Mount | Through Hole | Surface Mount | Through Hole | Surface Mount |
| 20 V | $\begin{gathered} \text { 1N5820 } \\ \text { MBR320P } \\ \text { SR302 } \end{gathered}$ | SK32 | 1N5823 SR502 SB520 |  | MUR320 <br> 31DF1 <br> HER302 <br> (all diodes <br> rated <br> to at least <br> 100 V ) | MURS320T3 <br> MURD320 <br> 30WF10 <br> (all diodes rated to at least 100 V ) | MUR420 <br> HER602 <br> (all diodes <br> rated <br> to at least <br> 100 V ) | MURD620CT 50WF10 <br> (all diodes rated to at least 100 V ) |
| 30 V | 1N5821 <br> MBR330 $\begin{gathered} \text { SR303 } \\ 31 D Q 03 \end{gathered}$ | SK33 30 WQ03 |  | 50WQ03 |  |  |  |  |
| 40 V | 1N5822 <br> MBR340 <br> SR304 <br> 31DQ04 | $\begin{gathered} \text { SK34 } \\ \text { 30WQ04 } \\ \text { MBRS340T3 } \\ \text { MBRD340 } \end{gathered}$ |  | $\begin{aligned} & \text { MBRD640CT } \\ & \text { 50WQ04 } \end{aligned}$ |  |  |  |  |
| 50 V | $\begin{gathered} \text { MBR350 } \\ 31 D Q 05 \\ \text { SR305 } \end{gathered}$ | $\begin{gathered} \text { SK35 } \\ \text { 30WQ05 } \end{gathered}$ | SB550 | 50WQ05 |  |  |  |  |
| 60 V | $\begin{aligned} & \text { MBR360 } \\ & \text { DQ06 } \\ & \text { SR306 } \end{aligned}$ | MBRS360T3 MBRD360 | 50SQ080 | MBRD660CT |  |  |  |  |

NOTE: Diodes listed in bold are available from ON Semiconductor.

Table 2. Inductor Selection by Manufacturer's Part Number

| Inductor Code | Inductor Value | Tech 39 | Schott Corp. | Pulse Eng. | Renco |
| :---: | :---: | :---: | :---: | :---: | :---: |
| L47 | $47 \mu \mathrm{H}$ | 77212 | 67126980 | PE-53112 | RL2442 |
| L68 | $68 \mu \mathrm{H}$ | 77282 | 67126990 | PE-92114 | RL2443 |
| L100 | $100 \mu \mathrm{H}$ | 77312 | 67127000 | PE-92108 | RL2444 |
| L150 | $150 \mu \mathrm{H}$ | 77380 | 67127010 | PE-53113 | RL1954 |
| L220 | $220 \mu \mathrm{H}$ | 77408 | 67127020 | PE-52626 | RL1953 |
| L330 | $330 \mu \mathrm{H}$ | 77456 | 67127030 | PE-52627 | RL1952 |
| L470 | $470 \mu \mathrm{H}$ | * | 67127040 | PE-53114 | RL1951 |
| L680 | $880 \mu \mathrm{H}$ | 77506 | 67127050 | PE-52629 | RL1950 |
| H150 | $150 \mu \mathrm{H}$ | 77362 | 67127060 | PE-53115 | RL2445 |
| H220 | $220 \mu \mathrm{H}$ | 77412 | 67127070 | PE-53116 | RL2446 |
| H330 | $330 \mu \mathrm{H}$ | 77482 | 67127080 | PE-53117 | RL2447 |
| H470 | $470 \mu \mathrm{H}$ | $\times$ | 67127090 | PE-53118 | RL1961 |
| H680 | $880 \mu \mathrm{H}$ | 77508 | 67127100 | PE-53119 | RL1960 |
| H1000 | $1000 \mu \mathrm{H}$ | 77556 | 67127110 | PE-53120 | RL1859 |
| H1500 | $1500 \mu \mathrm{H}$ | * | 67127120 | PE-53121 | RL1958 |
| H2200 | $2200 \mu \mathrm{H}$ | $\times$ | 67127130 | PE-53122 | RL2448 |

NOTE: ${ }^{\text {² }}$ Contact Manufacturer

