



## Dual Enhancement Mode Field Effect Transistor ( N and P Channel)

| PRODUCT SUMMARY (N-Channel) |                |                                 |
|-----------------------------|----------------|---------------------------------|
| V <sub>DSS</sub>            | I <sub>D</sub> | R <sub>DS(ON)</sub> ( m Ω ) Max |
| 40V                         | 6A             | 29 @ V <sub>GS</sub> = 10V      |
|                             |                | 40 @ V <sub>GS</sub> = 4.5V     |

| PRODUCT SUMMARY (P-Channel) |                |                                 |
|-----------------------------|----------------|---------------------------------|
| V <sub>DSS</sub>            | I <sub>D</sub> | R <sub>DS(ON)</sub> ( m Ω ) Max |
| -40V                        | -5A            | 42 @ V <sub>GS</sub> = -10V     |
|                             |                | 62 @ V <sub>GS</sub> = -4.5V    |



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

| Parameter  |                       | Symbol                            | N-Channel  | P-Channel | Unit |
|--|-----------------------|-----------------------------------|------------|-----------|------|
| Drain-Source Voltage                                   |                       | V <sub>DS</sub>                   | 40         | -40       | V    |
| Gate-Source Voltage                                    |                       | V <sub>GS</sub>                   | ±20        | ±20       | V    |
| Drain Current-Continuous <sup>a</sup> @ T <sub>a</sub> | 25°C                  | I <sub>D</sub>                    | 6          | -5        | A    |
|  | 70°C                  |                                   | 5.1        | -4.2      | A    |
| -Pulsed <sup>b</sup>                                   |                       | I <sub>DM</sub>                   | 28         | -20       | A    |
| Drain-Source Diode Forward Current <sup>a</sup>        |                       | I <sub>S</sub>                    | 1.7        | -1.7      | A    |
| Maximum Power Dissipation <sup>a</sup>                 | T <sub>a</sub> = 25°C | P <sub>D</sub>                    | 2          |           | W    |
|  | T <sub>a</sub> =70°C  |                                   | 1.44       |           |      |
| Operating Junction and Storage Temperature Range       |                       | T <sub>J</sub> , T <sub>STG</sub> | -55 to 150 |           | °C   |

### THERMAL CHARACTERISTICS

|  |                  |      |      |
|--|------------------|------|------|
| Thermal Resistance, Junction-to-Ambient <sup>a</sup> | R <sub>θJA</sub> | 62.5 | °C/W |
|--|------------------|------|------|

# STM8455

## N-Channel ELECTRICAL CHARACTERISTICS (TA=25 °C unless otherwise noted)

| Parameter                              | Symbol              | Condition   | Min | Typ <sup>c</sup> | Max  | Unit  |
|--|---------------------|---|-----|------------------|------|-------|
| OFF CHARACTERISTICS                    |                     |   |     |                  |      |       |
| Drain-Source Breakdown Voltage         | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA  | 40  |                  |      | V     |
| Zero Gate Voltage Drain Current        | I <sub>DSS</sub>    | V <sub>DS</sub> =32V, V <sub>GS</sub> =0V   |     |                  | 1    | uA    |
| Gate-Body Leakage                      | I <sub>GSS</sub>    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V  |     |                  | ±100 | nA    |
| ON CHARACTERISTICS <sup>b</sup>        |                     |   |     |                  |      |       |
| Gate Threshold Voltage                 | V <sub>GS(th)</sub> | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA  | 1.0 | 1.8              | 3.0  | V     |
| Drain-Source On-State Resistance       | R <sub>DS(ON)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =5A  |     | 23               | 29   | m ohm |
|  |                     | V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A   |     | 30               | 40   | m ohm |
| On-State Drain Current                 | I <sub>D(ON)</sub>  | V <sub>DS</sub> =5V, V <sub>GS</sub> =10V   | 20  |                  |      | A     |
| Forward Transconductance               | g <sub>FS</sub>     | V <sub>DS</sub> =5V, I <sub>D</sub> =5A   |     | 15               |      | S     |
| DYNAMIC CHARACTERISTICS <sup>c</sup>   |                     |   |     |                  |      |       |
| Input Capacitance                      | C <sub>ISS</sub>    | V <sub>DS</sub> =20V, V <sub>GS</sub> =0V<br>f=1.0MHz   |     | 890              |      | pF    |
| Output Capacitance                     | C <sub>OSS</sub>    |   |     | 115              |      | pF    |
| Reverse Transfer Capacitance           | C <sub>RSS</sub>    |   |     | 65               |      | pF    |
| SWITCHING CHARACTERISTICS <sup>c</sup> |                     |   |     |                  |      |       |
| Turn-On Delay Time                     | t <sub>D(ON)</sub>  | V <sub>DD</sub> =20V<br>I <sub>D</sub> =5A<br>V <sub>GS</sub> =10V<br>R <sub>GEN</sub> =3.3 ohm |     | 16               |      | ns    |
| Rise Time                              | t <sub>r</sub>      |   |     | 12               |      | ns    |
| Turn-Off Delay Time                    | t <sub>D(OFF)</sub> |   |     | 30               |      | ns    |
| Fall Time                              | t <sub>f</sub>      |   |     | 8                |      | ns    |
| Total Gate Charge                      | Q <sub>g</sub>      | V <sub>DS</sub> =24V, I <sub>D</sub> =5A, V <sub>GS</sub> =10V                                  |     | 17               |      | nC    |
|  |                     | V <sub>DS</sub> =24V, I <sub>D</sub> =5A, V <sub>GS</sub> =4.5V                                 |     | 8.5              |      | nC    |
| Gate-Source Charge                     | Q <sub>gs</sub>     | V <sub>DS</sub> =24V, I <sub>D</sub> =5A<br>V <sub>GS</sub> =4.5V                               |     | 2.2              |      | nC    |
| Gate-Drain Charge                      | Q <sub>gd</sub>     |   |     | 4.3              |      | nC    |

# STM8455

## P-Channel ELECTRICAL CHARACTERISTICS (TA=25 °C unless otherwise noted)

| Parameter                                    | Symbol              | Condition  | Min  | Typ <sup>c</sup> | Max  | Unit  |
|--|---------------------|--|------|------------------|------|-------|
| <b>OFF CHARACTERISTICS</b>                   |                     |  |      |                  |      |       |
| Drain-Source Breakdown Voltage               | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA  | -40  |                  |      | V     |
| Zero Gate Voltage Drain Current              | I <sub>DSS</sub>    | V <sub>DS</sub> =-32V, V <sub>GS</sub> =0V   |      |                  | -1   | uA    |
| Gate-Body Leakage                            | I <sub>GSS</sub>    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V   |      |                  | ±100 | nA    |
| <b>ON CHARACTERISTICS<sup>b</sup></b>        |                     |  |      |                  |      |       |
| Gate Threshold Voltage                       | V <sub>GS(th)</sub> | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA  | -1.0 | -1.8             | -3.0 | V     |
| Drain-Source On-State Resistance             | R <sub>DS(ON)</sub> | V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A   |      | 35               | 42   | m ohm |
|  |                     | V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3A  |      | 50               | 62   | m ohm |
| On-State Drain Current                       | I <sub>D(ON)</sub>  | V <sub>DS</sub> =-5V, V <sub>GS</sub> =-10V  | 16   |                  |      | A     |
| Forward Transconductance                     | g <sub>FS</sub>     | V <sub>DS</sub> =-5V, I <sub>D</sub> =-4A  |      | 10               |      | S     |
| <b>DYNAMIC CHARACTERISTICS<sup>c</sup></b>   |                     |  |      |                  |      |       |
| Input Capacitance                            | C <sub>ISS</sub>    | V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V<br>f=1.0MHz   |      | 900              |      | pF    |
| Output Capacitance                           | C <sub>OSS</sub>    |  |      | 140              |      | pF    |
| Reverse Transfer Capacitance                 | C <sub>RSS</sub>    |  |      | 85               |      | pF    |
| <b>SWITCHING CHARACTERISTICS<sup>c</sup></b> |                     |  |      |                  |      |       |
| Turn-On Delay Time                           | t <sub>D(ON)</sub>  | V <sub>D</sub> =-20V<br>I <sub>D</sub> =-4A<br>V <sub>GEN</sub> =-10V<br>R <sub>GEN</sub> =3.3 ohm |      | 12               |      | ns    |
| Rise Time                                    | t <sub>r</sub>      |  |      | 16               |      | ns    |
| Turn-Off Delay Time                          | t <sub>D(OFF)</sub> |  |      | 55               |      | ns    |
| Fall Time                                    | t <sub>f</sub>      |  |      | 30               |      | ns    |
| Total Gate Charge                            | Q <sub>g</sub>      | V <sub>DS</sub> =-24V, I <sub>D</sub> =-4A, V <sub>GS</sub> =-10V                                  |      | 17.6             |      | nC    |
|  |                     | V <sub>DS</sub> =-24V, I <sub>D</sub> =-4A, V <sub>GS</sub> =-4.5V                                 |      | 8.8              |      | nC    |
| Gate-Source Charge                           | Q <sub>gs</sub>     | V <sub>DS</sub> =-24V, I <sub>D</sub> =-4A<br>V <sub>GS</sub> =-4.5V                               |      | 1.8              |      | nC    |
| Gate-Drain Charge                            | Q <sub>gd</sub>     |  |      | 5                |      | nC    |

# STM8455

## ELECTRICAL CHARACTERISTICS ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

| Parameter   | Symbol   | Condition                                | Min  | Typ <sup>c</sup> | Max  | Unit |
|---|----------|--|------|------------------|------|------|
| <b>DRAIN-SOURCE DIODE CHARACTERISTICS<sup>b</sup></b> |          |  |      |                  |      |      |
| Diode Forward Voltage                                 | $V_{SD}$ | $V_{GS} = 0\text{V}, I_s = 1.7\text{A}$  | N-Ch | 0.8              | 1.3  | V    |
|   |          | $V_{GS} = 0\text{V}, I_s = -1.7\text{A}$ | P-Ch | -0.77            | -1.3 |      |

### Notes

- a. Surface Mounted on FR4 Board,  $t \leq 10\text{sec}$ .
  - b. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
  - c. Guaranteed by design, not subject to production testing.
- N-Channel

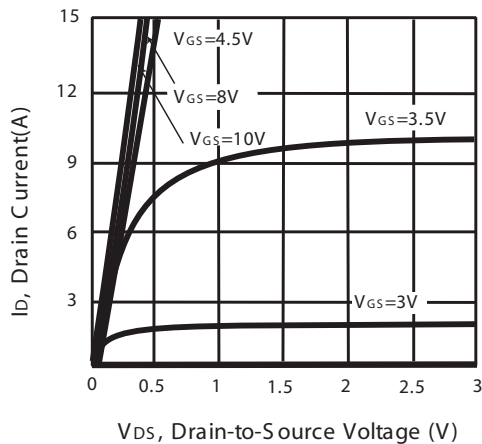


Figure 1. Output Characteristics

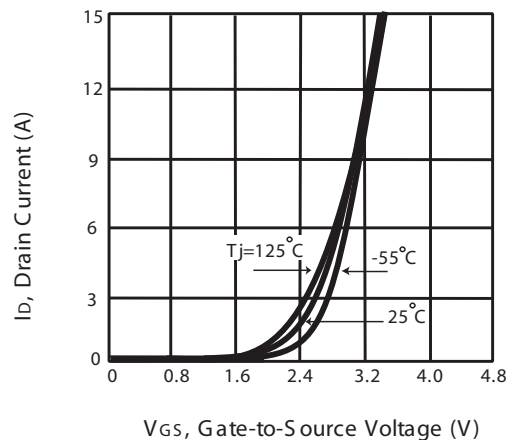


Figure 2. Transfer Characteristics

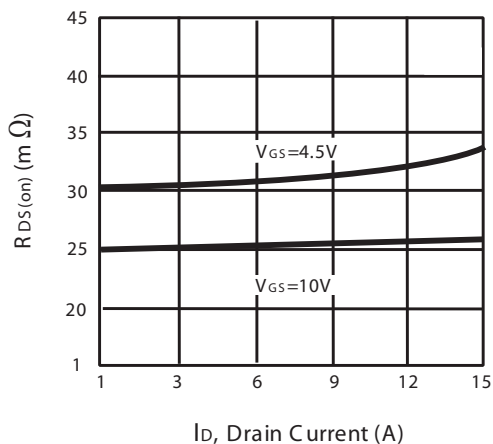


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

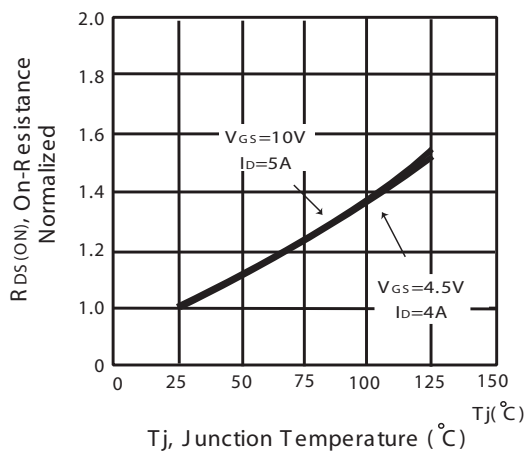


Figure 4. On-Resistance Variation with Drain Current and Temperature

# STM8455

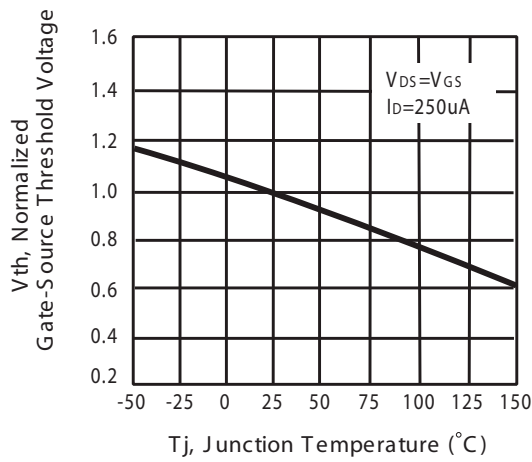


Figure 5. Gate Threshold Variation with Temperature

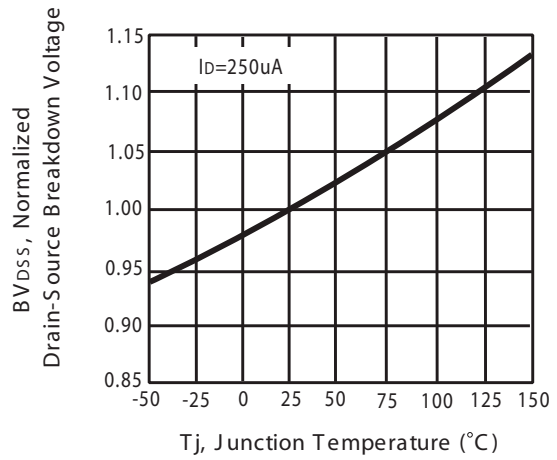


Figure 6. Breakdown Voltage Variation with Temperature

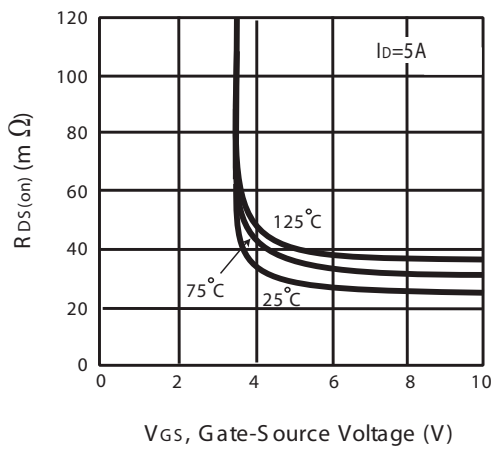


Figure 7. On-Resistance vs. Gate-Source Voltage

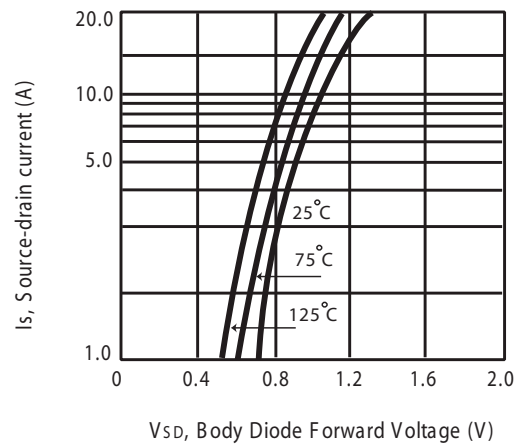


Figure 8. Body Diode Forward Voltage Variation with Source Current

# STM8455

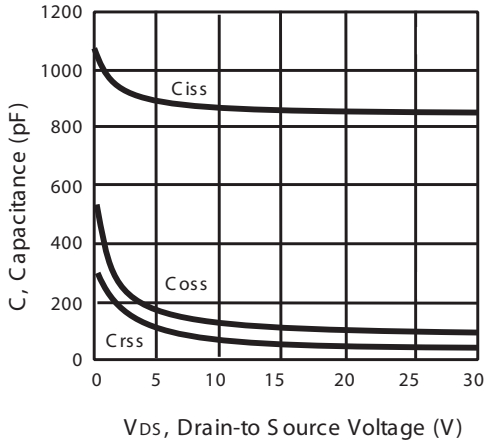


Figure 9. Capacitance

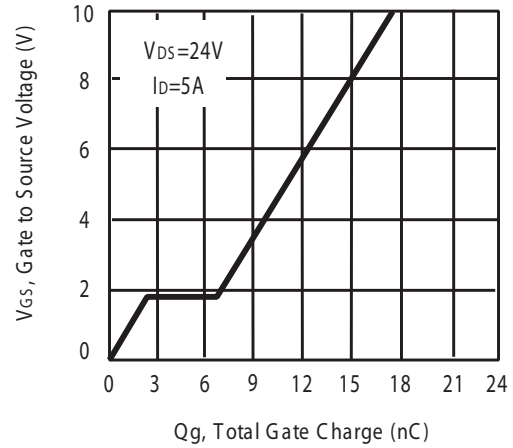


Figure 10. Gate Charge

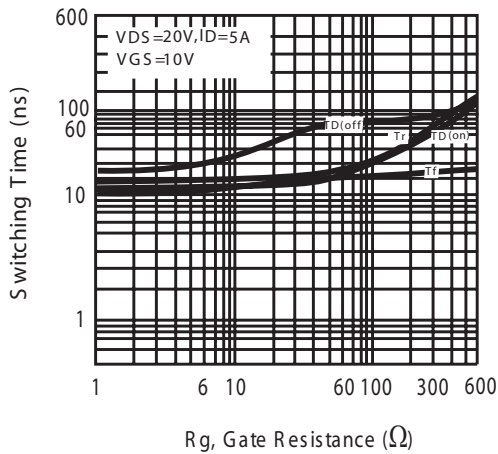


Figure 11. switching characteristics

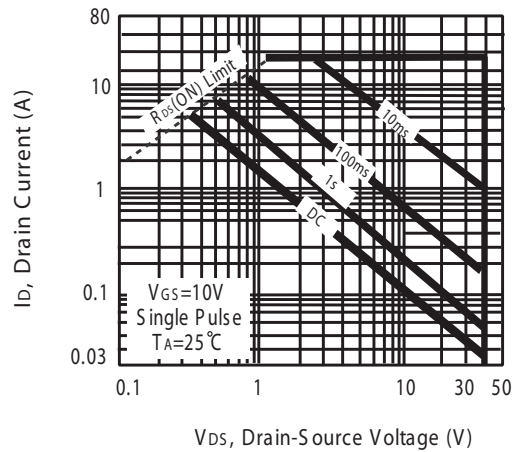
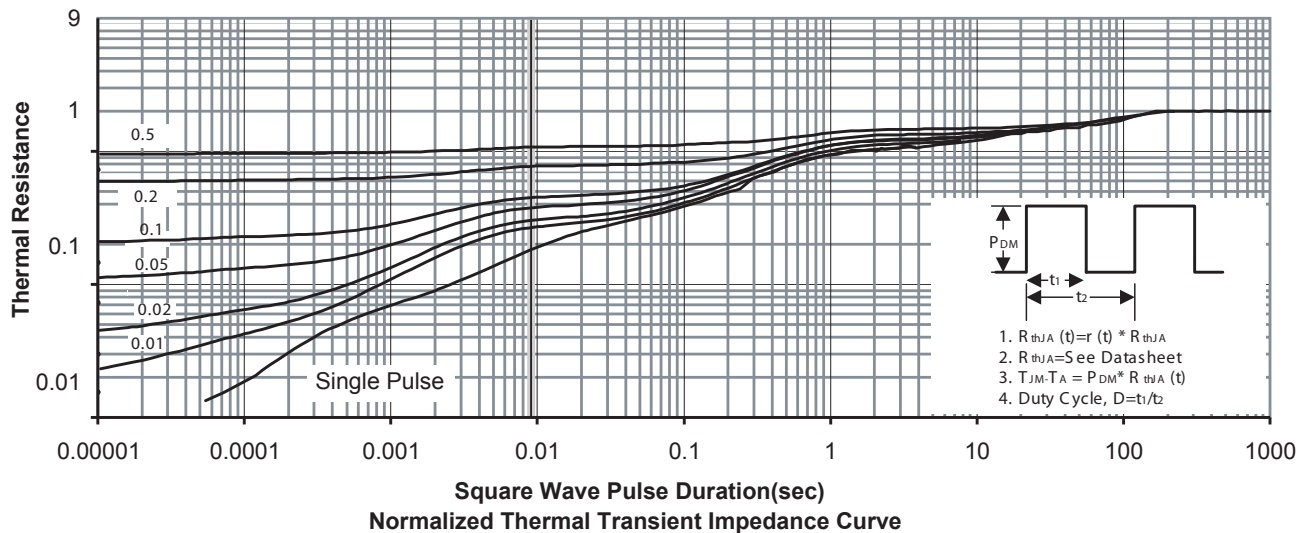


Figure 12. Maximum Safe Operating Area



# STM8455

P-Channel

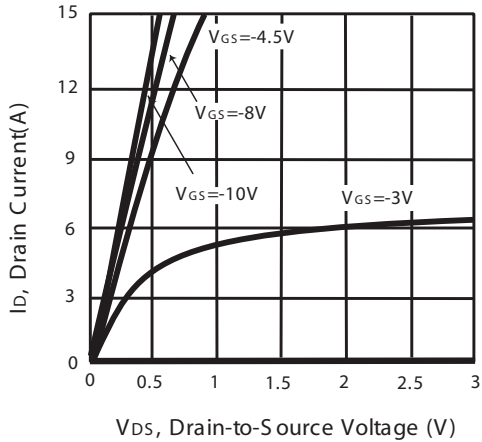


Figure 1. Output Characteristics

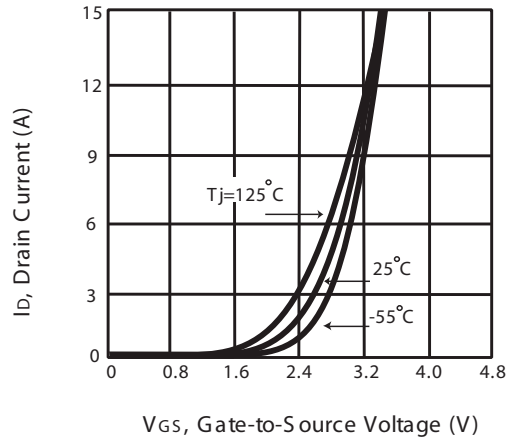


Figure 2. Transfer Characteristics

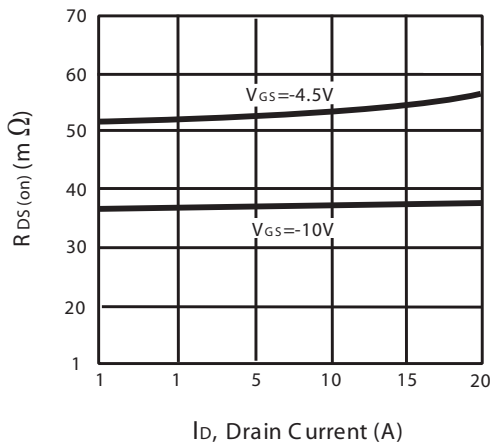


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

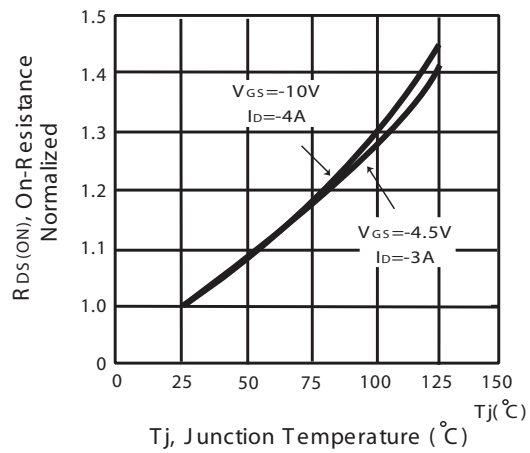


Figure 4. On-Resistance Variation with Drain Current and Temperature

# STM8455

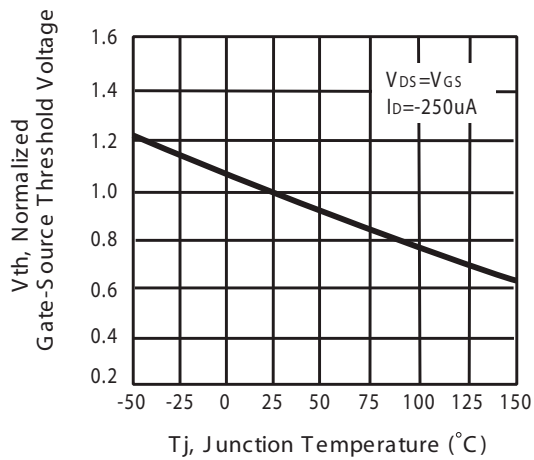


Figure 5. Gate Threshold Variation with Temperature

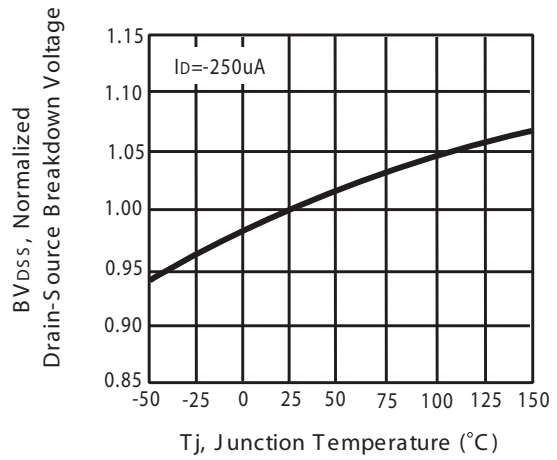


Figure 6. Breakdown Voltage Variation with Temperature

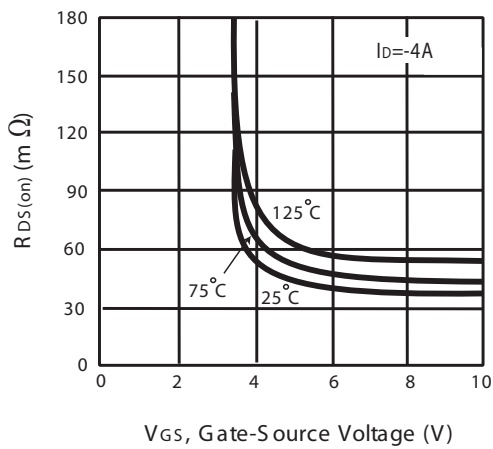


Figure 7. On-Resistance vs. Gate-Source Voltage

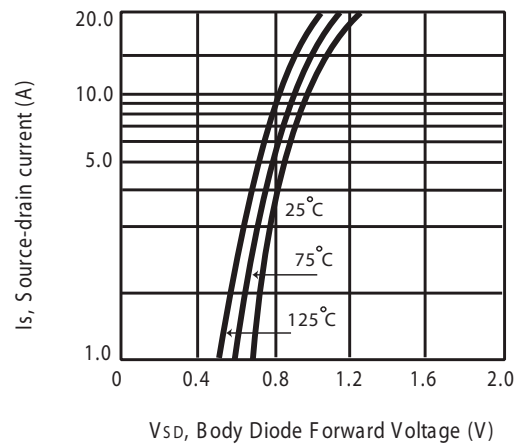


Figure 8. Body Diode Forward Voltage Variation with Source Current



# STM8455

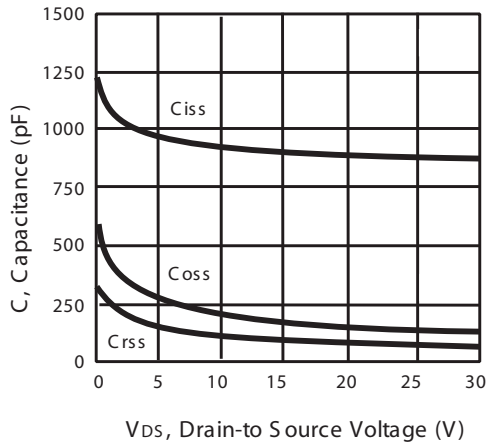


Figure 9. Capacitance

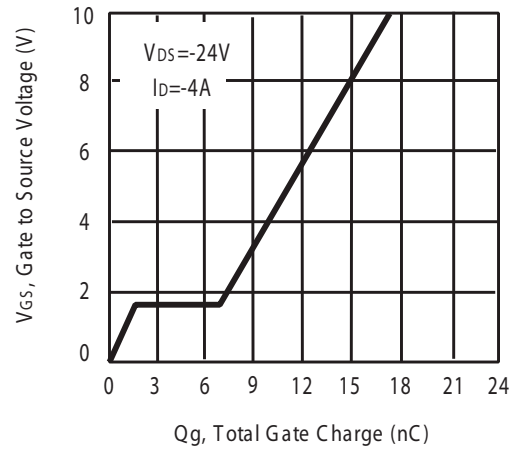


Figure 10. Gate Charge

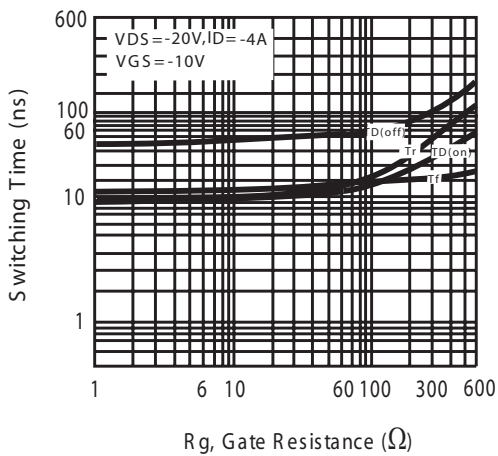


Figure 11. switching characteristics

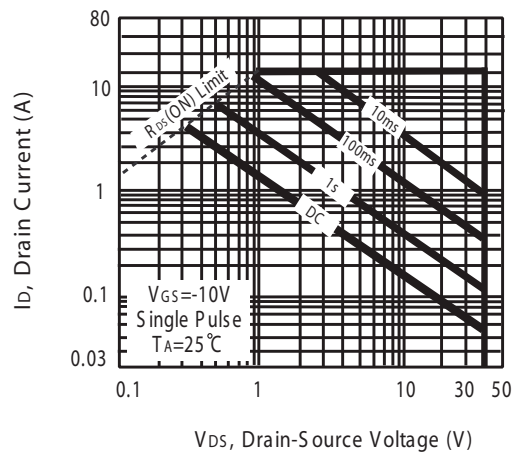
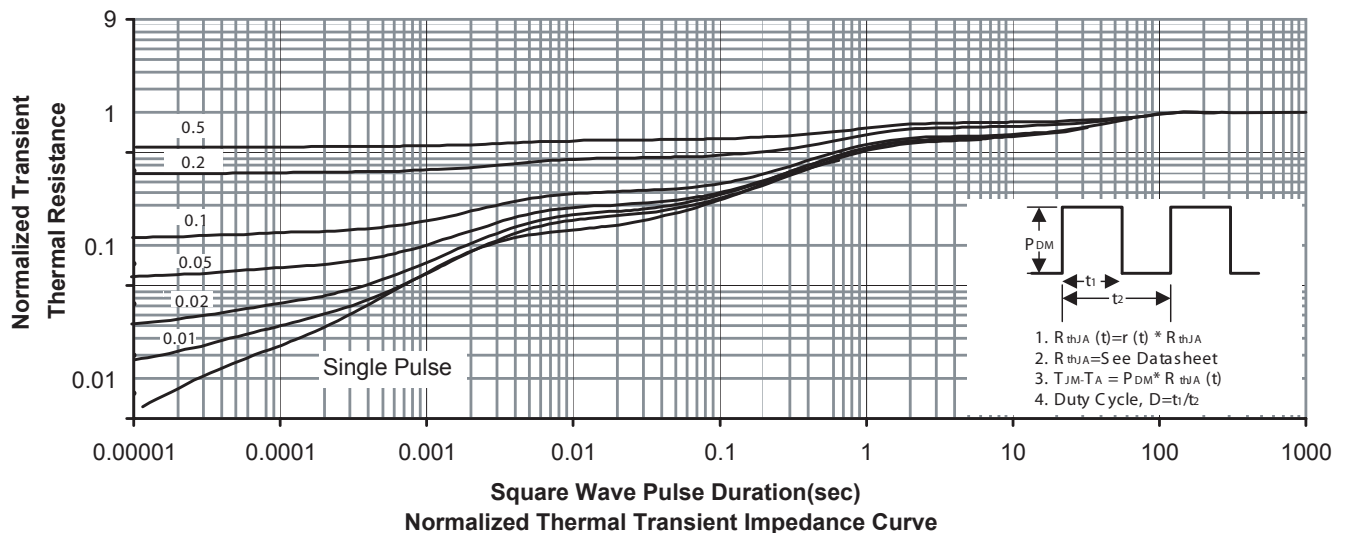
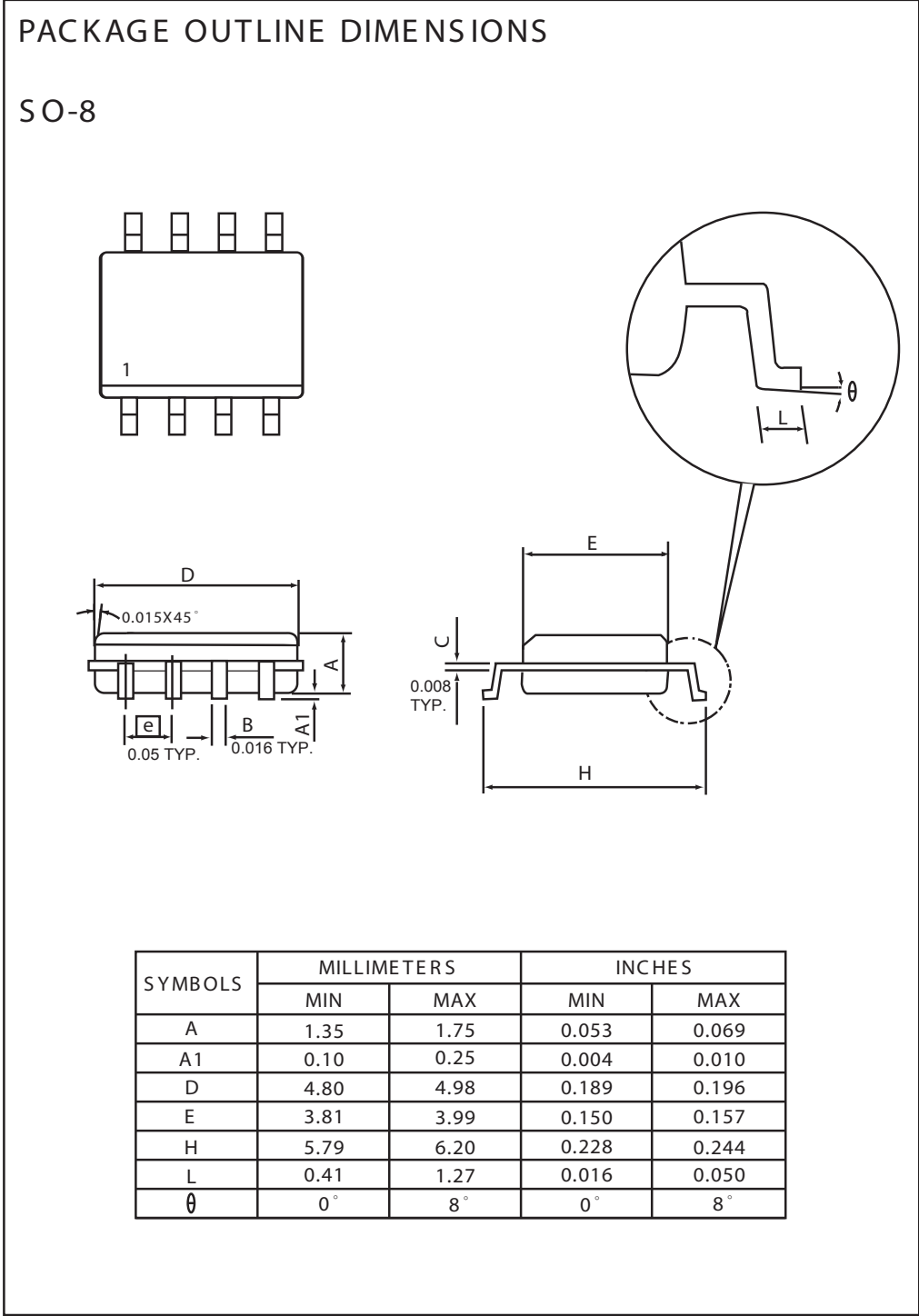


Figure 12. Maximum Safe Operating Area



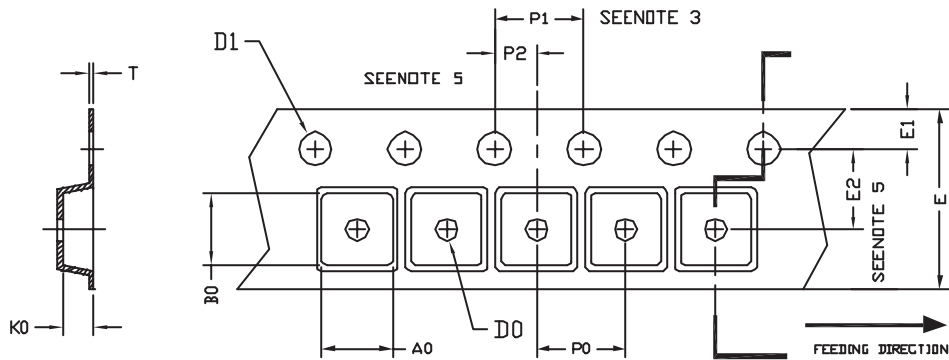
# STM8455



# STM8455

## SO-8 Tape and Reel Data

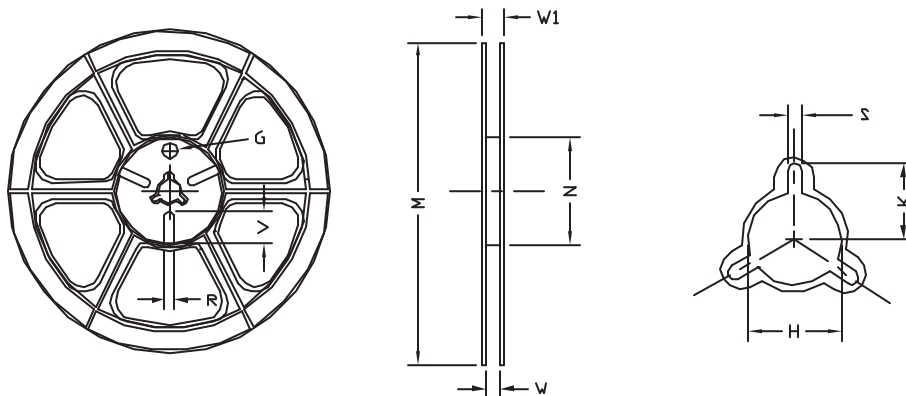
### SO-8 Carrier Tape



unit:mm

| PACKAGE          | A0   | B0   | K0   | D0                  | D1                           | E                 | E1   | E2                | P0  | P1  | P2                | T                 |
|------------------|------|------|------|---------------------|------------------------------|-------------------|------|-------------------|-----|-----|-------------------|-------------------|
| SOP 8N<br>150mil | 6.40 | 5.20 | 2.10 | $\phi$ 1.5<br>(MIN) | $\phi$ 1.5<br>+ 0.1<br>- 0.0 | 12.0<br>$\pm$ 0.3 | 1.75 | 5.5<br>$\pm$ 0.05 | 8.0 | 4.0 | 2.0<br>$\pm$ 0.05 | 0.3<br>$\pm$ 0.05 |

### SO-8 Reel



UNIT:mm

| TAPE SIZE | REEL SIZE  | M              | N               | W             | W1            | H                      | K   | S                 | G   | R   | V   |
|-----------|------------|----------------|-----------------|---------------|---------------|------------------------|-----|-------------------|-----|-----|-----|
| 12 mm     | $\phi$ 330 | 330<br>$\pm$ 1 | 62<br>$\pm$ 1.5 | 12.4<br>+ 0.2 | 16.8<br>- 0.4 | $\phi$ 12.75<br>+ 0.15 | --- | 2.0<br>$\pm$ 0.15 | --- | --- | --- |