

# STM8301

SamHop Microelectronics Corp.

Apr.28 2005 ver1.1

## Dual Enhancement Mode Field Effect Transistor ( N and P Channel)

PRODUCT (N-Channel)		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DSON</sub> (mΩ) Max
30V	7A	25 @ V <sub>GS</sub> = 10V
		42 @ V <sub>GS</sub> = 4.5V

PRODUCT SUMMARY (P-Channel)		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DSON</sub> (mΩ) Max
-30V	-4.5A	60 @ V <sub>GS</sub> = -10V
		80 @ V <sub>GS</sub> = -4.5V



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	±20	V
Drain Current-Continuous <sup>a</sup> @ T <sub>a</sub>	I <sub>D</sub>	7	-4.5	A
		6	-3.8	A
-Pulsed <sup>b</sup>	I <sub>DM</sub>	29	-18	A
Drain-Source Diode Forward Current <sup>a</sup>	I <sub>S</sub>	1.7	-1.7	A
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	2		W
T <sub>a</sub> =70°C		1.44		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150		°C

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient <sup>a</sup>	R <sub>θJA</sub>	62.5	°C/W
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N-Channel ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ C$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24V, V_{GS} = 0V$		1		$\mu A$
Gate-Body Leakage	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$		$\pm 100$		$nA$
<b>ON CHARACTERISTICS <sup>b</sup></b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.6	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 7A$		20	25	m ohm
		$V_{GS} = 4.5V, I_D = 5A$		34	42	m ohm
On-State Drain Current	$I_{D(ON)}$	$V_{DS} = 10V, V_{GS} = 10V$	29			A
Forward Transconductance	$g_{FS}$	$V_{DS} = 10V, I_D = 7A$		10		S
<b>DYNAMIC CHARACTERISTICS <sup>c</sup></b>						
Input Capacitance	$C_{ISS}$	$V_{DS} = 15V, V_{GS} = 0V$ $f = 1.0MHz$		792		pF
Output Capacitance	$C_{OSS}$			133		pF
Reverse Transfer Capacitance	$C_{RSS}$			87		pF
<b>SWITCHING CHARACTERISTICS <sup>c</sup></b>						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD} = 15V$ $I_D = 1A$ $V_{GS} = 10V$ $R_{GEN} = 6\text{ ohm}$		6.5		ns
Rise Time	$t_r$			12.9		ns
Turn-Off Delay Time	$t_{D(OFF)}$			17.9		ns
Fall Time	$t_f$			5.2		ns
Total Gate Charge	$Q_g$	$V_{DS} = 15V, I_D = 7A, V_{GS} = 10V$		13.6		nC
		$V_{DS} = 15V, I_D = 7A, V_{GS} = 4.5V$		6.8		nC
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15V, I_D = 7A,$ $V_{GS} = 10V$		2.0		nC
Gate-Drain Charge	$Q_{gd}$			3.5		nC

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P-Channel ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$			-1	$\mu\text{A}$
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$			$\pm 100$	$\text{nA}$
ON CHARACTERISTICS <sup>b</sup>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.5	-2.5	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}= -4.5\text{A}$		50	60	$\text{m ohm}$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}= -3\text{A}$		70	80	$\text{m ohm}$
On-State Drain Current	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=-10\text{V}$	-18			A
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-10\text{V}, I_{\text{D}} = - 4.5\text{A}$		9		S
DYNAMIC CHARACTERISTICS <sup>c</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		586		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			122		$\text{pF}$
Reverse Transfer Capacitance	$C_{\text{rss}}$			78		$\text{pF}$
SWITCHING CHARACTERISTICS <sup>c</sup>						
Turn-On Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=-15\text{V}$ $I_{\text{D}} = -1\text{A}$ $V_{\text{GEN}}=-10\text{V}$ $R_{\text{GEN}}=6 \text{ ohm}$		5.6		ns
Rise Time	$t_{\text{r}}$			4.7		ns
Turn-Off Delay Time	$t_{\text{D}(\text{OFF})}$			51.8		ns
Fall Time	$t_{\text{f}}$			33.5		ns
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-4.5\text{A}, V_{\text{GS}}=-10\text{V}$		11.2		$\text{nC}$
		$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-4.5\text{A}, V_{\text{GS}}=-4.5\text{V}$		5.4		$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}} = - 4.5\text{A},$ $V_{\text{GS}}=-10\text{V}$		1.4		$\text{nC}$
Gate-Drain Charge	$Q_{\text{gd}}$			2.7		$\text{nC}$

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## ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>b</sup>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_S = 1.7A$ $V_{GS} = 0V, I_S = -1.7A$	N-Ch P-Ch	0.79 -0.8	1.2 -1.2	V

### Notes

a. Surface Mounted on FR4 Board,  $t \leq 10\text{ sec}$ .

\*  $R_{\theta JA}$  is  $62.5^\circ\text{C}/\text{W}$  when mounted on  $1\text{ in}^2$  FR-4 board with 2oz Copper

\*  $R_{\theta JA}$  is  $125^\circ\text{C}/\text{W}$  when mounted on  $0.02\text{ in}^2$  FR-4 board with 2oz Copper

b. Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

c. Guaranteed by design, not subject to production testing.

### N-Channel

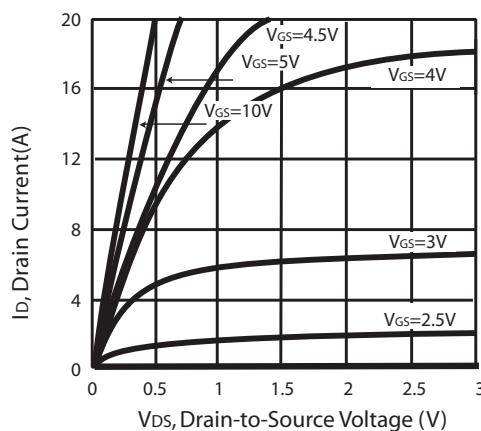


Figure 1. Output Characteristics

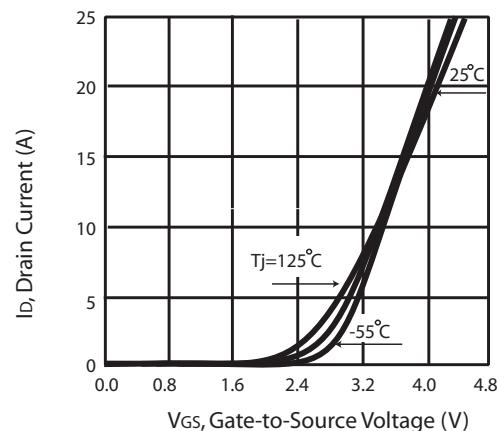


Figure 2. Transfer Characteristics

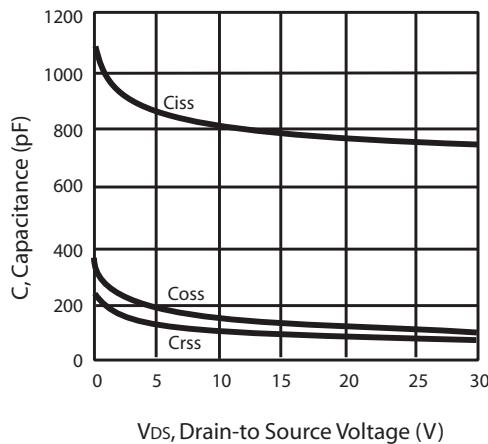


Figure 3. Capacitance

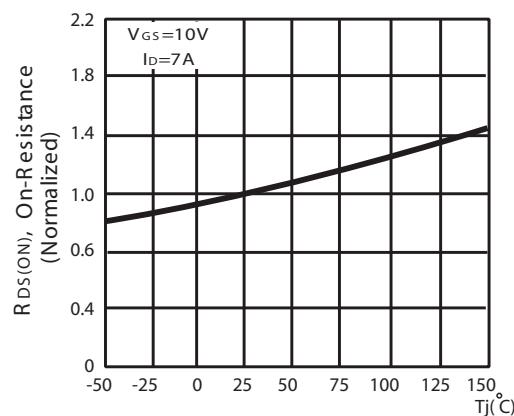


Figure 4. On-Resistance Variation with Drain Current and Temperature

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## N-Channel

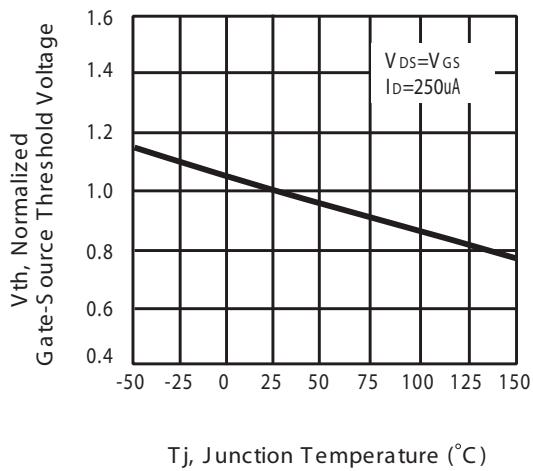


Figure 5. Gate Threshold Variation with Temperature

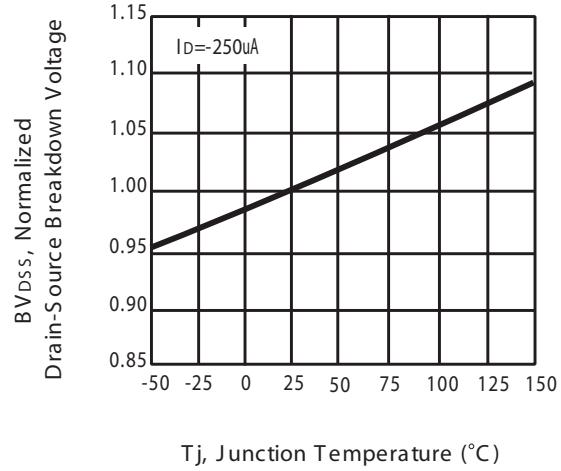


Figure 6. Breakdown Voltage Variation with Temperature

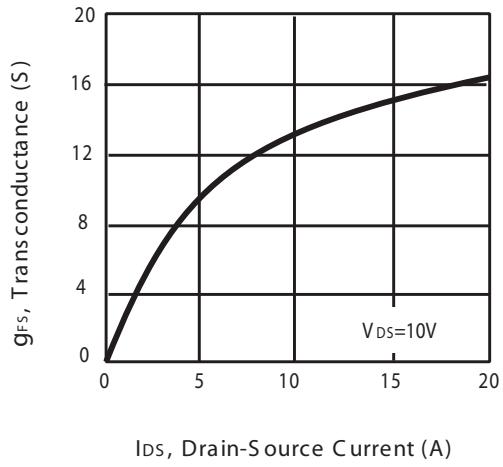


Figure 7. Transconductance Variation with Drain Current

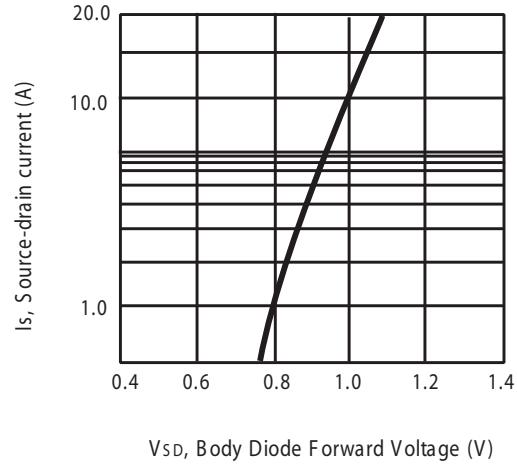


Figure 8. Body Diode Forward Voltage Variation with Source Current

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## P-Channel

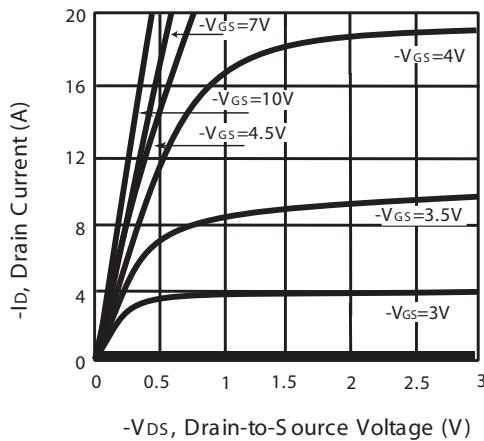


Figure 1. Output Characteristics

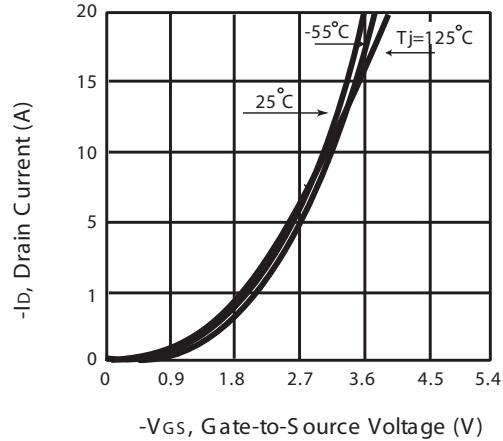


Figure 2. Transfer Characteristics

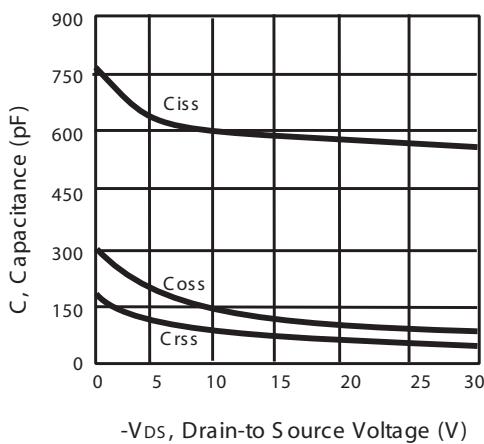


Figure 3. Capacitance

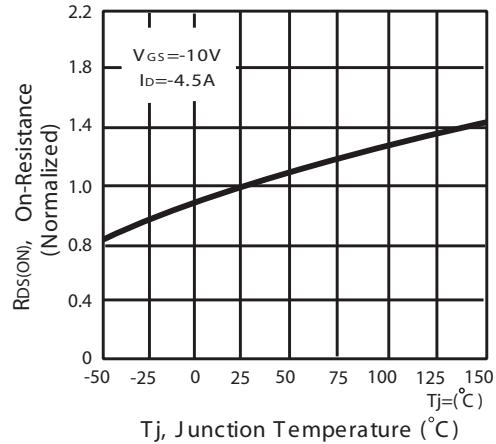


Figure 4. On-Resistance Variation with Temperature

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## P-Channel

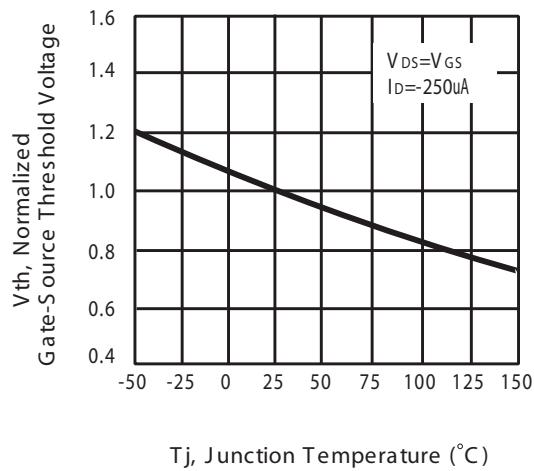


Figure 5. Gate Threshold Variation with Temperature

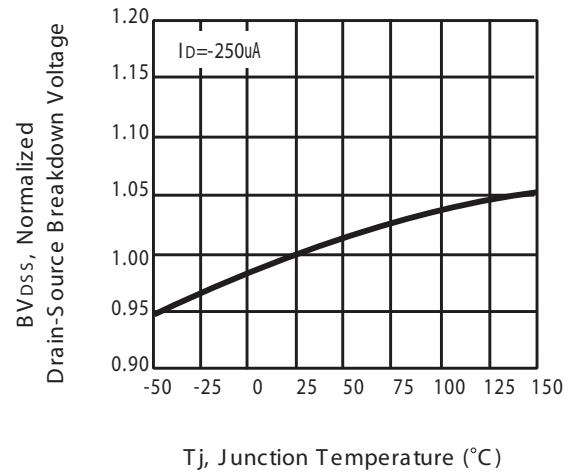


Figure 6. Breakdown Voltage Variation with Temperature

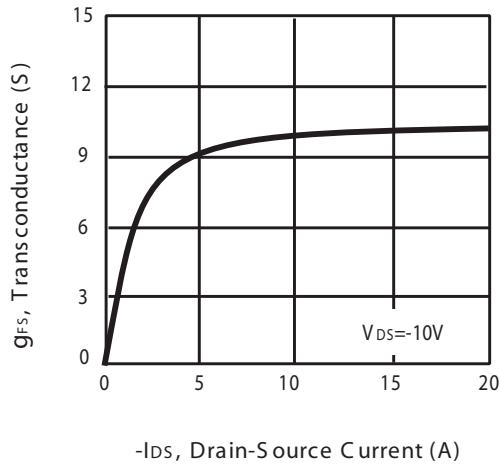


Figure 7. Transconductance Variation with Drain Current

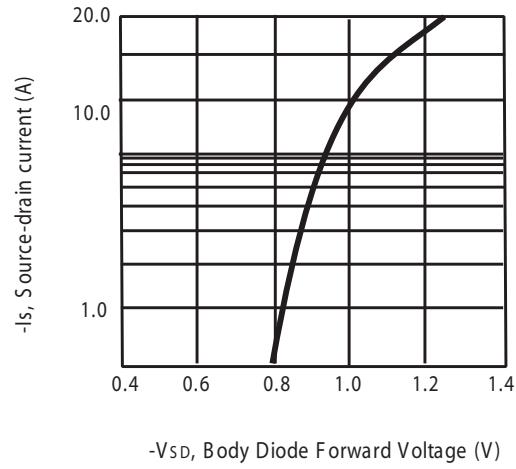


Figure 8. Body Diode Forward Voltage Variation with Source Current

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## N-Channel

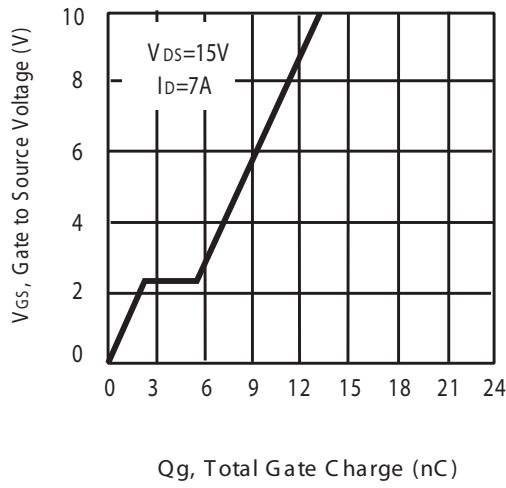


Figure 9. Gate Charge

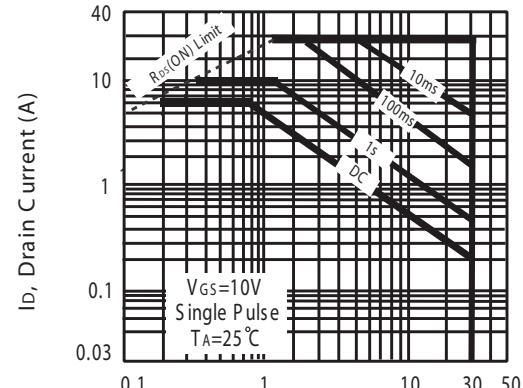


Figure 10. Maximum Safe Operating Area

## P-Channel

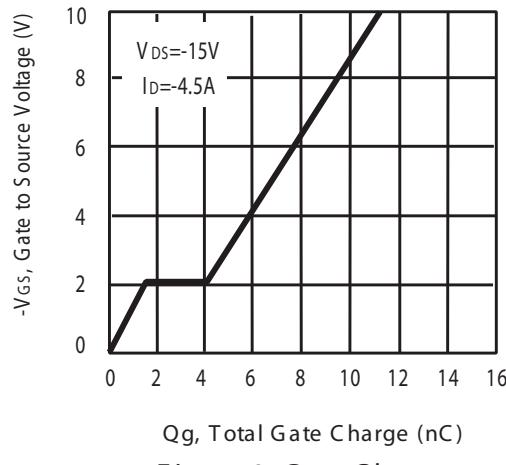


Figure 9. Gate Charge

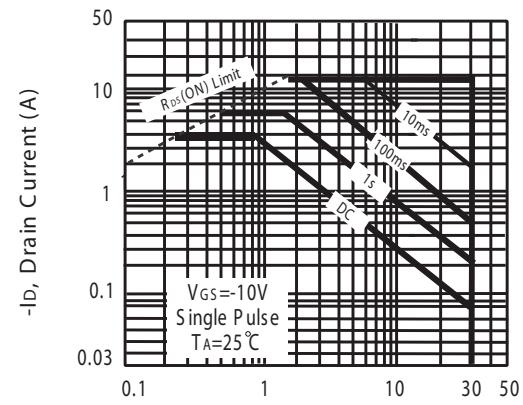


Figure 10. Maximum Safe Operating Area

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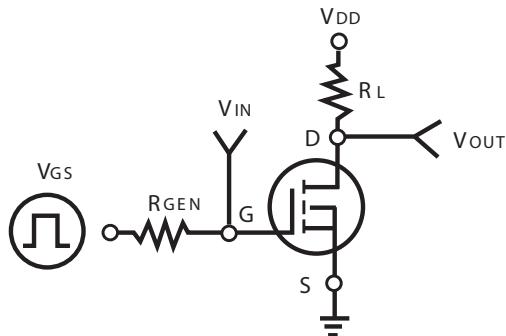


Figure 11. S switching Test Circuit

N-Channel

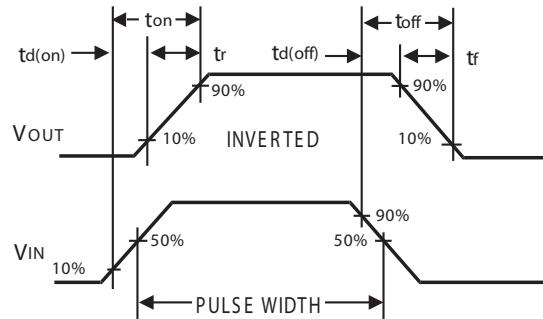
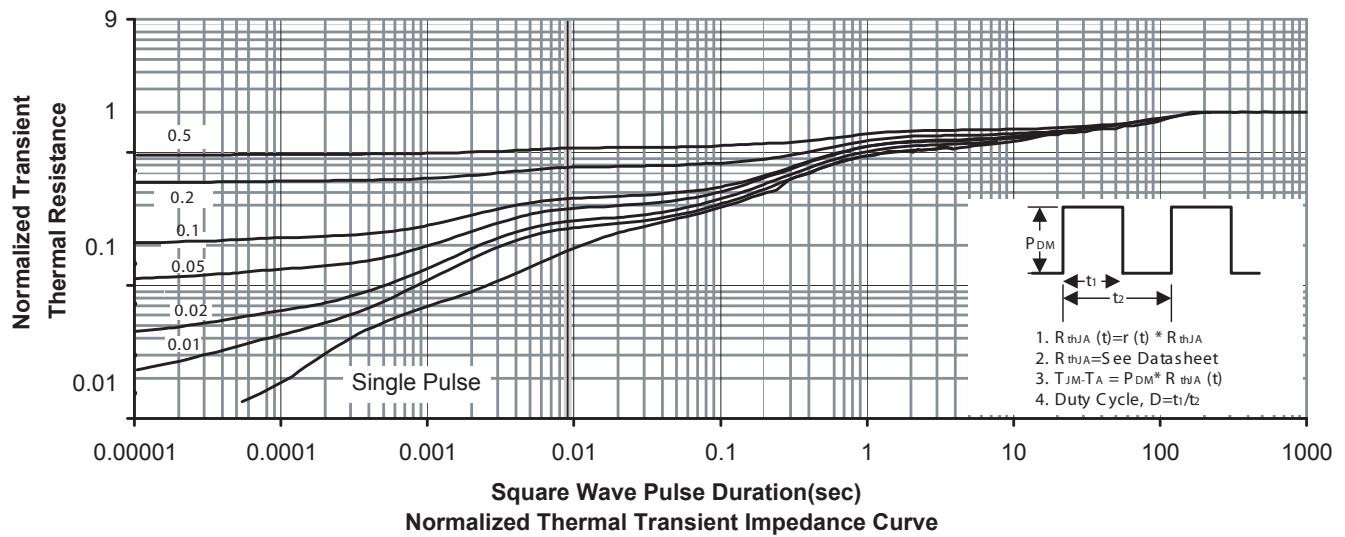
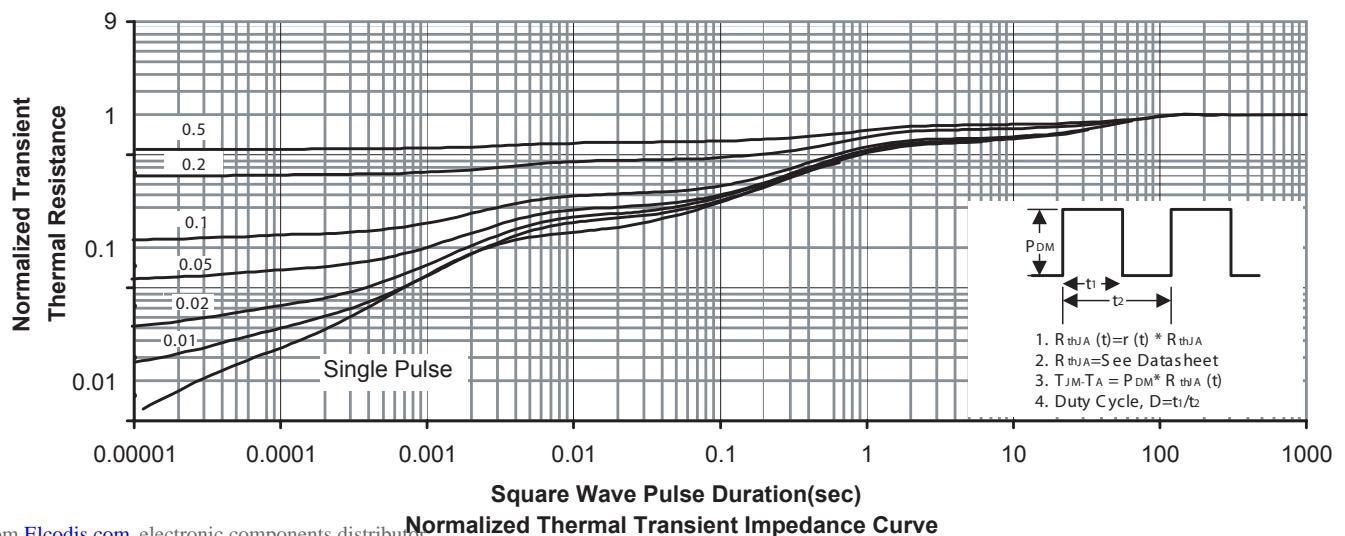
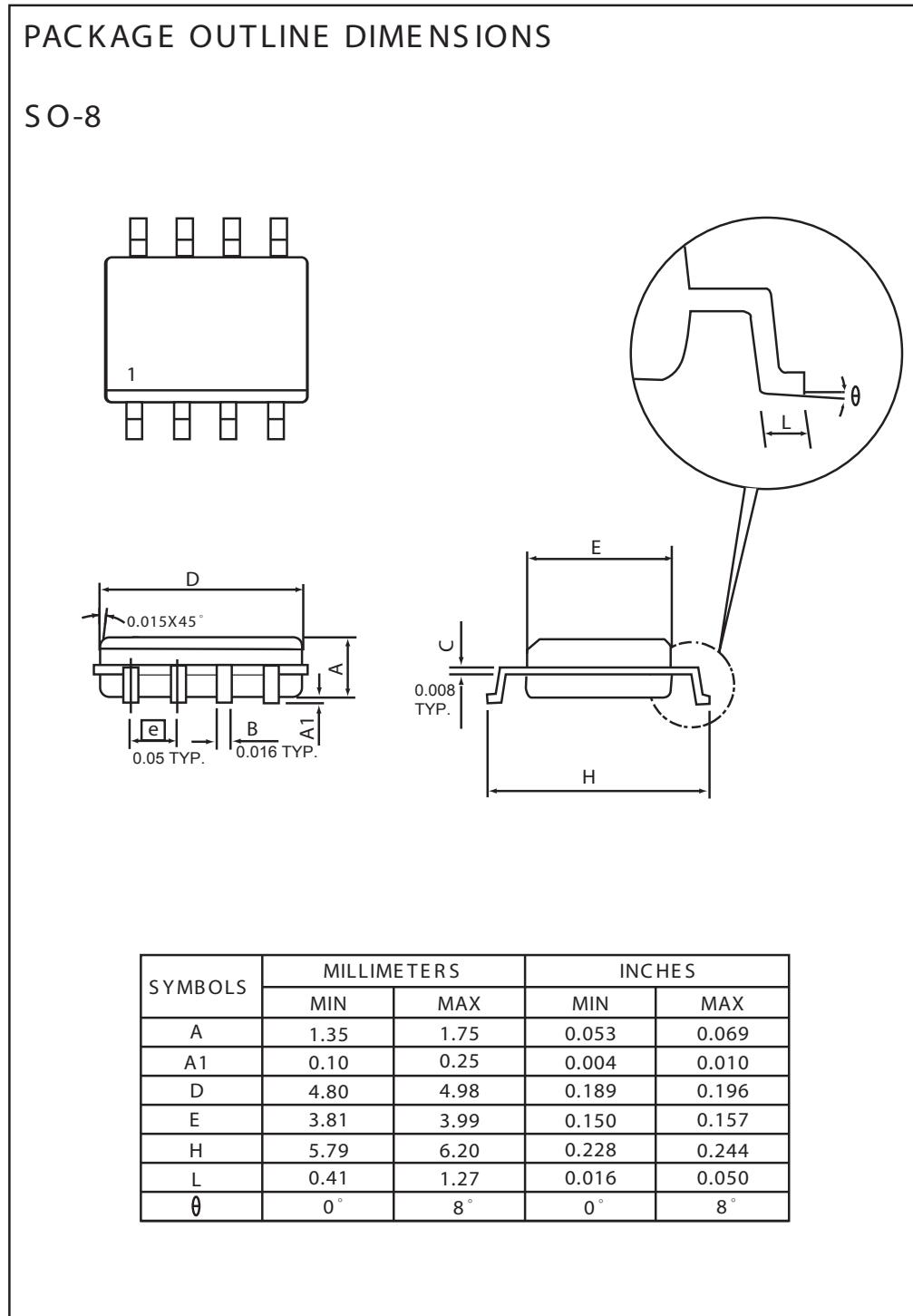


Figure 12. S switching Waveforms

P-Channel



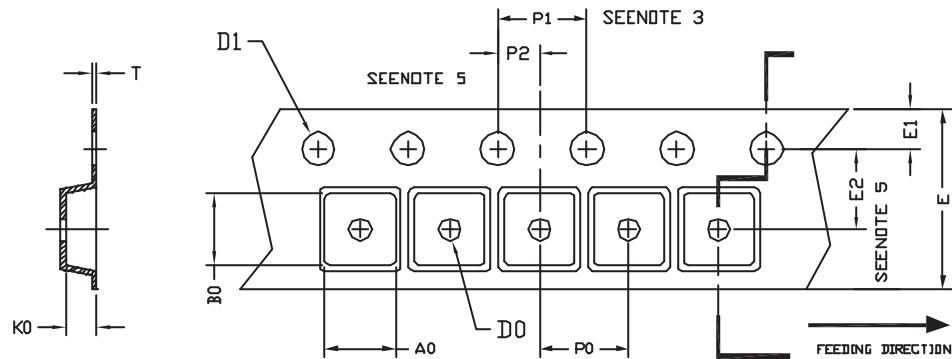
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## SO-8 Tape and Reel Data

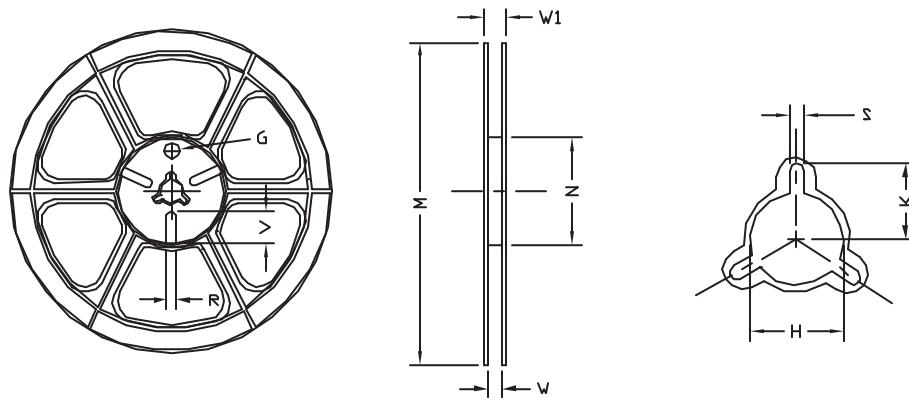
### SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.40	5.20	2.10	$\phi 1.5$ (MIN)	$\phi 1.5$ $+ 0.1$ $- 0.0$	12.0 $\pm 0.3$	1.75	5.5 $\pm 0.05$	8.0	4.0	$2.0$ $\pm 0.05$	$0.3$ $\pm 0.05$

### SO-8 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	330 $\pm 1$	62 $\pm 1.5$	12.4 $+ 0.2$	16.8 $- 0.4$	$\phi 12.75$ $+ 0.15$	---	2.0 $\pm 0.15$	---	---	---