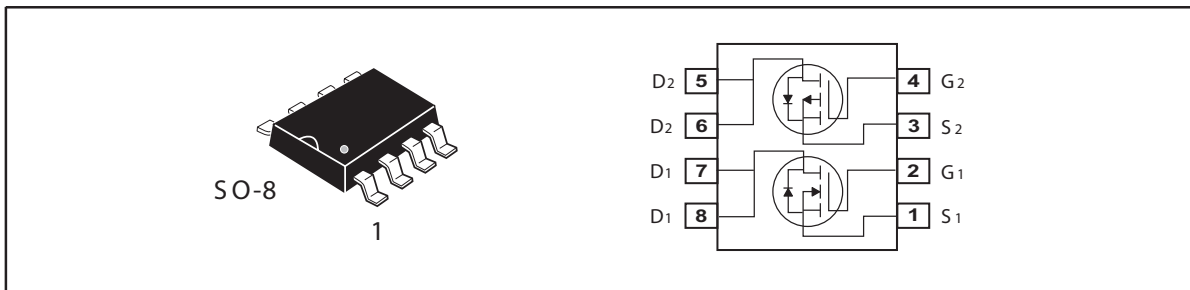




## Dual Enhancement Mode Field Effect Transistor ( N and P Channel )

| PRODUCT SUMMARY (N-Channel) |                |                              |
|-----------------------------|----------------|------------------------------|
| V <sub>DSS</sub>            | I <sub>D</sub> | R <sub>DS(ON)</sub> (mΩ) Max |
| 30V                         | 5.3A           | 46 @ V <sub>GS</sub> =10V    |
|                             |                | 65 @ V <sub>GS</sub> =4.5V   |

| PRODUCT SUMMARY (P-Channel) |                |                              |
|-----------------------------|----------------|------------------------------|
| V <sub>DSS</sub>            | I <sub>D</sub> | R <sub>DS(ON)</sub> (mΩ) Max |
| -30V                        | -4.7A          | 56 @ V <sub>GS</sub> =-10V   |
|                             |                | 90 @ V <sub>GS</sub> =-4.5V  |



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

| Symbol                            | Parameter  | N-Channel            | P-Channel | Units |   |
|-----------------------------------|--|----------------------|-----------|-------|---|
| V <sub>DS</sub>                   | Drain-Source Voltage                             | 30                   | -30       | V     |   |
| V <sub>GS</sub>                   | Gate-Source Voltage                              | ±20                  | ±20       | V     |   |
| I <sub>D</sub>                    | Drain Current-Continuous <sup>a</sup>            | T <sub>A</sub> =25°C | 5.3       | -4.7  | A |
|                                   |  | T <sub>A</sub> =70°C | 4.2       | -3.8  | A |
| I <sub>DM</sub>                   | -Pulsed <sup>b</sup>                             | 19                   | -17       | A     |   |
| P <sub>D</sub>                    | Maximum Power Dissipation <sup>a</sup>           | T <sub>A</sub> =25°C | 2.0       | W     |   |
|                                   |  | T <sub>A</sub> =70°C | 1.28      | W     |   |
| T <sub>J</sub> , T <sub>STG</sub> | Operating Junction and Storage Temperature Range | -55 to 150           |           | °C    |   |

### THERMAL CHARACTERISTICS

|                  |  |      |      |
|------------------|--|------|------|
| R <sub>θJA</sub> | Thermal Resistance, Junction-to-Ambient <sup>a</sup> | 62.5 | °C/W |
|------------------|--|------|------|

# STM8300

Ver 1.0

## N-Channel ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

| Symbol  | Parameter   | Conditions  | Min | Typ  | Max  | Units |
|---|---|---|-----|------|------|-------|
| <b>OFF CHARACTERISTICS</b>                    |   |   |     |      |      |       |
| BV <sub>DSS</sub>                             | Drain-Source Breakdown Voltage                        | V <sub>GS</sub> =0V , I <sub>D</sub> =250uA                       | 30  |      |      | V     |
| I <sub>DSS</sub>                              | Zero Gate Voltage Drain Current                       | V <sub>DS</sub> =24V , V <sub>GS</sub> =0V                        |     |      | 1    | uA    |
| I <sub>GSS</sub>                              | Gate-Body Leakage Current                             | V <sub>GS</sub> = ±20V , V <sub>DS</sub> =0V                      |     |      | ±100 | nA    |
| <b>ON CHARACTERISTICS</b>                     |   |   |     |      |      |       |
| V <sub>GS(th)</sub>                           | Gate Threshold Voltage                                | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA          | 1   | 1.6  | 3    | V     |
| R <sub>DS(ON)</sub>                           | Drain-Source On-State Resistance                      | V <sub>GS</sub> =10V , I <sub>D</sub> =5.3A                       |     | 38   | 46   | m ohm |
|   |   | V <sub>GS</sub> =4.5V , I <sub>D</sub> =4.5A                      |     | 48   | 65   | m ohm |
| g <sub>FS</sub>                               | Forward Transconductance                              | V <sub>DS</sub> =5V , I <sub>D</sub> =5.3A                        |     | 10   |      | S     |
| <b>DYNAMIC CHARACTERISTICS <sup>c</sup></b>   |   |   |     |      |      |       |
| C <sub>ISS</sub>                              | Input Capacitance                                     | V <sub>DS</sub> =15V, V <sub>GS</sub> =0V<br>f=1.0MHz             |     | 310  |      | pF    |
| C <sub>OSS</sub>                              | Output Capacitance                                    |   |     | 73   |      | pF    |
| C <sub>RSS</sub>                              | Reverse Transfer Capacitance                          |   |     | 44   |      | pF    |
| <b>SWITCHING CHARACTERISTICS <sup>c</sup></b> |   |   |     |      |      |       |
| t <sub>D(ON)</sub>                            | Turn-On Delay Time                                    | V <sub>DD</sub> =15V<br>I <sub>D</sub> =1A                        |     | 7.5  |      | ns    |
| t <sub>r</sub>                                | Rise Time   |   |     | 9.5  |      | ns    |
| t <sub>D(OFF)</sub>                           | Turn-Off Delay Time                                   | V <sub>GS</sub> =10V<br>R <sub>GEN</sub> =6 ohm                   |     | 16   |      | ns    |
| t <sub>f</sub>                                | Fall Time   |   |     | 13   |      | ns    |
| Q <sub>g</sub>                                | Total Gate Charge                                     | V <sub>DS</sub> =15V, I <sub>D</sub> =5.3A, V <sub>GS</sub> =10V  |     | 5.3  |      | nC    |
|   |   | V <sub>DS</sub> =15V, I <sub>D</sub> =5.3A, V <sub>GS</sub> =4.5V |     | 2.8  |      | nC    |
| Q <sub>gs</sub>                               | Gate-Source Charge                                    | V <sub>DS</sub> =15V, I <sub>D</sub> =5.3A,                       |     | 0.9  |      | nC    |
| Q <sub>gd</sub>                               | Gate-Drain Charge                                     | V <sub>GS</sub> =10V  |     | 1.2  |      | nC    |
| <b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>     |   |   |     |      |      |       |
| I <sub>S</sub>                                | Maximum Continuous Drain-Source Diode Forward Current |   |     |      | 1    | A     |
| V <sub>SD</sub>                               | Diode Forward Voltage <sup>b</sup>                    | V <sub>GS</sub> =0V, I <sub>S</sub> =1A                           |     | 0.79 | 1.2  | V     |

Jul,31,2008

# STM8300

Ver 1.0

## P-Channel ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

| Symbol   | Parameter   | Conditions   | Min  | Typ   | Max  | Units |    |
|--|---|--|------|-------|------|-------|----|
| <b>OFF CHARACTERISTICS</b>   |   |  |      |       |      |       |    |
| BV <sub>DSS</sub>  | Drain-Source Breakdown Voltage                        | V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA                           | -30  |       |      | V     |    |
| I <sub>DSS</sub>   | Zero Gate Voltage Drain Current                       | V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V                            |      |       | -1   | uA    |    |
| I <sub>GSS</sub>   | Gate-Body Leakage Current                             | V <sub>GS</sub> = ±20V , V <sub>DS</sub> =0V                           |      |       | ±100 | nA    |    |
| <b>ON CHARACTERISTICS</b>  |   |  |      |       |      |       |    |
| V <sub>GS(th)</sub>  | Gate Threshold Voltage                                | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA              | -1.0 | -1.8  | -3.0 | V     |    |
| R <sub>DS(ON)</sub>  | Drain-Source On-State Resistance                      | V <sub>GS</sub> =-10V , I <sub>D</sub> =-4.7A                          |      | 46    | 56   | m ohm |    |
|  |   | V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-3.7A                         |      | 68    | 90   | m ohm |    |
| g <sub>FS</sub>  | Forward Transconductance                              | V <sub>DS</sub> =-5V , I <sub>D</sub> =-4.7A                           |      | 7.5   |      | S     |    |
| <b>DYNAMIC CHARACTERISTICS <sup>c</sup></b>  |   |  |      |       |      |       |    |
| C <sub>ISS</sub>   | Input Capacitance                                     | V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V<br>f=1.0MHz                 |      | 520   |      | pF    |    |
| C <sub>OSS</sub>   | Output Capacitance                                    |  |      |       | 125  |       | pF |
| C <sub>RSS</sub>   | Reverse Transfer Capacitance                          |  |      |       | 78   |       | pF |
| <b>SWITCHING CHARACTERISTICS <sup>c</sup></b>  |   |  |      |       |      |       |    |
| t <sub>D(ON)</sub>   | Turn-On Delay Time                                    | V <sub>DD</sub> =-15V<br>I <sub>D</sub> =-1A                           |      | 7.5   |      | ns    |    |
| t <sub>r</sub>   | Rise Time   |  |      |       | 12.4 |       | ns |
| t <sub>D(OFF)</sub>  | Turn-Off Delay Time                                   | V <sub>GS</sub> =-10V<br>R <sub>GEN</sub> =6 ohm                       |      | 62    |      | ns    |    |
| t <sub>f</sub>   | Fall Time   |  |      |       | 37   |       | ns |
| Q <sub>g</sub>   | Total Gate Charge                                     | V <sub>DS</sub> =-15V, I <sub>D</sub> =-4.7A, V <sub>GS</sub> =-10V    |      | 10.3  |      | nC    |    |
|  |   | V <sub>DS</sub> =-15V, I <sub>D</sub> =-4.7A, V <sub>GS</sub> =-4.5V   |      | 5.2   |      | nC    |    |
| Q <sub>gs</sub>  | Gate-Source Charge                                    | V <sub>DS</sub> =-15V, I <sub>D</sub> =-4.7A,<br>V <sub>GS</sub> =-10V |      | 1.1   |      | nC    |    |
| Q <sub>gd</sub>  | Gate-Drain Charge                                     |  |      |       | 2.8  |       | nC |
| <b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>  |   |  |      |       |      |       |    |
| I <sub>S</sub>   | Maximum Continuous Drain-Source Diode Forward Current |  |      |       | -1   | A     |    |
| V <sub>SD</sub>  | Diode Forward Voltage <sup>b</sup>                    | V <sub>GS</sub> =0V, I <sub>S</sub> =-1A                               |      | -0.77 | -1.2 | V     |    |
| <b>Notes</b><br>a.Surface Mounted on FR4 Board, t ≤ 10sec.<br>b.Pulse Test:Pulse Width ≤ 300us, Duty Cycle ≤ 2%.<br>c.Guaranteed by design, not subject to production testing. |   |  |      |       |      |       |    |

Jul,31,2008

## N-Channel

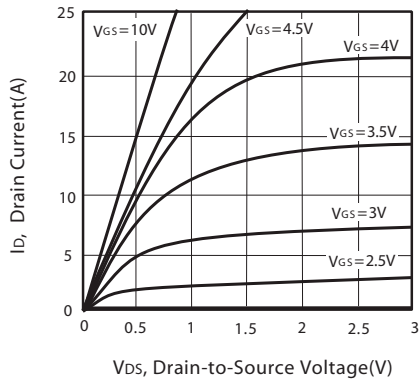


Figure 1. Output Characteristics

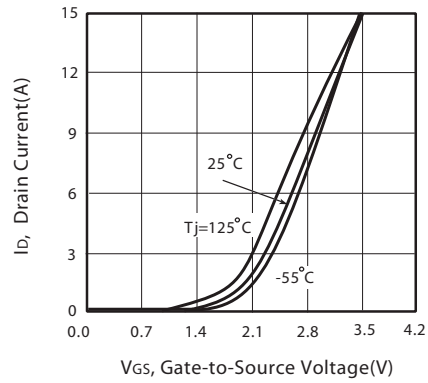


Figure 2. Transfer Characteristics

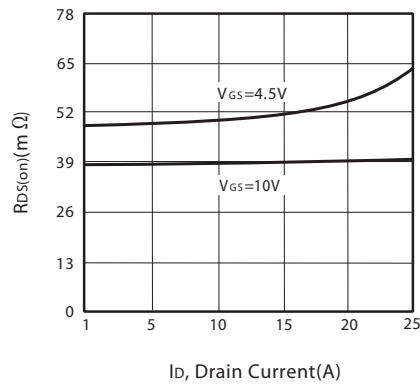


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

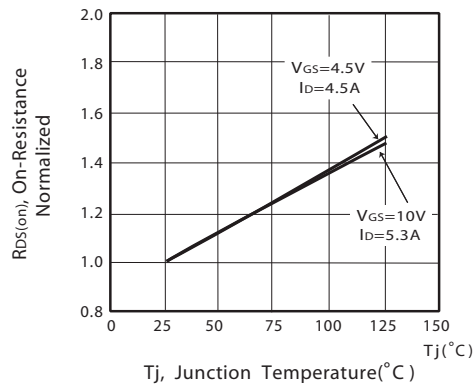


Figure 4. On-Resistance Variation with Drain Current and Temperature

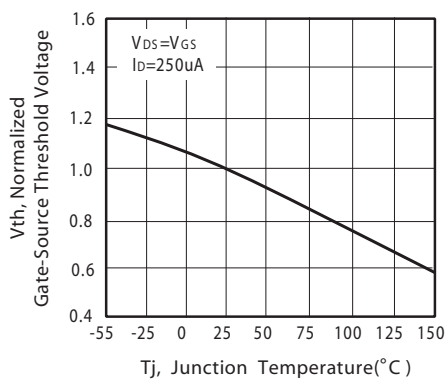


Figure 5. Gate Threshold Variation with Temperature

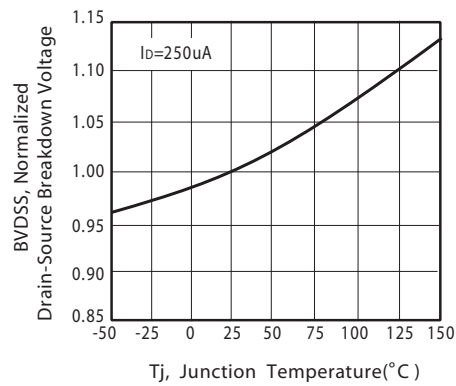


Figure 6. Breakdown Voltage Variation with Temperature

Jul,31,2008

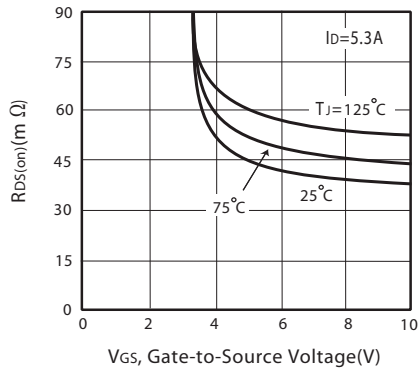


Figure 7. On-Resistance vs. Gate-Source Voltage

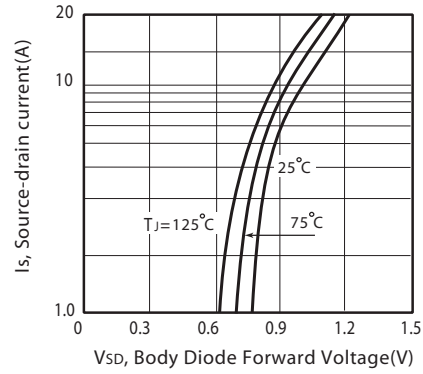


Figure 8. Body Diode Forward Voltage Variation with Source Current

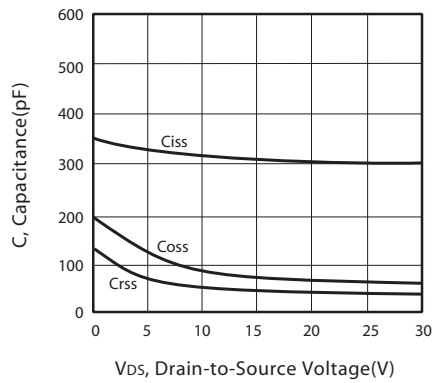


Figure 9. Capacitance

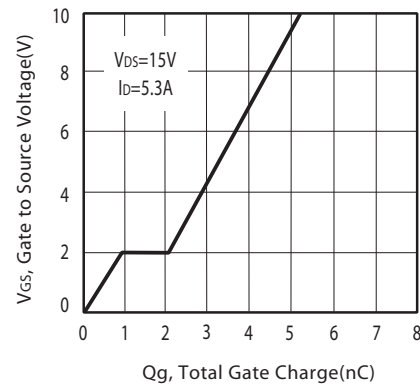


Figure 10. Gate Charge

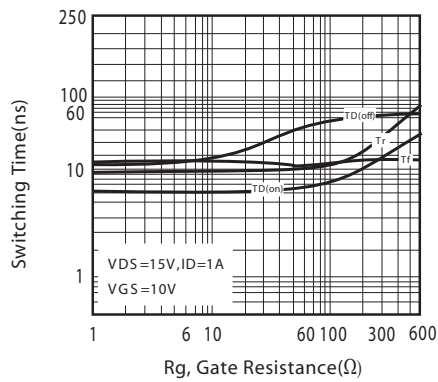


Figure 11. switching characteristics

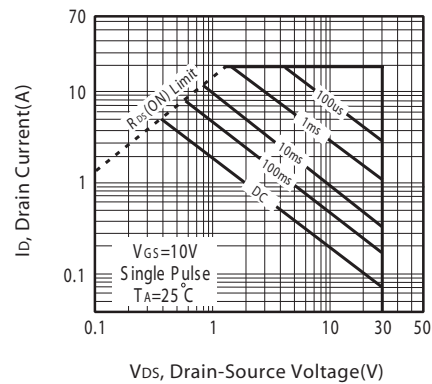
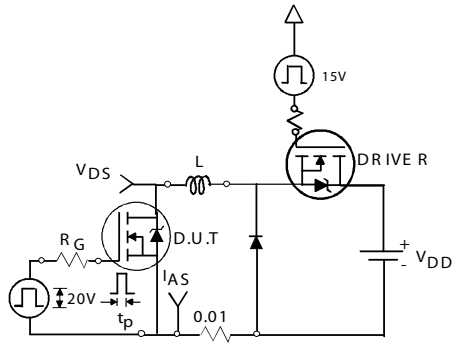
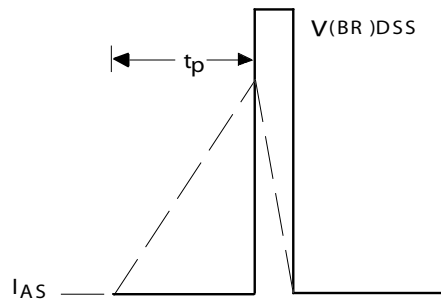


Figure 12. Maximum Safe Operating Area



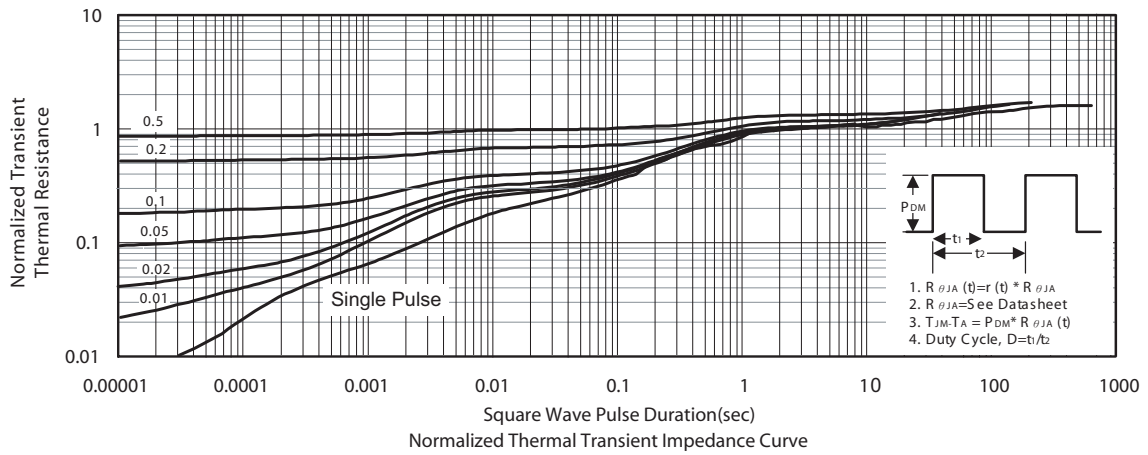
Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.



## P-Channel

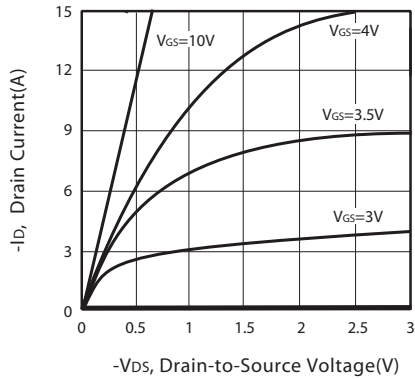


Figure 1. Output Characteristics

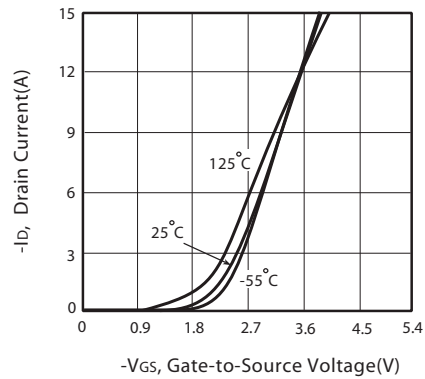


Figure 2. Transfer Characteristics

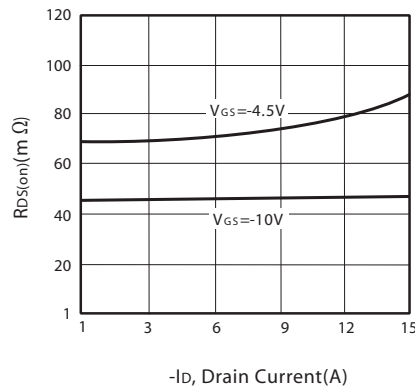


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

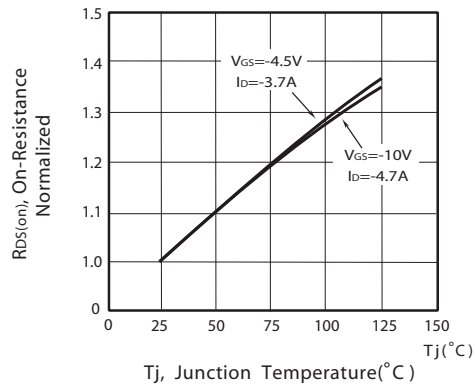


Figure 4. On-Resistance Variation with Drain Current and Temperature

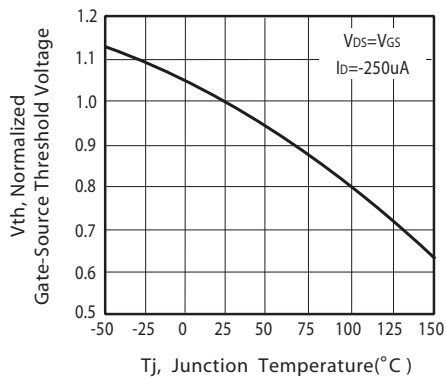


Figure 5. Gate Threshold Variation with Temperature

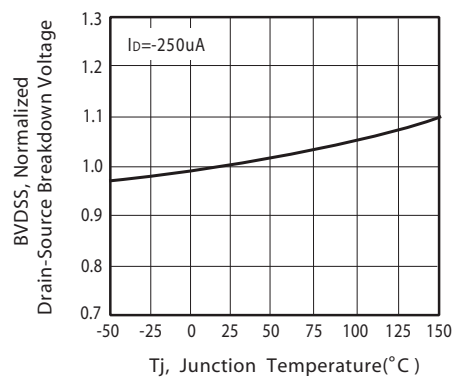
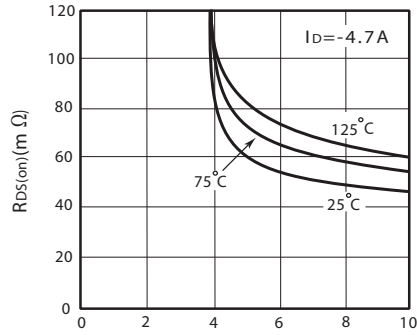
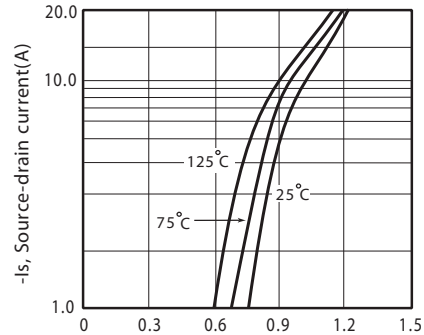


Figure 6. Breakdown Voltage Variation with Temperature



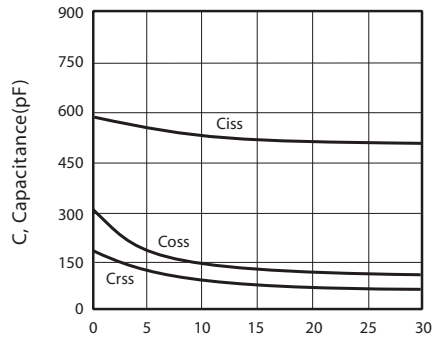
-VGS, Gate-to-Source Voltage(V)

Figure 7. On-Resistance vs. Gate-Source Voltage



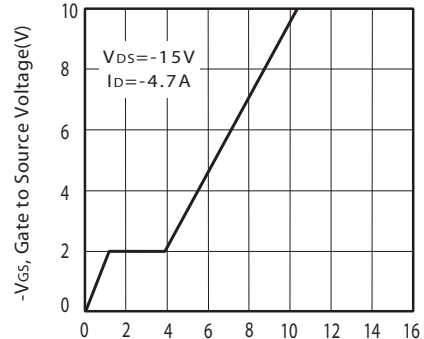
-VSD, Body Diode Forward Voltage(V)

Figure 8. Body Diode Forward Voltage Variation with Source Current



VDS, Drain-to-Source Voltage(V)

Figure 9. Capacitance



Qg, Total Gate Charge(nC)

Figure 10. Gate Charge

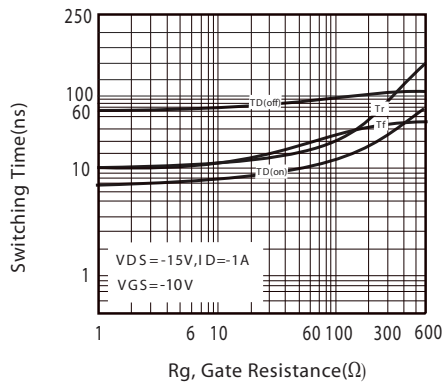


Figure 11. switching characteristics

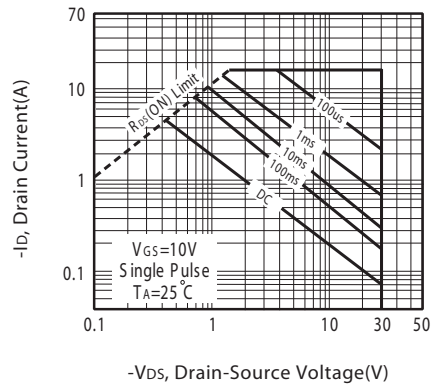
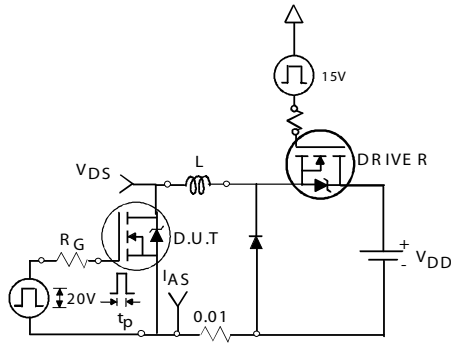


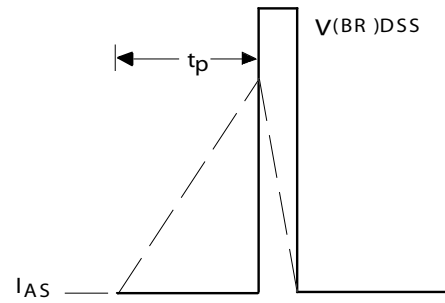
Figure 12. Maximum Safe Operating Area





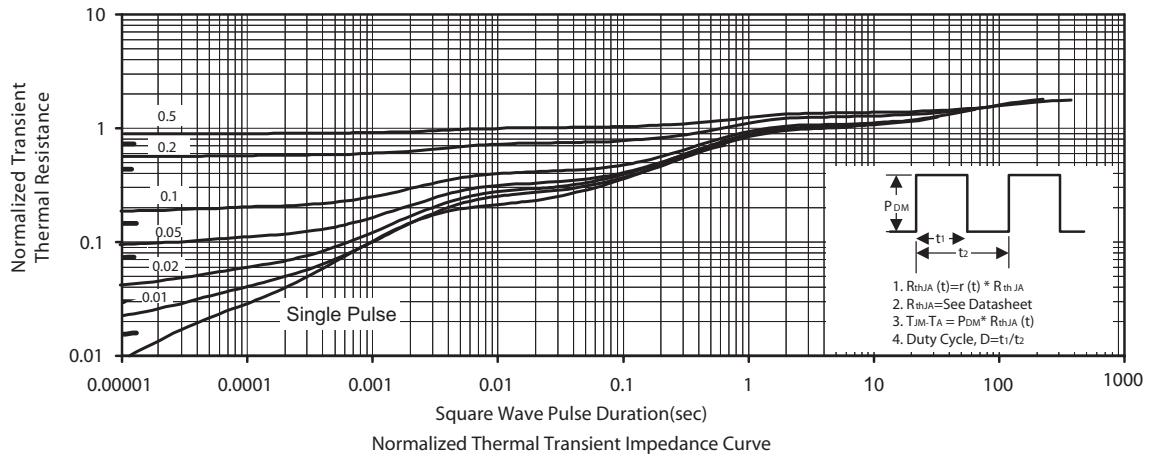
Unclamped Inductive Test Circuit

Figure 13a.



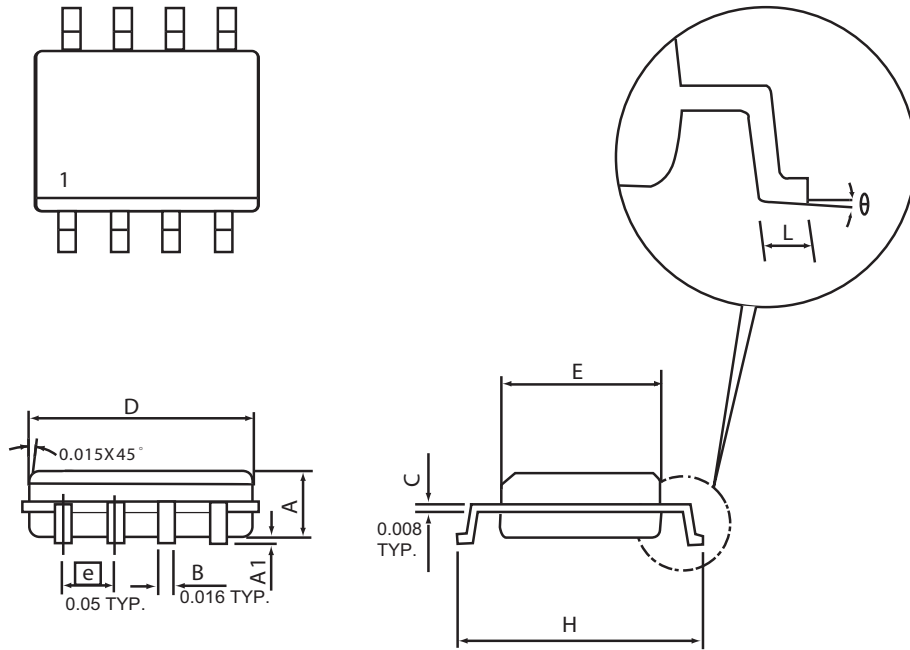
Unclamped Inductive Waveforms

Figure 13b.



## PACKAGE OUTLINE DIMENSIONS

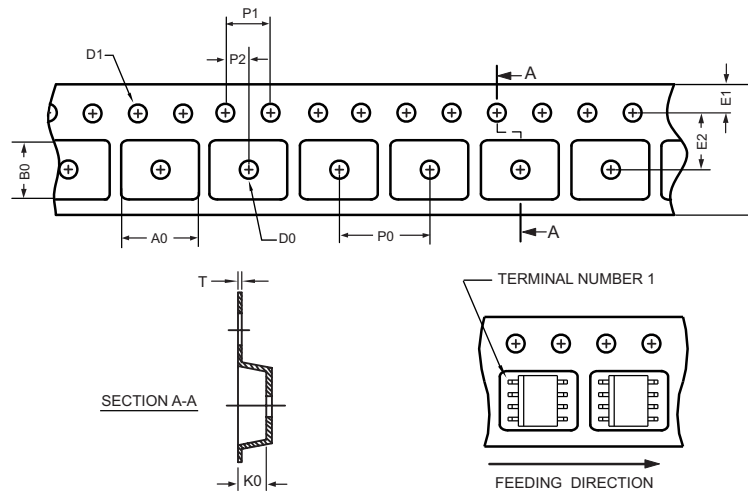
### SO-8



| SYMBOLS | MILLIMETERS |      | INCHES |       |
|---------|-------------|------|--------|-------|
|         | MIN         | MAX  | MIN    | MAX   |
| A       | 1.35        | 1.75 | 0.053  | 0.069 |
| A1      | 0.10        | 0.25 | 0.004  | 0.010 |
| D       | 4.80        | 4.98 | 0.189  | 0.196 |
| E       | 3.81        | 3.99 | 0.150  | 0.157 |
| H       | 5.79        | 6.20 | 0.228  | 0.244 |
| L       | 0.41        | 1.27 | 0.016  | 0.050 |
| θ       | 0°          | 8°   | 0°     | 8°    |

## SO-8 Tape and Reel Data

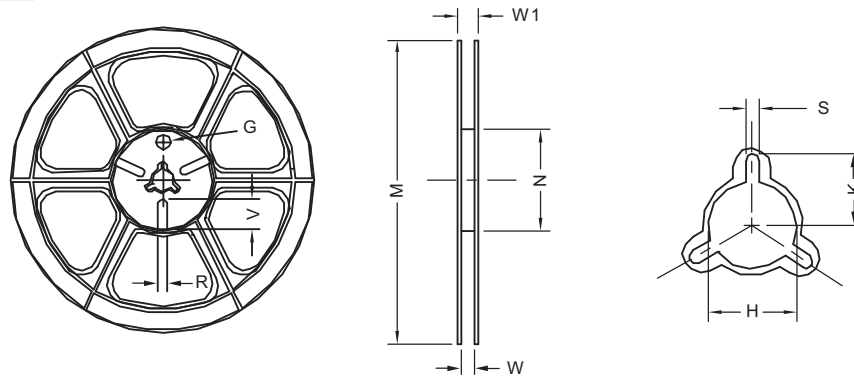
### SO-8 Carrier Tape



unit:mm

| PACKAGE          | A0                 | B0                 | K0                 | D0                  | D1                        | E                     | E1                 | E2                | P0                | P1                | P2                | T                   |
|------------------|--------------------|--------------------|--------------------|---------------------|---------------------------|-----------------------|--------------------|-------------------|-------------------|-------------------|-------------------|---------------------|
| SOP 8N<br>150mil | 6.50<br>$\pm 0.15$ | 5.25<br>$\pm 0.10$ | 2.10<br>$\pm 0.10$ | $\phi$ 1.5<br>(MIN) | $\phi$ 1.55<br>$\pm 0.10$ | 12.0<br>+0.3<br>- 0.1 | 1.75<br>$\pm 0.10$ | 5.5<br>$\pm 0.10$ | 8.0<br>$\pm 0.10$ | 4.0<br>$\pm 0.10$ | 2.0<br>$\pm 0.10$ | 0.30<br>$\pm 0.013$ |

### SO-8 Reel



UNIT:mm

| TAPE SIZE | REEL SIZE  | M              | N               | W            | W1            | H                      | K   | S                 | G   | R   | V   |
|-----------|------------|----------------|-----------------|--------------|---------------|------------------------|-----|-------------------|-----|-----|-----|
| 12 mm     | $\phi$ 330 | 330<br>$\pm 1$ | 62<br>$\pm 1.5$ | 12.4<br>+0.2 | 16.8<br>- 0.4 | $\phi$ 12.75<br>+ 0.15 | --- | 2.0<br>$\pm 0.15$ | --- | --- | --- |