AMC79L05



DESCRIPTION

AMC79L05 is 3-terminal fixed The a negative-voltage designed for a wide range of applications. This regulator can provide local on card regulation, eliminating the distribution problems associated with single point regulation. In addition, it can be used with power-pass elements to make high-current voltage regulators with 100mA output current.

Protection features such as thermal shutdown and current limiting have been designed internally which will protect the device from damage in case of overload or overheating.

100mA / 3-TERMINAL **NEGATIVE VOLTAGE REGULATOR**

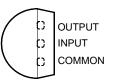
FEATURES

- ±5% tolerance of output voltage
- Wide input range
- Internal thermal overload protection
- Output current > 100mA
- **No External Components**
- Short circuit protection
- Available in 3L plastic TO-92 and plastic 8 pin S.O.I.C.
- Identical pin assignment to earlier 79L05 series.

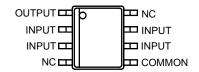
APPLICATIONS

- Logic Systems
- Computer Add-On Cards
- Modem Cards
- Power Suppliers

PACKAGE PIN OUT



3-Pin Plastic TO-92 (Top View)



8-Pin S.O.I.C Surface Mount (Top View)

ORDER INFORMATION						
T (⁰ C)	LP Plastic TO-92	DM Plastic SO-8				
$T_A (^{o}C)$	3-pin	8-pin				
0 to 70	AMC79L05LP	AMC79L05DM				
0 to 70	AMC79L05LP(Lead Free)	AMC79L05DM(Lead Free)				
 Note: 1.All surface-mount and TO-92 packages are available in Tape & Reel. Append the letter "T" to part number (i.e. AMC79L05LPT o AMC79L05DMT). 2.For TO-92 in Tape & Box (without reel), add suffix "TB" (i.e. AMC79L05LPTB). 3.The letter "F" is marked for Lead Free process. 						

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ABSOLUTE MAXIMUM RATINGS (Note 1)							
Input Voltage	-30V						
Operating free-air temperature range, T _A	0° C to 70° C						
Storage temperature range	-65° C to 150° C						
Lead temperature (soldiering, 10 seconds)	260 °C						
Note 1: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. out of the specified terminal.	Currents are positive into, negative						

THERMAL DATA

LP PACKAGE: 156 °C/W Thermal Resistance-Junction to Ambient, θ_{JA} 156 °C/W DM PACKAGE: 165 °C/W Junction Temperature Calculation: $T_J = T_A + (P_D \times \theta_{JA})$. 165 °C/W The θ_{JA} numbers are guidelines for the thermal performance of the device/pc-board system. All of the above assume no ambient airflow.

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RECOMMENDED OPERATING CONDITIONS							
Deremeter	Symbol	Recommended Operating Conditions			TT		
Parameter		Min.	Тур.	Max.	Units		
Input Voltage	VI	-7		-20	V		
Output Current	I _{OUT}			100	mA		
Operating Virtual Junction Temperature	T _J	0		125	°C		

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, these specifications in **bold type** apply over the operating temperature range of $0^{\circ}C \le T_J \le +150^{\circ}C$, $V_{IN} = -10V$, $I_{OUT} = 40$ mA, $C_{IN} = 0.33\mu$ F, $C_{OUT} = 0.1\mu$ F, and are for DC characteristics only. (Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

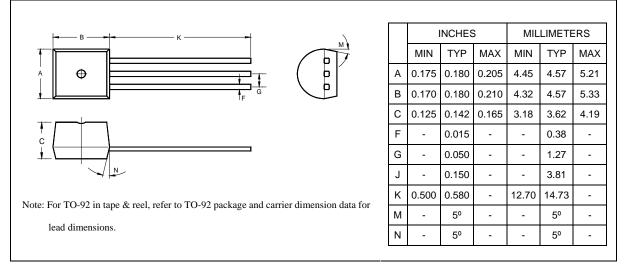
Demonstern	Symbol	Test Carditians	AMC79L05			T.L. M.	
Parameter		Test Conditions	Min.	Тур.	Max.	Units	
		$T_J = 25 \ ^{\circ}C$	-4.8	-5.0	-5.2		
Output Voltage	V _{OUT}	$-7V \le V_{IN} \le -20V, \ 1mA \le I_{OUT} \le 40mA$	-4.75		-5.25	V	
		$1 \text{mA} \le I_{\text{OUT}} \le 70 \text{mA}$	-4.75		-5.25		
Line Deculation	$\triangle_{V_{OI}}$	$-7V \le V_{IN} \le -20V, T_J = 25 \ ^{o}C$		15	150	mV	
Line Regulation		$-8V \le V_{IN} \le -20V, T_J = 25 \ ^{o}C$			100		
Load Dogulation	ΔV_{OL}	$1\text{mA} \le I_{\text{OUT}} \le 100\text{mA}, \text{T}_{\text{J}} = 25 ^{\circ}\text{C}$		20	60	mV	
Load Regulation		$1 \text{mA} \le I_{\text{OUT}} \le 40 \text{mA}, \text{T}_{\text{J}} = 25 ^{\circ}\text{C}$		10	30		
Peak Output Current	I _{PEAK}	$T_J = 25 \ ^{\circ}C$		140		mA	
Dropout Voltage		$T_J = 25 \ ^{\circ}C$		1.7		V	
Quiacaant Cumant	I _Q	$T_J = 25 \ ^{\circ}C$			6.6	mA	
Quiescent Current		$T_J = 125 \ ^{\mathrm{o}}\mathrm{C}$			6.0		
Oniogoant Change Change	$ riangle I_Q$	$-8V \le V_{IN} \le -20V$			1.5	mA	
Quiescent Current Change		$1mA \leq I_{OUT} \leq 40mA$			0.1		
Ripple Rejection (note 2)	R _R	f =120Hz, -8V $\leq V_{IN} \leq$ -18V, T _J = 25 °C	41	49		dB	
Output Noise Voltage (note 2)	V_{ORMS}	$10Hz \leq f \leq 100KHz, \ T_J = 25 \ ^{\rm o}C$		40		μV	

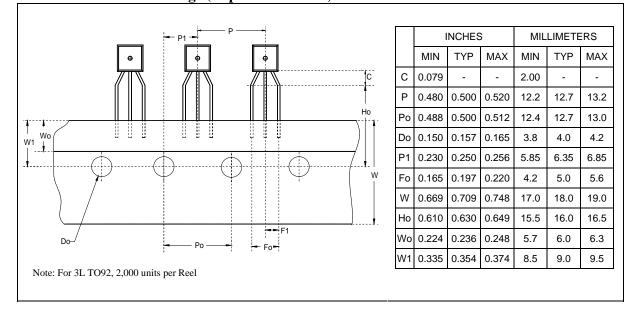
These parameters, although guaranteed, are not 100% tested in production prior to shipment Note 2:

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PACKAGE

3-Pin Plastic TO-92

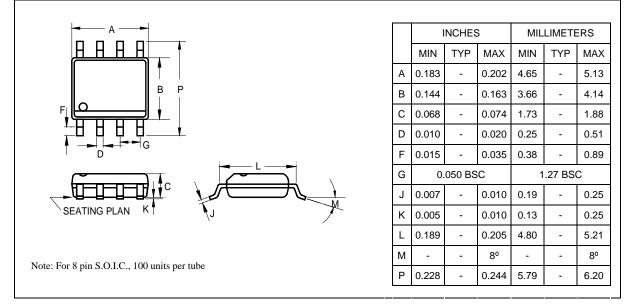




3-Pin Plastic TO-92 Package (Taped and Reeled) and Carrier Dimensions

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8-Pin Plastic S.O.I.C.





IMPORTANT NOTICE

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