

# DATA SHEET

## **CBT6800**

10-bit bus switch with precharged outputs  
for live insertion

Product specification  
Supersedes data of 1999 Mar 18

1999 Oct 28

# 10-bit bus switch with precharged outputs for live insertion

## CBT6800

### FEATURES

- 5  $\Omega$  switch connection between two ports
- TTL compatible input and output levels
- Outputs are precharged by bias voltage to minimize signal distortion during live insertion
- Latch-up protection exceeds 100 mA per JESD78
- ESD protection exceeds 2000 V HBM per JESD22-A114, 200 V MM per JESD22-A115 and 1000 V CDM per JESD22-C101

### DESCRIPTION

The CBT6800 provides ten bits of high-speed TTL-compatible bus switching. The low on-state resistance of the switch allows bi-directional connections to be made while adding near-zero propagation delay. The device also precharges the B port to a user-selectable bias voltage (BIASV) to minimize live-insertion noise.

The CBT6800 is organized as one 10-bit switch with a single enable (ON) input. When  $\overline{ON}$  is low, the switch is on and port A is connected to port B. When  $\overline{ON}$  is high, the switch between port A and port B is open and the B port is precharged to BIASV through the equivalent of a 10 k $\Omega$  resistor.

The CBT6800 is characterized for operation from  $-40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ .

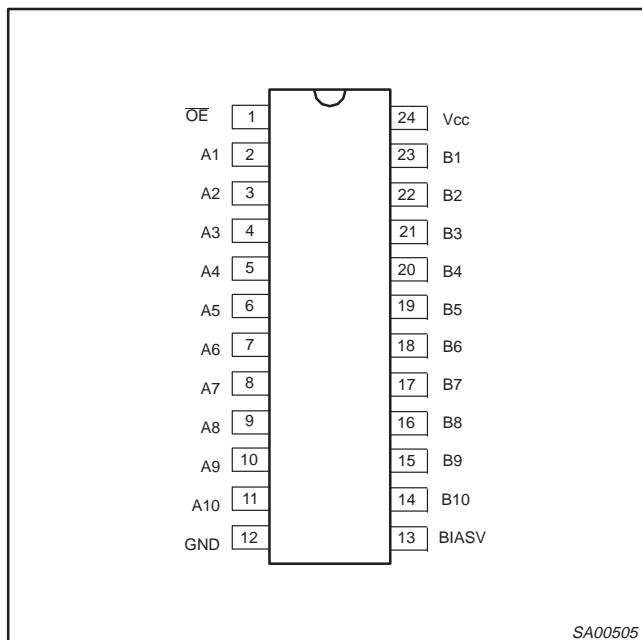
### QUICK REFERENCE DATA

| SYMBOL                               | PARAMETER                                 | CONDITIONS<br>$T_{\text{amb}} = 25^{\circ}\text{C}; \text{GND} = 0 \text{ V}$ | TYPICAL | UNIT          |
|--------------------------------------|---|---|---------|---------------|
| $t_{\text{PLH}}$<br>$t_{\text{PHL}}$ | Propagation delay<br>An to Bn or Bn to An | $C_L = 50 \text{ pF}; V_{\text{CC}} = 5 \text{ V}$                            | 250     | ps            |
| $C_{\text{IN}}$                      | Input capacitance – control pin           | $V_I = 0 \text{ V}$ or $V_{\text{CC}}$  | 3.5     | pF            |
| $C_{\text{OUT}}$                     | Output capacitance – I/O pins             | Outputs disabled; $V_O = 0 \text{ V}$ or $V_{\text{CC}}$                      | 8.2     | pF            |
| $I_{\text{CCZ}}$                     | Total supply current                      | Outputs disabled; $V_{\text{CC}} = 5.5 \text{ V}$                             | 1       | $\mu\text{A}$ |

### ORDERING INFORMATION

| PACKAGES                    | TEMPERATURE RANGE                              | ORDER CODE    | DWG NUMBER |
|-----------------------------|--|---------------|------------|
| 24-Pin Plastic TSSOP Type I | $-40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ | CBT6800 PW DH | SOT355-1   |

### PIN CONFIGURATION



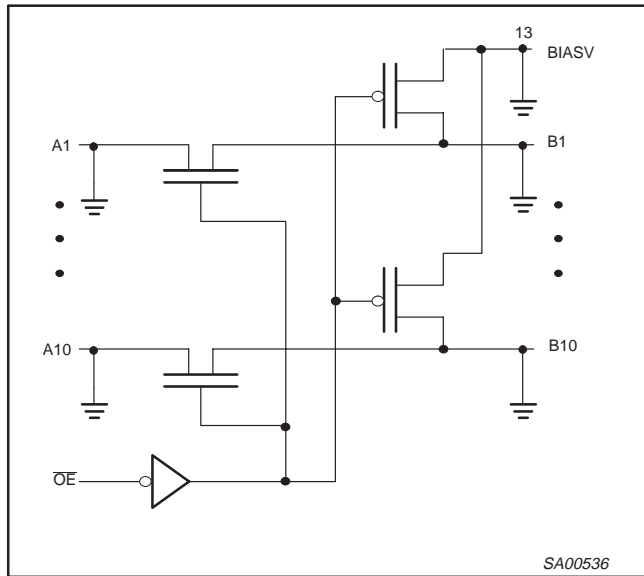
### PIN DESCRIPTION

| PIN NUMBER                                | SYMBOL          | NAME AND FUNCTION                    |
|---|-----------------|--------------------------------------|
| 1   | $\overline{OE}$ | Output enable                        |
| 13  | BIASV           | Precharge bias voltage input         |
| 2, 3, 4, 5, 6,<br>7, 8, 9, 10, 11         | A1–A10          | A-port I/O pins                      |
| 23, 22, 21, 20, 19,<br>18, 17, 16, 15, 14 | B1–B10          | B-port I/O pins – with active pullup |
| 12  | GND             | Ground (V)                           |
| 24  | $V_{\text{CC}}$ | Positive supply voltage              |

# 10-bit bus switch with precharged outputs for live insertion

CBT6800

## LOGIC SYMBOL



## FUNCTION TABLE

| OE | B1 – B10 | FUNCTION  |
|----|----------|-----------|
| L  | A1 – A10 | Connect   |
| H  | BIASV    | Precharge |

H = High voltage level  
L = Low voltage level  
Z = High impedance "off" state

## ABSOLUTE MAXIMUM RATINGS<sup>1, 2</sup>

| SYMBOL           | PARAMETER                     | CONDITIONS         | RATING       | UNIT |
|------------------|-------------------------------|--------------------|--------------|------|
| V <sub>CC</sub>  | DC supply voltage             |                    | -0.5 to +7.0 | V    |
| I <sub>IK</sub>  | DC input diode current        |                    | -50          | mA   |
| V <sub>I</sub>   | DC input voltage <sup>3</sup> |                    | -1.2 to +7.0 | V    |
| I <sub>SW</sub>  | DC clamp diode current        | V <sub>O</sub> < 0 | -50          | mA   |
| T <sub>stg</sub> | Storage temperature range     |                    | -65 to 150   | °C   |
| BiasV            | DC voltage range              |                    | -0.5 to 6.0  | V    |

### NOTES:

1. Stresses beyond those listed may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
2. The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability. The maximum junction temperature of this integrated circuit should not exceed 150°C.
3. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

## RECOMMENDED OPERATING CONDITIONS

| SYMBOL           | PARAMETER                            | LIMITS |                 | UNIT |
|------------------|--------------------------------------|--------|-----------------|------|
|                  |                                      | Min    | Max             |      |
| V <sub>CC</sub>  | DC supply voltage                    | 4.5    | 5.5             | V    |
| BIASV            | DC supply voltage                    | 1.3    | V <sub>CC</sub> | V    |
| V <sub>IH</sub>  | High-level input voltage             | 2.0    |                 | V    |
| V <sub>IL</sub>  | Low-level Input voltage              |        | 0.8             | V    |
| T <sub>amb</sub> | Operating free-air temperature range | -40    | +85             | °C   |

# 10-bit bus switch with precharged outputs for live insertion

CBT6800

## DC ELECTRICAL CHARACTERISTICS

| SYMBOL          | PARAMETER                               | TEST CONDITIONS   | LIMITS  |                  |         | UNIT          |
|-----------------|---|---|---|------------------|---------|---------------|
|                 |   |   | $T_{amb} = -40^{\circ}\text{C to } +85^{\circ}\text{C}$ |                  |         |               |
|                 |   |   | Min   | Typ <sup>1</sup> | Max     |               |
| $V_{IK}$        | Input clamp voltage                     | $V_{CC} = 4.5\text{ V}; I_I = -18\text{ mA}$                                  |   |                  | -1.2    | V             |
| $I_I$           | Input leakage current – $\overline{OE}$ | $V_{CC} = 5.5\text{ V}; V_I = \text{GND or } 5.5\text{ V}$                    |   |                  | $\pm 5$ | $\mu\text{A}$ |
| $I_O$           | Output bias current                     | $V_{CC} = 4.5\text{ V}; \text{BiasV} = 2.4\text{ V}; V_O = 0$                 | 0.25  |                  |         | mA            |
| $I_{CC}$        | Quiescent supply current                | $V_{CC} = 5.5\text{ V}; I_O = 0, V_I = V_{CC}\text{ or GND}$                  |   |                  | 50      | $\mu\text{A}$ |
| $\Delta I_{CC}$ | Control pins <sup>2</sup>               | $V_{CC} = 5.5\text{ V}$ , one input at 3.4 V, other inputs at $V_{CC}$ or GND |   |                  | 2.5     | mA            |
| $C_I$           | Control pins                            | $V_I = 3\text{ V or } 0$  |   | 3.5              |         | pF            |
| $C_{O(OFF)}$    | Off-state capacitance – I/O pins        | $V_O = 3\text{ V or } 0$ ; switch off   |   | 8.2              |         | pF            |
| $r_{on}^3$      | On-resistance                           | $V_{CC} = 4.5\text{ V}; V_I = 0\text{ V}; I_I = 64\text{ mA}$                 |   | 5                | 7       | $\Omega$      |
|                 |   | $V_{CC} = 4.5\text{ V}; V_I = 0\text{ V}; I_I = 30\text{ mA}$                 |   | 5                | 7       |               |
|                 |   | $V_{CC} = 4.5\text{ V}; V_I = 2.4\text{ V}; I_I = 15\text{ mA}$               |   | 10               | 15      |               |
| VP              | Pass gate voltage                       | $V_{IN} = V_{CC} = 5.0\text{ V}, I_{OUT} = -100\text{ }\mu\text{A}$           | 3.4   | 3.6              | 3.9     | V             |

### NOTES:

- All typical values are at  $V_{CC} = 5\text{ V}, T_{amb} = 25^{\circ}\text{C}$
- This is the increase in supply current for each input that is at the specified TTL voltage level rather than  $V_{CC}$  or GND
- Measured by the voltage drop between the A and the B terminals at the indicated current through the switch. On-state resistance is determined by the lowest voltage of the two (A or B) terminals.

# 10-bit bus switch with precharged outputs for live insertion

CBT6800

## AC CHARACTERISTICS

GND = 0 V;  $C_L = 50 \text{ pF}$ ;  $t_r = t_f \leq 2.5 \text{ ns}$

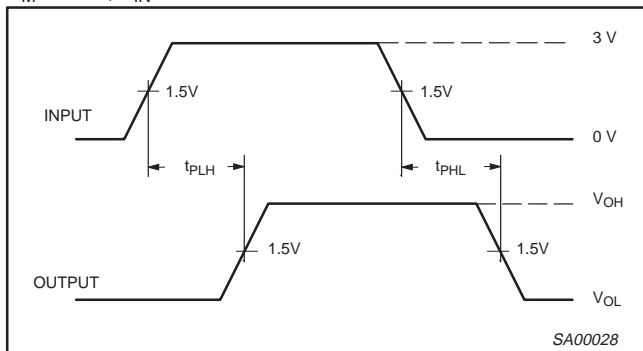
| SYMBOL                 | PARAMETER                      | FROM (INPUT)           | TO (OUTPUT) | LIMITS                                      |            | UNIT |
|------------------------|--------------------------------|------------------------|-------------|---|------------|------|
|                        |                                |                        |             | $V_{CC} = +5.0 \text{ V} \pm 0.5 \text{ V}$ |            |      |
|                        |                                |                        |             | Min   | Max        |      |
| $t_{pd}$               | Propagation delay <sup>1</sup> | A or B                 | B or A      |   | .25        | ns   |
| $t_{PZH}$<br>$T_{PZL}$ | BIASV = GND<br>BIASV = 3 V     | $\overline{\text{ON}}$ | A or B      | 2.4<br>3.0                                  | 7.7<br>8.3 | ns   |
| $t_{PHZ}$<br>$T_{PLZ}$ | BIASV = GND<br>BIASV = 3 V     | $\overline{\text{ON}}$ | A or B      | 1.0<br>3.1                                  | 5.3<br>7.8 | ns   |

**NOTE:**

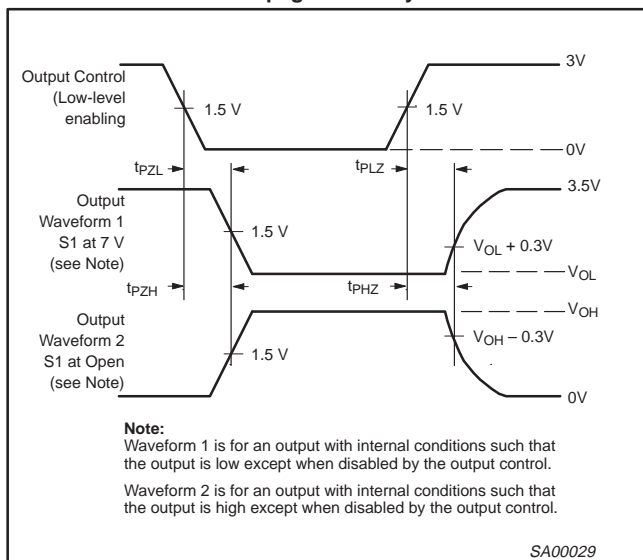
1. This parameter is warranted but not production tested. The propagation delay is based on the RC time constant of the typical on-state resistance of the switch and a load capacitance of 50 pF, when driven by an ideal voltage source (zero output impedance).

## AC WAVEFORMS

$V_M = 1.5 \text{ V}$ ,  $V_{IN} = \text{GND to } 3.0 \text{ V}$

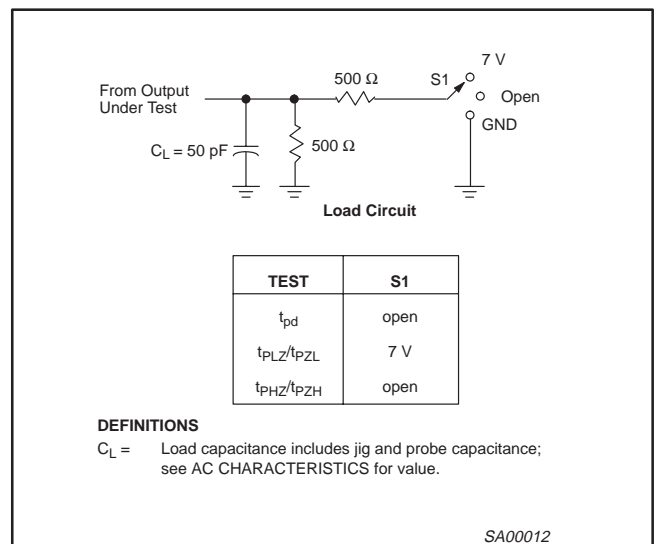


**Waveform 1. Waveforms Showing the Input (An) to Output (Yn) Propagation Delays**



**Waveform 2. Waveforms Showing the 3-State Output Enable and Disable Times**

## TEST CIRCUIT AND WAVEFORMS



**NOTES:**

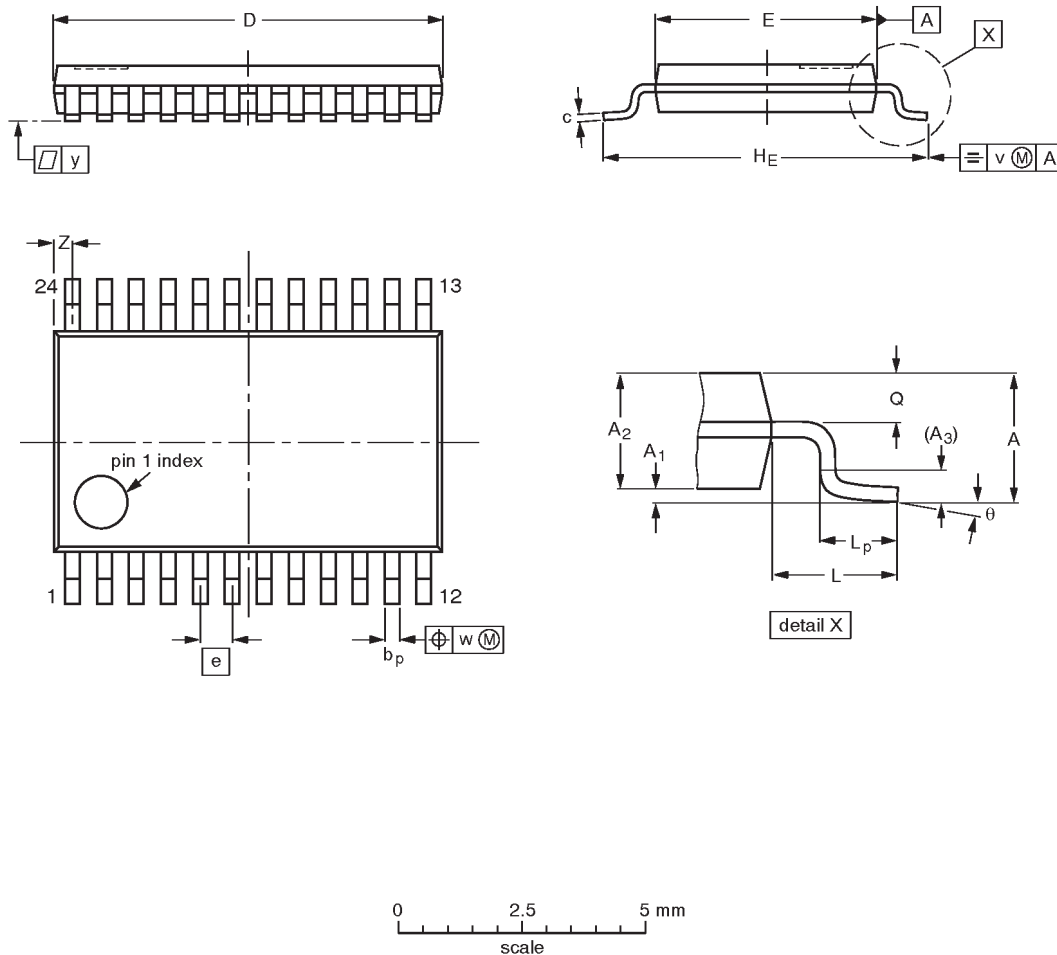
1. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r \leq 2.5 \text{ ns}$ ,  $t_f \leq 2.5 \text{ ns}$ .
2. The outputs are measured one at a time with one transition per measurement.

# 10-bit bus switch with precharged outputs for live insertion

## CBT6800

**TSSOP24:** plastic thin shrink small outline package; 24 leads; body width 4.4 mm

**SOT355-1**



**DIMENSIONS (mm are the original dimensions)**

| UNIT | A max. | A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | b <sub>p</sub> | c          | D <sup>(1)</sup> | E <sup>(2)</sup> | e    | H <sub>E</sub> | L   | L <sub>p</sub> | Q          | v   | w    | y   | z <sup>(1)</sup> | θ        |
|------|--------|----------------|----------------|----------------|----------------|------------|------------------|------------------|------|----------------|-----|----------------|------------|-----|------|-----|------------------|----------|
| mm   | 1.10   | 0.15<br>0.05   | 0.95<br>0.80   | 0.25           | 0.30<br>0.19   | 0.2<br>0.1 | 7.9<br>7.7       | 4.5<br>4.3       | 0.65 | 6.6<br>6.2     | 1.0 | 0.75<br>0.50   | 0.4<br>0.3 | 0.2 | 0.13 | 0.1 | 0.5<br>0.2       | 8°<br>0° |

**Notes**

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

| OUTLINE VERSION | REFERENCES |          |      |  | EUROPEAN PROJECTION | ISSUE DATE            |
|-----------------|------------|----------|------|--|---------------------|-----------------------|
|                 | IEC        | JEDEC    | EIAJ |  |                     |                       |
| SOT355-1        |            | MO-153AD |      |  |                     | -93-06-16<br>95-02-04 |

---

10-bit bus switch with precharged outputs  
for live insertion

---

CBT6800

**NOTES**

# 10-bit bus switch with precharged outputs for live insertion

CBT6800

## Data sheet status

| Data sheet status         | Product status | Definition [1]   |
|---------------------------|----------------|--|
| Objective specification   | Development    | This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.  |
| Preliminary specification | Qualification  | This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product. |
| Product specification     | Production     | This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.   |

[1] Please consult the most recently issued datasheet before initiating or completing a design.

## Definitions

**Short-form specification** — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

**Limiting values definition** — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

**Application information** — Applications that are described herein for any of these products are for illustrative purposes only. Philips Semiconductors make no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

## Disclaimers

**Life support** — These products are not designed for use in life support appliances, devices or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips Semiconductors customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips Semiconductors for any damages resulting from such application.

**Right to make changes** — Philips Semiconductors reserves the right to make changes, without notice, in the products, including circuits, standard cells, and/or software, described or contained herein in order to improve design and/or performance. Philips Semiconductors assumes no responsibility or liability for the use of any of these products, conveys no license or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.

Philips Semiconductors  
811 East Arques Avenue  
P.O. Box 3409  
Sunnyvale, California 94088-3409  
Telephone 800-234-7381

© Copyright Philips Electronics North America Corporation 1999  
All rights reserved. Printed in U.S.A.

Date of release: 10-99

Document order number:

9397 750 06552

*Let's make things better.*

Philips  
Semiconductors



**PHILIPS**