TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PAU04FU

Dual Inverter (unbuffer) with 3.6 V Tolerant Input

Features

- Low voltage operation: $V_{CC} = 1.8 \sim 3.6 \text{ V}$
- Quiescent supply current: I_{CC} < 20 $\mu A~(max)$

VCC = 3.6 V, Ta = -40~85°C)

• High-speed operation: $t_{pd} = 3.5 \text{ ns} (max) (V_{CC} = 3.0 \sim 3.6 \text{ V})$

 t_{pd} = 4.2 ns (max) (V_{CC} = 2.3~2.7 V)

 $t_{pd} = 8.4 \text{ ns} (max) (V_{CC} = 1.8 \text{ V})$

• High-output current: $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$ $I_{OH}/I_{OL} = \pm 18 \text{ mA (min)} (V_{CC} = 2.3 \text{ V})$

 $I_{OH}/I_{OL} = \pm 6 \text{ mA} \text{ (min)} (V_{CC} = 1.8 \text{ V})$

- Latch-up performance: ±300 mA
- ESD Performance: ±200 V (JEITA)
 - ±2000 V (MIL)
- 3.6 V tolerant function for input and power down protection are provided.

Absolute Maximum Ratings (Ta = 25°C)

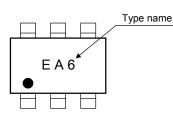
Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	V _{IN}	-0.5~4.6	V
DC output voltage	Vout	-0.5~V _{CC} + 0.5(Note 1)	V
Input diode current	I _{IK}	-50	mA
Output diode current	lok	±50 (Note 2)	mA
DC output current	IOUT	±50	mA
DC V _{CC} /ground current	ICC	±100	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

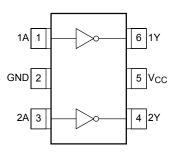
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

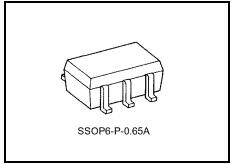
- Note 1: Date retention only
- Note 2: High or low state. V_{OUT} absolute maximum rating must be observed.

Marking



Pin Assignment (top view)

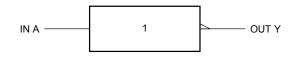




Weight: 0.0068 g (typ.)

<u>TOSHIBA</u>

Logic Diagram



А	Y

Truth Table

H L	L	Н
	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.8~3.6	V
Supply voltage	vcc	1.2~3.6 (Note 3)	v
Input voltage	V _{IN}	-0.3~3.6	V
Output voltage	VOUT	0~V _{CC} (Note 4)	V
		±24 (Note 5)	
Output Current	I _{OH} /I _{OL}	±18 (Note 6)	mA
		±6 (Note 7)	
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10 (Note 8)	ns/V

- Note 3: Date Retention Only
- Note 4: High or low state
- Note 5: $V_{CC} = 3.0 \sim 3.6 \text{ V}$
- Note 6: $V_{CC} = 2.3 \sim 2.7 \text{ V}$
- Note 7: V_{CC} = 1.8 V
- Note 8: $V_{CC} = 3.0 V$

<u>TOSHIBA</u>

Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

Characteristics		Symbol	Test Condition			Min	Max	Unit
Charac	clensucs	Symbol			V _{CC} (V)	IVIIII	Max	Unit
"H" level		VIH			1.8	$\begin{array}{c} 0.85 \times \\ V_{CC} \end{array}$		
Input voltage		ЧH		_	2.3~3.6	0.8 × V _{CC}	_	V
input voltage	"L" level	Ma			1.8	_	0.15 × V _{CC}	v
	Lievei	VIL		_	2.3~3.6	_	$0.2 \times V_{CC}$	
				$I_{OH} = -100 \ \mu A$	1.8~3.6	V _{CC} - 0.2	_	
				I _{OH} = -6 mA	1.8	1.4	_	-
				$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	
	"H" level	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -18 mA	2.3	1.7	_	
				I _{OH} = -12 mA	2.7	2.2	_	
				I _{OH} = -18 mA	3.0	2.4	_	
Output voltage				I _{OH} = -24 mA	3.0	2.2		V
				$I_{OL} = 100 \ \mu A$	1.8~3.6		0.2	
"L" level			I _{OH} = 6 mA	1.8		0.3		
		V _{OL}	$V_{IN} = V_{IH}$	$I_{OL} = 12 \text{ mA}$	2.3		0.4	
	"L" level			$I_{OL} = 18 \text{ mA}$	2.3		0.6	
				$I_{OL} = 12 \text{ mA}$	2.7	_	0.4	
				I _{OL} = 18 mA	3.0		0.4	
			I _{OL} = 24 mA		3.0		0.55	
Input leakage curre	ent	I _{IN}	V _{IN} = 0~3.6 V		2.7~3.6		±5.0	μA
Quiescent supply of	ourrent	laa	$V_{IN} = V_{CC}$ or GND		2.7~3.6		20.0	
Quiescent supply (Junent	ICC	$V_{CC} \leq (V_{IN}, V_O)$	$V_{CC} \leqq (V_{IN}, V_{OUT}) \leqq 3.6 \text{ V}$			— ±20.0	μA

AC Characteristics (Ta = -40~85°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	t _{pLH} t _{pHL}	(Fig.1, 2)	1.8	1.0	8.4	
Propagation delay time			2.5 ± 0.2	0.8	4.2	ns
			$\textbf{3.3}\pm\textbf{0.3}$	0.6	3.5	

For $C_L = pF$,add approximately 300 ps to the Ac maximum specification.

Dynamic Switching Characteristics (Ta = 25° C, input t_r = t_f = 2.0 ns, C_L = 30 pF)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
		$V_{IN} = 1.8 V, V_{IL} = 0 V$ ((Note 9)	1.8	0.25	
Quiet output maximum dynamic V_{OL}	VOLP	$V_{IN} = 2.5 V, V_{IL} = 0 V$ ((Note 9)	2.5	0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ ((Note 9)	3.3	0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$ ((Note 9)	1.8	-0.25	
Quiet output maximum dynamic VOL	V _{OLV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ ((Note 9)	2.5	-0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ ((Note 9)	3.3	-0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$ ((Note 9)	1.8	1.5	
Quiet output maximum dynamic VOH	V _{OHP}	$V_{IN} = 2.5 V, V_{IL} = 0 V$ ((Note 9)	2.5	1.9	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ ((Note 9)	3.3	2.2	

Note 9: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_		1.8, 2.5, 3.3	4	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 10)	1.8, 2.5, 3.3	7	pF

Note 10: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

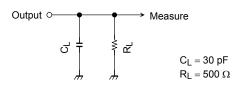
Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

TOSHIBA

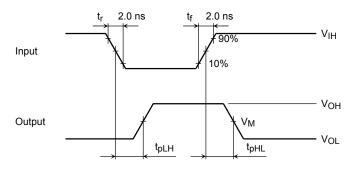
Test Circuit

Figure 1



AC Waveform

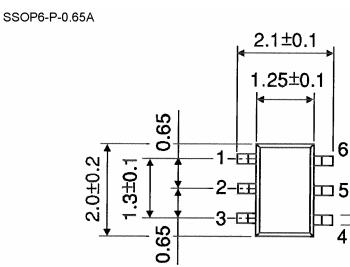
Figure 2 t_{pLH}, t_{pHL}



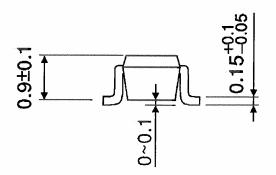
Symbol		V _{CC}	
Symbol	$3.3\pm0.3~V$	$2.5\pm0.2~\text{V}$	1.8 V
VIH	2.7 V	V _{CC}	V _{CC}
VM	1.5 V	V _{CC} /2	V _{CC} /2

TOSHIBA

Package Dimensions



Unit: mm



2.0

Weight: 0.0068 g (typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in his document shall be made at the customer's own risk.
- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patents or other rights of TOSHIBA or the third parties.
- Please contact your sales representative for product-by-product details in this document regarding RoHS compatibility. Please use these products in this document in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances. Toshiba assumes no liability for damage or losses occurring as a result of noncompliance with applicable laws and regulations.