TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SAU04F,TC7SAU04FU

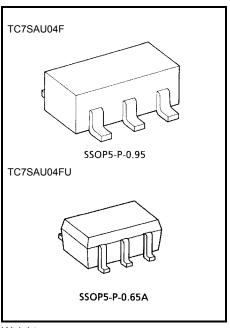
Inverter (Un-Buffer)

Features

- Low voltage operation $: V_{CC} = 1.8 \sim 3.6 \text{ V}$
- High speed operation : t_{pd} = 3.5 ns (max) (V_{CC} = 3.0~3.6 V)
- : t_{pd} = 4.2 ns (max) (V_{CC} = 2.3~2.7 V)
 - : t_{pd} = 8.4 ns (max) (V_{CC} = 1.8 V)
- High Output current
 - : I_{OH}/I_{OL} = ±18 mA (min) (V_{CC} = 2.3 V)
 - : I_{OH}/I_{OL} = ±6 mA (min) (V_{CC} = 1.8 V)

 $: I_{OH}/I_{OL} = \pm 24 \text{ mA} (min) (V_{CC} = 3.0 \text{ V})$

• 3.6-V tolerant input



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	VIN	-0.5~4.6	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5 (Note 1)	V
Input diode current	lık	-50	mA
Output diode current	I _{OK}	±50 (Note 2)	mA
DC output current	IOUT	±50	mA
Power dissipation	PD	200	mW
DC V _{CC} /ground current	ICC	±100	mA
Storage temperature range	T _{stg}	-65~150	

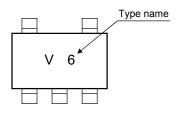
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

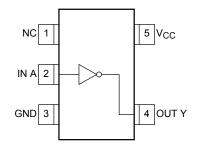
- Note 1: High or low state. $I_{\mbox{OUT}}$ absolute maximum rating must be observed.
- Note 2: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

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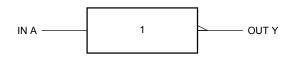
Marking



Pin Assignment (top view)



Logic Diagram



А	Y
L	Н
Н	L

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Dowor oupply voltage	Vee	1.8~3.6	V	
Power supply voltage	Vcc	1.2~3.6 (Note 3)	v	
Input voltage	V _{IN}	-0.3~3.6	V	
Output voltage	V _{OUT}	0~V _{CC} (Note 4)	V	
		± 24 (Note 5)		
Output current	IOH/IOL	± 18 (Note 6)	mA	
		±6 (Note 7)		
Operating temperature range	T _{opr}	-40~85	°C	

Note 3: Data retention only

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 6: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 7: $V_{CC} = 1.8 V$

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Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

Charac	teristics	Symbol	Test Condition			Min	Max	Unit		
Charac	lensues	Symbol			V _{CC} (V)	IVIIII	Max	Unit		
		V _{IH} —		1.8	$0.85 \times V_{CC}$	_				
Input voltage	High level	ЧН		_	2.3~3.6	0.8 × V _{CC}	_	V		
input voltage	Low level	. V			1.8	_	0.15 × V _{CC}	v		
	Low level	V _{IL}		_	2.3~3.6	_	$0.2 \times V_{CC}$			
				I _{OH} = -100 μA	1.8~3.6	V _{CC} - 0.2		_		
			l.	I _{OH} = -6 mA	1.8	1.4	_			
				$I_{OH} = -12 \text{ mA}$	2.3	1.8	_			
	High level	V _{OH}	$V_{IN} = V_{IL}$	I _{OH} = -18 mA	2.3	1.7	_			
				$I_{OH} = -12 \text{ mA}$	2.7	2.2	_			
				I _{OH} = -18 mA	3.0	2.4	_			
Output voltage							I _{OH} = -24 mA	3.0	2.2	_
		$\frac{I_{OL} = 100 \ \mu A}{I_{OL} = 6 \ mA}$		$I_{OL} = 100 \ \mu A$	1.8~3.6		0.2			
			$I_{OL} = 6 \text{ mA}$	1.8		0.3				
				$I_{OL} = 12 \text{ mA}$	2.3		0.4	-		
	Low level	V _{OL}	$V_{IN}=V_{IH}$	$V_{IN} = V_{IH}$ $I_{OL} = 18 \text{ mA}$	2.3	_	0.6			
			$I_{OL} = 12 \text{ mA}$	2.7	_	0.4				
			I _{OL} = 18 mA	3.0	_	0.4				
			I _{OL} = 24 mA	3.0	_	0.55				
Input leakage curre	ent	I _{IN}	V _{IN} = 0~3.6 V		2.7~3.6		±5.0	μA		
Quiescent supply of	nurrent	lee	$V_{IN} = V_{CC}$ or GN	D	2.7~3.6		20.0	μA		
Quiescent supply (ICC	$V_{CC} \leq (V_{IN}) \leq 3.6$	3 V	2.7~3.6		±20.0	μΑ		

AC Characteristics (Ta = -40~85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	•		1.8	1.0	8.4	
Propagation delay time	t _{pLH}	Figure 1, Figure 2 2.5 ±	2.5 ± 0.2	0.8	4.2	ns
	^t pHL		$\textbf{3.3}\pm\textbf{0.3}$	0.6	3.5	

For $C_L = 50 \text{ pF}$, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25° C, input: t_r = t_f = 2.0 ns, C_L = 30 pF)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
		$V_{IN} = 1.8 \ V, \ V_{IL} = 0 \ V$	(Note 8)	1.8	0.25	
Quiet output maximum dynamic $~V_{OL}$	VOLP	$V_{IN} = 2.5 \ V, \ V_{IL} = 0 \ V$	(Note 8)	2.5	0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 8)	3.3	0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 8)	1.8	-0.25	
Quiet output minimum dynamic V_{OL}	V _{OLV}	$V_{IN}=2.5~V,~V_{IL}=0~V$	(Note 8)	2.5	-0.6	ns
		$V_{IN} = 3.3 V, V_{IL} = 0 V$	(Note 8)	3.3	-0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 8)	1.8	1.5	
Quiet output minimum dynamic V_{OH}	VOHV	$V_{IN} = 2.5 \ V, \ V_{IL} = 0 \ V$	(Note 8)	2.5	1.9	ns
		$V_{IN} = 3.3 V, V_{IL} = 0 V$	(Note8)	3.3	2.2	

Note8: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	—		1.8, 2.5, 3.3	4	pF
Power dissipation capacitance	C _{PD}	$f_{IN} = 10 \text{ MHz}$	(Note 9)	1.8, 2.5, 3.3	7	pF

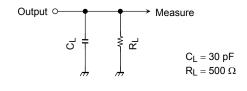
Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

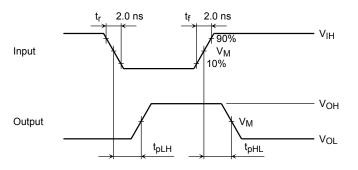
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AC Test Circuit

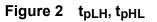




AC Waveforms



Symbol	V _{CC}						
Symbol	$3.3\pm0.3~V$	$2.5\pm0.2~\text{V}$	1.8 V				
VIH	2.7 V	V _{CC}	V _{CC}				
VM	1.5 V	V _{CC} /2	V _{CC} /2				

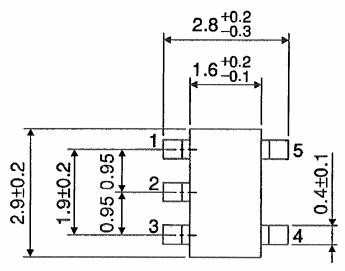


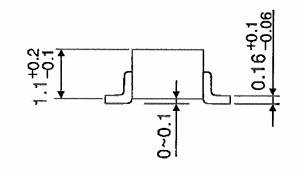
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Package Dimensions



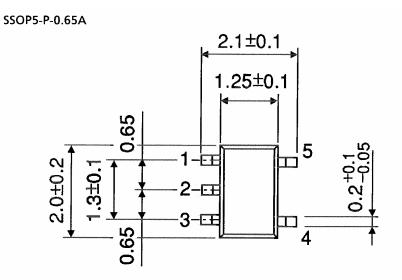
Unit : mm

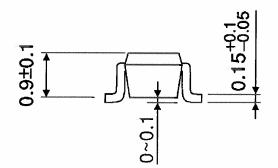




Weight: 0.016 g (typ.)

Package Dimensions





Weight: 0.006 g (typ.)

Unit : mm

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20070701-EN GENERAL

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