TOSHIBA Phototransistor Silicon NPN Epitaxial Planar

TPS610(F)

Lead(Pb)-Free Photoelectric Counter Various Kinds Of Readers Position Detection

- $\phi 5 mm$ epoxy resin package
- High sensitivity: $I_L = 250 \mu A(typ.)$
- Half value angle: $\theta 1/2 = \pm 8^{\circ}$ (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-emitter voltage	V _{CEO}	30	V
Emitter-collector voltage	V _{ECO}	5	V
Collector Current	IC	50	mA
Collector power dissipation	PC	-150	mW
Collector power dissipation derating (Ta>25°C)	ΔP _C / °C	-2	mW / °C
Operating temperature range	T _{opr}	-20~75	°C
Storage temperature range	T _{stg}	-30~100	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit in mm | Total Reserve | Control of the co

Weight: 0.3g (typ.)

Pin Connection

2 1 . Emitter

Opto-Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Dark current		I _D (I _{CEO})	V _{CE} = 24V, E = 0	_	0.005	0.1	μA
Light current		IL	$V_{CE} = 3V, E = 0.1 \text{mW} / \text{cm}^2$ (Note)	100	250	_	μA
Collector– emitter saturation voltage		V _{CE} (sat)	$I_C = 50\mu\text{A}, E = 0.1\text{mW} / \text{cm}^2$ (Note)	_	0.25	0.4	٧
Switching time	Rise time	t _r	V_{CC} = 5V, I_C = 2mA R_L = 100 Ω	_	6	_	μs
	Fall time	t _f		_	6	_	
Peak sensitivity wavelength		λP	_	_	800	_	nm
Half value angle		$\theta \frac{1}{2}$	_	_	±8	_	۰

Note: Color temperature = 2870K, standard tungsten lamp

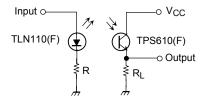
Precaution

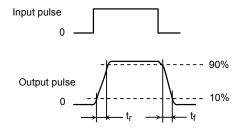
Please be careful of the followings.

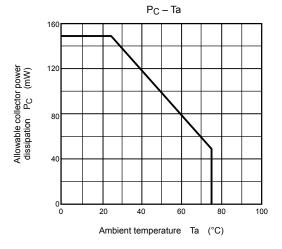
- 1. Soldering shall be performed at the top portion from the lead stopper.
- 2. Soldering temperature: 260°C max. Soldering time: 5s max.
- 3. When the lead is formed, the lead shall be formed at the top portion of the stopper without leaving forming stress to the body of the device. Soldering shall be performed after lead forming.

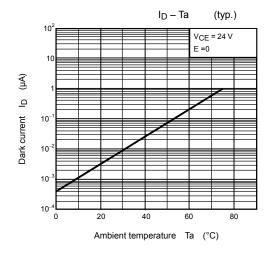
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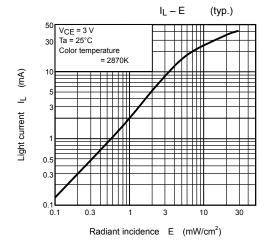
Fig.1 Switching time test circuit

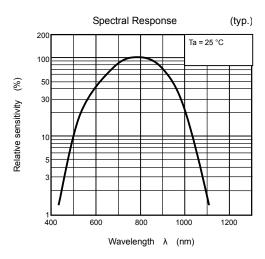






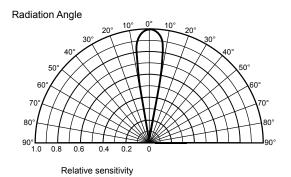


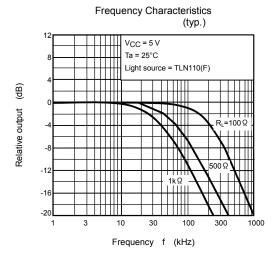


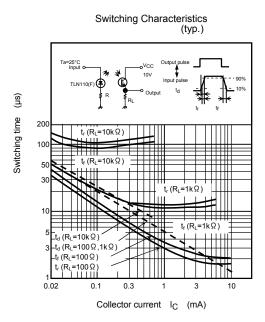


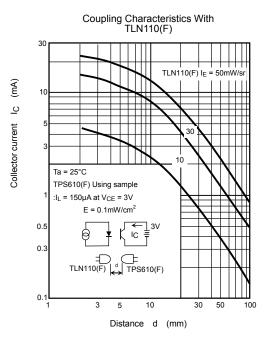
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Directional Sensitivity Characteristic (typ.) (Ta = 25°C)









RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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