## Freescale Semiconductor Data Sheet

# PLL Tuned VHF (Channel 3 / 4) Audio / Video Modulator

The MC44BC375U and MC44BC375UA are audio/video RF modulators for use in VCRs, set-top boxes, and similar devices. They are designed for applications requiring National Television System Committee, NTSC, channel 3 or channel 4, RF outputs. The difference between the two modulators is the RF output power level. The nominal RF<sub>OUT</sub> level for the MC44BC375U is 76 dB $\mu$ V (typical) and the RF<sub>OUT</sub> level for the MC44BC375UA is 80 dB $\mu$ V (typical).

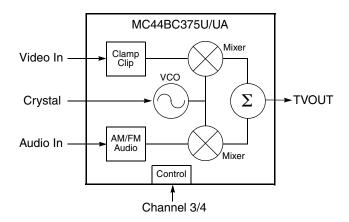
A programmable Phase-Locked Loop (PLL) tunes the modulator, and the desired channel 3 or 4 frequency isselected using the external package pin, Channel Selection (CHS). No external tank circuit components are required, reducing the sensitivity of the PCB design and the need for external adjustments. The PLL obtains its reference from a 4.0 MHz crystal oscillator.

The modulators incorporate a sound subcarrier oscillator and use a second programmable PLL to derive subcarrier frequencies. The 4.5 MHz or 5.5 MHz sound carrier frequency is selectable via the package pin, Sound Frequency Selection (SFS). The Picture to Sound subcarrier ratio is also selectable by the pin, Picture to Sound Selection (PSS), to choose either a 14 dB or 16 dB ratio.

A low-power standby mode is selectable via the pin, Psave/LO, which turns off all internal VCOs and mixers and simultaneously switches ON an output pin, Logic Output Port (LOP), which may be used as an antenna switch driver.

### Features

- Two RF output power levels offered:
  - MC44BC375U has typical  $\text{RF}_{\text{OUT}}$  power of 76 dBµV
  - MC44BC375UA has typical RF<sub>OUT</sub> power of 80 dBμV
  - U.S. broadcast frequency for Channel 3 or 4 operation is selected by CHS pin
- Programmable VHF oscillator and sound subcarrier oscillator. No external varicaps, diodes/inductors, or tuned components required.
- Programmable Picture/Sound Carrier Ratio (14 dB or 16 dB) is selected by PSS pin
- Programmable Sound Subcarrier Oscillator (4.5 or 5.5 MHz) is selected by SFS pin)
- Peak White Clip
- · Low-power standby mode is selected by the Psave/LO pin
- Logic Output Port is selected by Psave/LO pin (Antenna Switch Driver)
- Transient output inhibit during PLL Lock-up at power-ON
- · Available in Pb-free, RoHS compliant, narrow body SOIC 16 package
- Also available, for a limited time, in the standard Pb package with the D suffix



ORDERING INFORMATION				
Device	Temp. Range	RoHS		
MC44BC375UEF, R2	-20°C to +85°C	Yes		
MC44BC375UAEF, R2	-20°C to +85°C	Yes		
MC44BC375UD, R2	-20°C to +85°C	No		
MC44BC375UAD, R2	-20°C to +85°C	No		

**Note:** For tape and reel, add the R2 suffix.



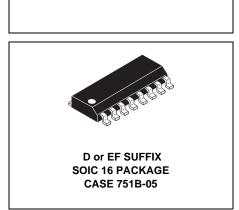
Document Number: MC44BC375U Rev 3, 8/2006

**VRoHS** 

# MC44BC375U MC44BC375UA

PLL TUNED VHF (CHANNEL 3 / 4) AUDIO / VIDEO MODULATOR

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# **PIN DESCRIPTIONS**

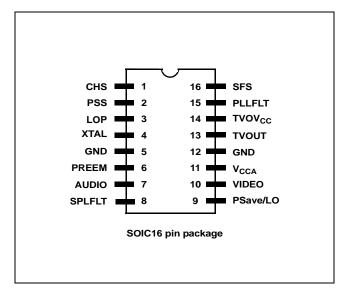


Figure 1. Pin Connections

Pin number	Pin Name	Description
1	CHS	Channel select (Low = channel 4, High = channel 3)
2	PSS	Picture-to-Sound ratio select (Low = 14 dB, High = 16 dB)
3	LOP	Logical output port controlled by pin 9
4	XTAL	Crystal / oscillator input
5	GND	Ground
6	PREEMP	Pre-emphasis capacitor
7	AUDIO <sub>IN</sub>	Audio input
8	SPLFLT	Sound PLL loop filter
9	PSAVE/LO	Power save mode and LO control
10	VIDEO <sub>IN</sub>	Video input
11	V <sub>CC</sub>	Main analog supply voltage
12	GND	Analog ground
13	RF <sub>OUT</sub>	RF output signal
14	RFOV <sub>CC</sub>	RFoutput stage supply voltage
15	PLLFLT	RF PLL loop filter
16	SFS	Sound frequency select (Low = 5.5 MHz, High = MHz 4.5 MHz)

### Table 1. Package Pin Descriptions

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## **FUNCTIONAL OVERVIEW**

Figure 2 shows a simplified block diagram of the MC44BS375U and MC33BS375UA devices. There are two main sections:

- 1. A PLL section to synthesize the VHF output channel frequency for channel 3 or channel 4.
- 2. A modulator section which accepts audio and video inputs and modulates the VHF carrier.

The high frequency BiCMOS technology allows integration of the VHF tank circuit and certain filtering functions eliminating the need for a number of external components.

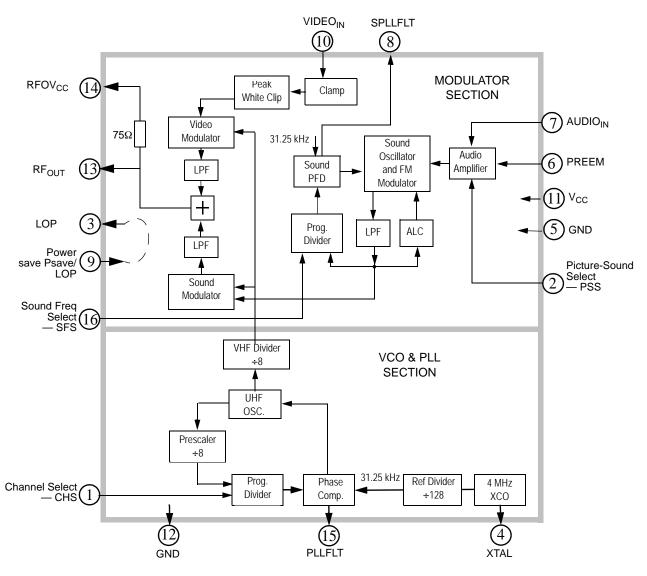


Figure 2. MC44BS375U/UA Simplified Block Diagram

#### **POWER SUPPLY**

The two device  $V_{CCS}$  (pins 11 and 14) must be applied at the same time to ensure all internal blocks are correctly biased. Do NOT bias any other pin before  $V_{CC}$  is applied to the device.

### STANDBY MODES

During standby mode, the modulator is switched to low power consumption. The sound oscillator, UHF oscillator and VHF dividers, video and sound modulator sections bias are internally turned OFF. The modulator is programmed in standby mode when pin 9, PSave/LO, is set to a "LO" level (see "Pin Selection" on page 4).

### LOGIC OUTPUT PORT

The Logic Output Port is used to control an external switch (such as an antenna switch or an attenuator). This pin is an open collector transistor able to sink up to 15 mA at a low voltage.

The LOP output is switched between a "HI" and "LO" level with pin 9, Psave/LO (see "Pin Selection" on page 4).

#### TRANSIENT OUTPUT INHIBIT

To minimize the risk of interference to other channels while the UHF PLL is acquiring a lock on the desired frequency, the Sound and Video modulators are turned OFF at power-ON (i.e.,  $V_{CC}$  is switched from 0 V to 5.0 V or device is switched from "Standby mode" to "Normal operation").

There is a time-out of 263 ms until the output is enabled. This allows the UHF PLL to settle to its programmed frequency. During the 263 ms time-out, the sound PLL current source is set to 10  $\mu$ A typical to speed up the locking time. After the 263 ms time-out, the current source is switched to 1.0  $\mu$ A. Use care when selecting loop filter components to ensure the loop transient does not exceed this delay.

#### VIDEO SECTION

The modulator requires a composite video input with negative going sync pulses and a nominal level of 1.0 V<sub>(pp)</sub>. This signal is AC coupled to the video input where the sync tip level is clamped.

The video modulation depth typical value is given for  $1.0 V_{CBVS}$  input level. It can be adjusted to any lower value by simply adding a resistive divider at video input, resulting in a lower signal seen by the video input stage.

The video signal is then passed to a peak white clip circuit whose function is to soft clip the top of the video waveform if the amplitude from the sync tip to peak white goes too high. In this way, over-modulation of the carrier by the video is avoided. The clipping function is always engaged.

#### SOUND SECTION

The multivibrator oscillator is fully integrated and does not require any external components. An internal low pass filter and matched structure give a very low harmonics level.

The sound modulator system consists of an FM modulator incorporating the sound subcarrier oscillator. The audio input signal is AC coupled into the amplifier which then drives the modulator.

The audio pre-emphasis circuit is a high-pass filter with an external capacitor C1 and an internal resistor (100 k $\Omega$  typical). The recommended capacitor value (750 pF) is for M/N standards, time constant is 75  $\mu$ s and it is 470 pF for B/G standards (50  $\mu$ s).

The audio bandwidth specification is for 50 Hz to 15 KHz range, with pre-emphasis circuit engaged. Without this preemphasis circuit, it is possible to extend the audio bandwidth to high frequencies, as there is no internal frequency limitation (stereo application).

#### PLL SECTION — DIVIDERS

The reference divider is a fixed ÷128 resulting in a reference frequency of 31.25 KHz with a 4.0 MHz crystal. The 31.25 KHz reference frequency is used for both the UHF and Sound PLLs.

The prescaler is a fixed  $\div 8$  and is permanently engaged. The VHF divider is also a fixed  $\div 8$ .

The programmable divider's division ratio is controlled by the CHS pin voltage in order to select channel 3 or channel 4.

#### **PIN SELECTION**

Pins CHS, PSS, and SFS are internally pulled up to 5.0 V. By default (open condition), all pins are "HI".

Table 2.	Configuration	Pin Settings
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Pin No	Pin Name	LO (grounded)	HI (open or +5.0 V)
1	CHS	CH4	CH3
2	PSS	14 dB	16 dB
9	PSave/ LOP	Power save Mode LOP pin LO (Low voltage)	Normal Operation LOP pin HI (High impedance)
16	SFS	5.5 MHz	4.5 MHz

# ELECTRICAL SPECIFICATIONS

#### **Table 3. Absolute Maximum Ratings**

Absolute maximum continuous ratings are those maximum values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation at absolute-maximum-rated conditions is not implied.

Characteristic	Symbol	Min	Мах	Units
Supply Voltage	V <sub>CC</sub>	-0.3	+6.0	V
Any Input Voltage	V <sub>in</sub>	-0.3	V <sub>CC</sub> + 0.3	V
Storage Temperature Range	T <sub>stg</sub>	-65	+150	°C
Junction Temperature	Т <sub>Ј</sub>	_	+150	°C

#### **Table 4. General Specifications**

Characteristic	Symbol	Min	Тур	max	Units
ESD Protection (Machine Model)	MM	200	500	—	V
ESD Protection (Human Body Model) <sup>(1)</sup>	HBM	4000	6000	_	V
Thermal resistance from Junction to Ambient	$R_{\ThetaJA}$		140	_	°C/W

1. MIL STD 883C method 3015-7.

#### Table 5. Recommended Operating Conditions and DC Characteristics

Characteristic		Symbol	Min	Тур	Max	Units
Supply Voltage		V <sub>CC</sub>	+4.5	+5.0	+5.5	V
Total supply current (all sections active)	MC44BC375U MC44BC375UA	I <sub>CC</sub> I <sub>CC</sub>	40 40	47 50	55 57	mA mA
Total standby mode supply current	MC44BC375U MC44BC375UA	I <sub>CC</sub> I <sub>CC</sub>	3.0 3.0	5.0 6.0	7.0 8.0	mA mA
Sound comparator charge pump current:	During locking When locked		7.0 0.7	10 1.0	12 1.5	μΑ μΑ
RF comparator charge pump current			60	100	150	μΑ
Crystal oscillator stability-negative resistanc	e		1.0	—		KΩ
Threshold level on pins: CHS, PSS, SFS, Psave/LO		V <sub>th</sub>	_	2.1		V
Logic Output Port: Saturation vol	age at I <sub>OL</sub> = 15 mA Leakage current	V <sub>OL</sub> I <sub>leackage</sub>		210	300 1.0	mV μA
Ambient Temperature		Τ <sub>Α</sub>	-20	—	+85	°C

# PERFORMANCE CHARACTERISTICS

Unless otherwise stated, all performance characteristics are for:

- Power Supply,  $V_{CC} = 5.0 V$
- Ambient Temperature,  $T_A = 25^{\circ}C$
- Video Input 1.0 V<sub>p-p</sub>, 10-step grayscale.
- RF inputs/outputs into 75  $\Omega$  load.

#### NOTE:

#### Specifications only valid for envelope demodulation.

#### **Table 6. High Frequency Characteristics**

The parameters listed are based on the type of test conditions found in the column Type.

- A = 100% tested
- B = 100% Correlation tested
- C = Characterized on samples
- D = Design parameter

See the "Characterization Measurement Conditions" on page 8 for each C type parameter.

Parameter	Test Conditions <sup>(1)</sup>	Device	Min	Тур	Мах	Unit	Туре
RF <sub>OUT</sub> output level	Output signal from modulator section	MC44BC375U MC44BC375UA	74 78	76 80	79 83	dBμV	В
Sound subcarrier harmonics (Fp+n*Fs)	Reference picture carrier	MC44BC375U MC44BC375UA		.73 -72	-65 .65	dBc	С
Second harmonic of chroma subcarrier	Using red EBU bar	MC44BC375U MC44BC375UA		-65 —	-55 -60	dBc	С
Chroma/Sound intermodulation: Fp+ (Fsnd-Fchr)	Using red EBU bar	MC44BC375U MC44BC375UA		-65 —	-60 -60	dBc	С
Fo (picture carrier) harmonics <sup>(2)</sup>	2nd harmonic	MC44BC375U MC44BC375UA		30 —	42 46	dBμV	С
	3rd harmonic:	MC44BC375U MC44BC375UA		66 —	69 73	dBμV	С
Out band (picture carrier) spurious	1/2*Fo - 1/4*Fo - 3/2*Fo - 3/4*Fo	MC44BC375U MC44BC375UA		_	27 32	dBμV	С
In band spurious (Fo @ 5.0 MHz)	No video sound modulation	MC44BC375U MC44BC375UA		-70 —	-62 -60	dBc	С
4.0 MHz Crystal spurious (Fo ± 4.0 MHz)	No video sound modulation	MC44BC375U MC44BC375UA		-82 —	-75 -75	dBc	С

1. See Performance Measurement Test Set-ups, Table 9.

2. Picture carrier harmonics are highly dependant on PCB layout and decoupling capacitors.

#### **Table 7. Video Performance Characteristics**

Parameter	Test Conditions <sup>(1)</sup>	Min	Тур	Max	Unit	Туре
Video bandwidth	Reference 0 dB at 100 KHz, measured at 5.0 MHz	-1.5	-0.5	—	dB	С
Video input level	75 Ω load	_	_	1.5	Vcvbs	D
Video input current		_	0.2	1.0	μΑ	А
Video input impedance		_	500	—	KΩ	А
Video S/N	Weighted	56	60	—	dB	С
	Unweighted	49	52	—	_	С
Differential Phase	On line 17 in M standard (FCC)	_	±1.0	—	deg	С
Differential Gain	On line 17 in M standard (FCC)	—	1.0	5.0	%	С
Luma/Sync ratio	Input ratio 7.0:3.0	6.8/3.2	_	7.2/2.8	_	В
Video modulation depth	Video input level = 1.0 V <sub>CVBS</sub>	80	85	90	%	В
Peak White Clip	Video Modulation depth for video = $1.4 V_{CVBS}$	90.5	95	99.5	%	В

1. See Performance Measurement Test Set-ups, Table 9.

#### MC44BC375U

### Table 8. Audio Performance Characteristics

Parameter	Test Conditions <sup>(1)</sup>	Min	Тур	Мах	Unit	Туре
Picture-to-Sound ratio	PSS pin = High PSS pin = Low	14 12	16 14	18 14	dB	В
Picture to Sound ratio difference	between PSS = HI and PSS = LO	1.5	2.0	2.5	dB	В
Audio modulation depth	Using specific pre-emphasis circuit, Audio input = 1.	0 KHz @ 2	205 mVrm	s	•	
	Fs = 4.5 MHz (100% modulation = ±25 KHz FM deviation)	75	85	95	%	В
	Fs = 5.5 MHz (100% modulation = ±50 KHz FM deviation)	75	85	95	%	В
Audio input resistance		45	55	61	KΩ	А
Audio Frequency response	Reference 0 dB at 1.0 KHz, Measure from 50 Hz to 15 KHz Depends on loop filter components	-2.5	_	+2.0	dB	С
Audio Distortion FM (THD only)	At 1.0 KHz, 100% modulation (±50 KHz) No video	—	0.3	1.0	%	С
Audio S/N with Sync Buzz FM		50	53	—	dB	С

1. See Performance Measurement Test Set-ups, Table 9.

# CHARACTERIZATION MEASUREMENT CONDITIONS

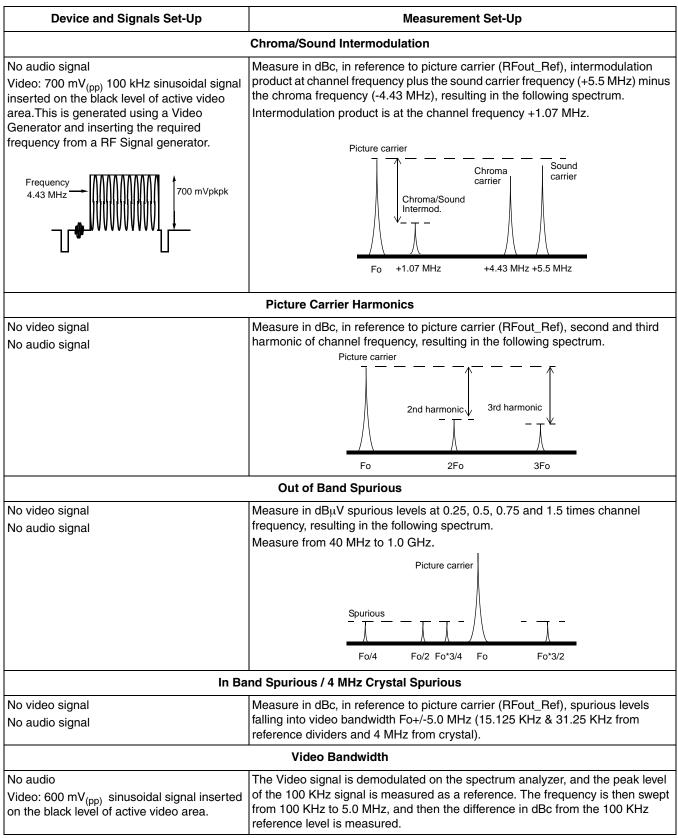
Device default configuration unless otherwise specified:

- Sound subcarrier frequency = 5.5 MHz
- Picture-to-sound carrier ratio = 14 dB
- CH3 and CH4 frequencies

- RF Inputs / Output into 75  $\Omega$  Load using a 75 to 50  $\Omega$  transformation
- Video Input 1.0 V pk-pk
- Audio pre-emphasis circuit engaged

### Table 9. Performance Measurement Test Set-Ups

Device and Signals Set-Up	Measurement Set-Up
	RF <sub>OUT</sub> Output Level
Video: 10 steps grey scale No audio	Measured picture carrier in dBµV with the HP8596E Spectrum Analyzer using a 75 to 50 $\Omega$ transformation, all cables losses and transformation pads having been calibrated.
	Measurement is used as a reference for other tests: RFout_Ref
	Sound Subcarrier Harmonics
Video: 10 steps grey scale No audio signal	Measure in dBc second and third sound harmonics levels in reference to picture carrier (RFout_Ref).
Sec	ond Harmonics of Chroma Subcarrier
No audio Video: a 700 mV <sub>(pp)</sub> 100 kHz sinusoidal signal is inserted on the black level of active video area.	Measure in dBc, in reference to picture carrier (RFout_Ref), second harmonic of chroma at channel frequency plus 2 times chroma frequency, resulting in the following spectrum.  Picture carrier  Chroma Chro



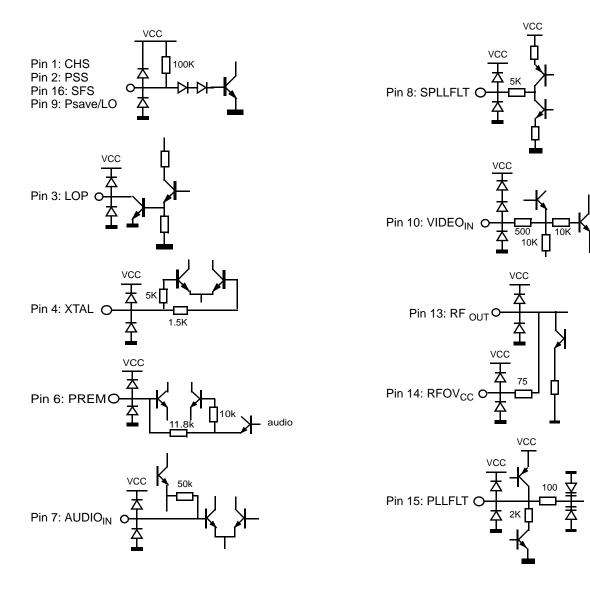
Downloaded from **Elcodis.com** electronic components distributor

Device and Signals Set-Up	Measurement Set-Up
	Weighted Video Signal to Noise
Video: 100% White video signal — 1 Vpk-pk. No audio signal This is measured using a Demodulator in B/G using a CCIR Rec. 567 weighting network, 100 KHz to 5.0 MHz band with sound trap and envelope detection, and a Video Analyzer.	The Video Analyzer measures the ratio between the amplitude of the active area of the video signal (700 mV) and the noise level in Vrms on a video black level which is shown below. VideoS/N is calculated as 20 x log(700 /N) in dB.
L	Jnweighted Video Signal to Noise
Same as above with CCIR filter disabled.	Same as above.
	Video Differential Phase
Video: 5 step Grey Scale — 1 $V_{(pp)}$ . No audio signal This is measured using a Demodulator in B/G using a CCIR Rec. 567 weighting network, 100 KHz to 5.0 MHz band with sound trap, and envelope detection, and a Video Analyzer.	On line CCIR 330, the video analyzer DP measure consists of calculating the difference of the Chroma phase at the black level and the different chroma subcarrier phase angles at each step of the greyscale. The largest positive or negative difference indicates the distortion. DIFF PHASE = $\frac{\text{the largest positive or negative difference}}{\text{the phase at position 0}} * 100\%$ The video analyzer method takes the worst step from the first 4 steps.
	Video Differential Gain
Video: 5 step Grey Scale — 1 V <sub>(pp)</sub> . No audio signal This is measured using a Demodulator in B/G using a CCIR Rec. 567 weighting network, 100 KHz to 5.0 MHz band with sound trap and envelope detection, and a Video Analyzer.	On line CCIR 330 shown below, the video analyzer DG measure consists of calculating the difference of the Chroma amplitude at the black level and the different amplitudes at each step of the greyscale. The largest positive or negative difference indicates the distortion.

Device and Signals Set-Up	Measurement Set-Up					
Video Modulation Depth						
No audio signal Video: 10 step grey scale	This is measured using a HP8596E Spectrum Analyzer with a TV Trigger option, allowing demodulation and triggering on any specified TV Line. The analyzer is centered on the maximum peak of the Video signal and reduced to zero Hertz span in Linear mode to demodulate the Video carrier. $\int_{a} \int_{a} \int$					
	Picture to Sound Ratio					
No video signal No audio Signal "PSS" pin set Low and High	Measure in dBc sound carrier in reference to picture carrier (RFout_Ref) for "PSS" = Low (PS = 14 dB typical) and for "PSS" = High (PS = 16 dB), Picture carrier Sound carrier Fo +5.5 MHz					
Audio	o Modulation Depth — FM Modulation					
Video Black Level Audio signal: 1.0 KHz, 205 mVrms. This is measured using a Demodulator in B/G and an Audio Analyzer at 1.0 KHz.	The audio signal 205 mV at 1.0 KHz is supplied by the Audio Analyzer, and the FM demodulated signal deviation is indicated on the Demodulator in KHz peak. This value is then converted in% of FM deviation, based on specified standards.					
	Audio Frequency Response					
Video Black Level Audio signal: 50 Hz to 15 KHz, 100 mV <sub>rms</sub> This is measured using a Demodulator in B/G and an Audio Analyzer.	The audio signal 1.0 KHz 100 mV <sub>rms</sub> is supplied by the Audio Analyzer, demodulated by the Demodulator and the audio analyzer measures the AC amplitude of this demodulated audio signal: this value is taken as a reference (0 dB). The audio signal is then swept from 50 Hz to 15 KHz, and demodulated AC amplitude is measured in dB relative to the 1.0 KHz reference. Audio pre-emphasis and de-emphasis circuits are engaged, all audio analyzer filters are switched OFF.					
	Audio Distortion FM					
Audio: 1.0 KHz, adjustable level Video Black Level This is measured using a Demodulator in B/G and an Audio Analyzer at 1.0 KHz. The output level of the Audio analyzer is varied to obtain a deviation of 50 KHz indicated on the Demodulator.	The input rms detector of the Audio Analyzer converts the ac level of the combined signal + noise + distortion to dc. It then removes the fundamental signal (1.0 KHz) after having measured the frequency. The output rms detector converts the residual noise + distortion to dc. The dc voltmeter measures both dc signals and calculates the ratio in% of the two signals. ADist = (Distorsion + Noise)/(Distorsion + Noise + Signal)					

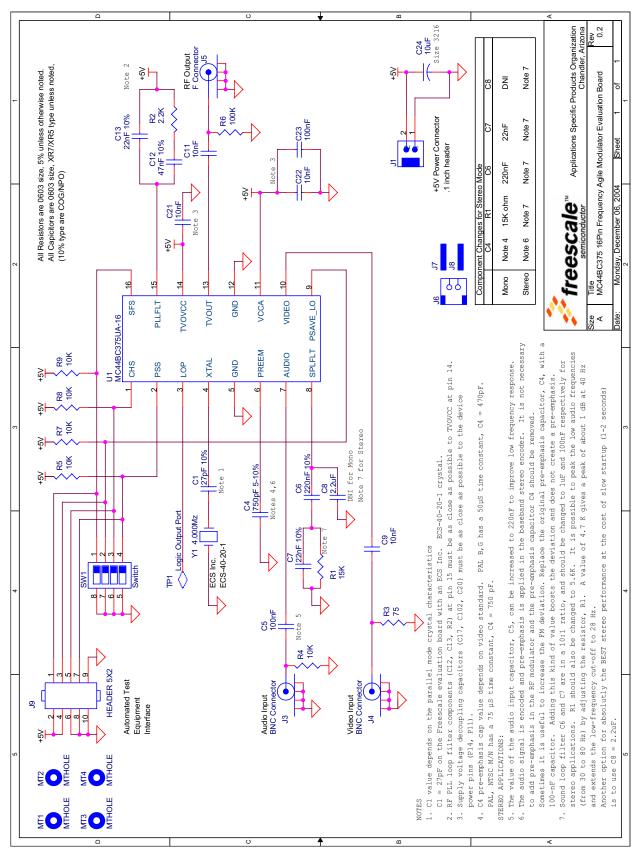
Device and Signals Set-Up	Measurement Set-Up					
Audio Signal to Noise						
This is measured using a Demodulator in B/G and an Audio Analyzer at 1.0 KHz. The output level of the Audio analyzer is varied to obtain a Modulation Deviation of 25 KHz indicated on the AMFS Demodulator.	The Audio Analyzer alternately turns ON and OFF its internal audio source to make a measure of the Audio signal plus noise and then another measure of only the noise.					
	The measurement is made using the internal CCIR468-2 Filter of the Audio Analyzer together with the internal 30+/-2.0 KHz (60 dB/decade) Lowpass filters. The AMFS demodulator uses a quasi-parallel demodulation as is the case in a normal TV set. In this mode the Nyquist filter is bypassed and the video carrier is used without added delay to effectuate intercarrier conversion. In this mode the phase noise information fully cancels out and the true S/N can be measured.					
	ASN(dB) = 20 × log(Signal + Noise)/(Noise)					

# **PIN CIRCUIT SCHEMATICS**





# **APPLICATION SCHEMATIC**

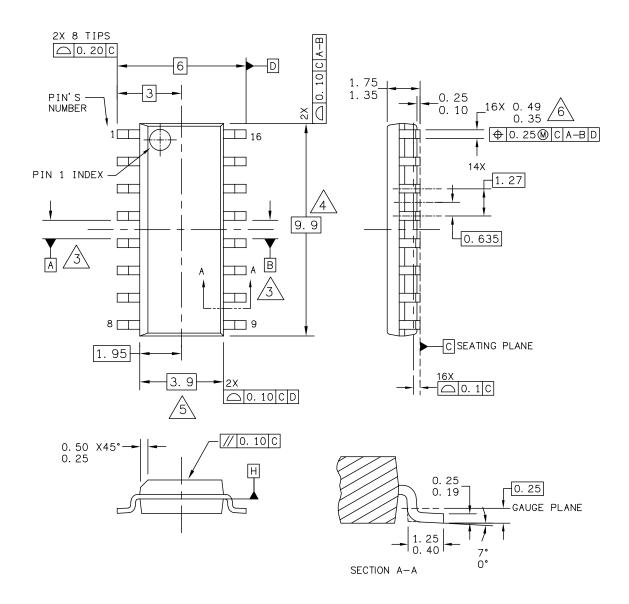




MC44BC375U

Application Specific Products Freescale Semiconductor

### PACKAGE DIMENSIONS



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TITLE:		): 98ASB42566B	REV: M	
16LD SOIC N/B, 1.27 PITCH CASE-OUTLINE		CASE NUMBER	R: 751B-05	06 FEB 2006
	STANDARD: JE	DEC MS-012AC		

Figure 5. SOIC16 Package Dimensions

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#### NOTES:

- 1. DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 🖄 DATUMS A & B TO BE DETERMINED AT DATUM H.
- A this dimension does not include mold flash, protrusion or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 0.15 mm per side.
- THIS DIMENSION DOES NOT INCLUDE INTER-LEAD FLASH OR PROTRUSIONS. INTER-LEAD FLASH AND PROTRUSIONS SHALL NOT EXCEED 0.25 MM PER SIDE. THIS DIMENSION IS DETERMINED AT THE PLANE WHERE THE BOTTOM OF THE LEADS EXIT THE PLASTIC BODY.
- THIS DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL NOT CAUSE THE LEAD WIDTH TO EXCEED 0.62 mm.

© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICA	LOUTLINE	PRINT VERSION NO	DT TO SCALE
TITLE:			): 98ASB42566B	REV: M
16LD SOIC N/B, 1.2 CASE OUTLIN		CASE NUMBER	R: 751B-05	06 FEB 2006
CASE OUTEIN		STANDARD: JE	DEC MS-012AC	

Figure 6. SOIC16 Package Dimensions - part 2

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