## Preliminary Technical Data

## FEATURES

Wideband DC to $\mathbf{2 G H z}$
ADG918/ADG919 Absorptive/Reflective Switches High Off Isolation ( 43 dB @ 1 GHz )
Low Insertion Loss ( 1 dB DC to 900 MHz )
Single 1.65 to 2.75 V power supply
CMOS/LVTTL Control Logic
8 Lead MSOP \& Tiny $3 \times 3 \mathrm{~mm}$ CSP Packages
Low Power Consumption ( $5 \mu \mathrm{~A}$ )

## APPLICATIONS

Wireless Communications
General Purpose RF switching
Dual Band Applications
Filter Selection

## Antenna Switch

Digital Transceiver Front-End Switch
IF Switching
GENERAL DESCRIPTION
The ADG918/ADG919 are wideband switches using a CMOS process to provide high isolation and low insertion loss to 1 GHz . The ADG918 is an absorptive switch having 50 ohm terminated shunt legs, while the ADG919 is a reflective switch. These devices are designed such that the isolation is high over the DC to 1 GHz frequency range. They have on board CMOS control logic, thus eliminating the need for external controlling circuitry. The control inputs are both CMOS and LVTTL compatible. The low power consumption of these CMOS devices makes them ideally suited to wireless applications and general purpose high frequency switching.


Figure 1. Isolation vs Frequency
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FUNCTIONAL BLOCK DIAGRAM


Table 1. Truth Table

| Control | Signal Path |
| :--- | :--- |
| 0 | RF2 to RF Common |
| 1 | RF1 to RF Common |



Figure 2. Loss vs Frequency

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## ADG918/ADG919-SPECIFICATIONS ${ }^{1}$

$\left(\mathrm{V}_{\mathrm{DD}}=+1.65 \mathrm{~V}\right.$ to +2.75 V , $\mathrm{GND}=0 \mathrm{~V}$, All specifications $\mathrm{T}_{\text {MIN }}$ to $\mathrm{T}_{\mathrm{MAX}}$ unless otherwise noted)


## NOTES

${ }^{1}$ Temperature range is as follows: B Version: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$.
${ }^{2}$ Typical values are at $+25^{\circ} \mathrm{C}$ unless otherwise stated.
${ }^{3}$ Guaranteed by design, not subject to production test.
Specifications subject to change without notice.

## ABSOLUTE MAXIMUM RATINGS ${ }^{1}$

$\left(\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$ unless otherwise noted)
$V_{D D}$ to GND
Inputs to GND
Input Power
Operating Temperature Range Industrial (B Version)
Storage Temperature Range
Junction Temperature
MSOP Package $\theta_{\mathrm{JA}}$ Thermal Impedance
CSP Package
$\theta_{\mathrm{JA}}$ Thermal Impedance
Lead Temperature, Soldering (10seconds)
IR Reflow, Peak Temperature ( $<20$ seconds)
$\mathrm{TBD}^{\circ} \mathrm{C} / \mathrm{W}$ $300^{\circ} \mathrm{C}$

## NOTES

${ }^{1}$ Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those listed in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Only one absolute maximum rating may be applied at any one time.

## PIN CONFIGURATION

8-Lead MSOP (RM-8)
3x3mm CSP (CP-8)


## ORDERING GUIDE

| Model | Temperature Range | Package Descriptions | Branding |
| :--- | :--- | :--- | :--- |
| ADG918BRM | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | MSOP (Micro Small Outline Package) |  |
| ADG918BCP | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | Chip Scale Package | RM-8 |
| ADG919BRM | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | MSOP (Micro Small Outline Package) |  |
| ADG919BCP | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | Chip Scale Package | CP-8 |

## CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the ADG919 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.


## OUTLINE DIMENSIONS

Dimensions shown in inches and (mm).
MSOP
(RM-8)


Chip $\underset{\text { (CP-8) }}{\text { Scale Package }}$
$3 \times 3 \mathrm{~mm}$

TBD

