TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

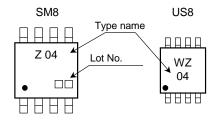
TC7WZ04FU,TC7WZ04FK

Triple Inverter

Features

- High output drive: ±24 mA (min) @VCC = 3 V
- Super high speed operation: t_{pd} 2.3 ns (typ.) @VCC = 5 V, 50 pF
- Operation voltage range: $V_{CC (opr)} = 1.65 \sim 5.5 \text{ V}$
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (JEITA) ±2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V V_{CC}.

Marking



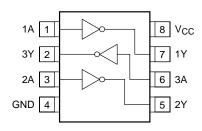
TC7WZ04FU SSOP8-P-0.65 TC7WZ04FK SSOP8-P-0.50A

Weight SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5~6	V	
DC input voltage	V _{IN}	-0.5~6	V	
DC output voltage	V _{OUT}	-0.5~6	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	-20	mA	
DC output current	lout	±50	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	P _D	300 (SM8) 200 (US8)	mW	
Storage temperature	T _{stg}	-65~150	°C	
Lead temperature (10s)	TL	260	°C	

Pin Assignment (top view)





Truth Table

А	Y
L	Н
Н	L

Logic Diagram



Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	1.65~5.5	V	
Supply voltage	VCC	1.5~5.5 (Note 1)	v	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _О Т	0~5.5 (Note 2)	V	
		0~V _{CC} (Note 3)	V	
Operating temperature	T _{opr}	-40~85	°C	
		$0 \sim 20 \; (V_{CC} = 1.8 \; V \pm 0.15 \; V, \\ 2.5 \; V \pm 0.2 \; V)$	ns/V	
Input rise and fall time	d _t /d _V	0~10 (V _{CC} = 3.3 V ± 0.3 V)		
		0~5 (V _{CC} = 5.5 V ± 0.5 V)		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or low state



Electrical Characteristics

DC Characteristics

Characteristics		Symbol	Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
		Syllibol	1650	rest Condition		Min	Тур.	Max	Min	Max	Onit
Input voltage	High level	V _{IH}	_		1.65~ 1.95	0.75 × V _{CC}	_	_	0.75 × V _{CC}		V
					2.3~5.5	0.7 × V _{CC}	_	_	0.7 × V _{CC}		
	Low level	V _{IL}	_		1.65~ 1.95		_	0.25 × V _{CC}		0.25 × V _{CC}	
	Low level				2.3~5.5		_	0.3 × V _{CC}	l	$\begin{array}{c} 0.3 \\ \times \text{V}_{CC} \end{array}$	
					1.65	1.55	1.65		1.55		
				$I_{OH} = -100 \mu A$	2.3	2.2	2.3	_	2.2	_	
				ΙΟΗ - 100 μ/	3.0	2.9	3.0	_	2.9	_	
		Vон			4.5	4.4	4.5	_	4.4	_	
	High level		$V_{IN} = V_{IL}$	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52	_	1.29	_	
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_	
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	_	2.3	_	
Output				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
voltage	Low level	VoL	V _{IN} = V _{IH}	Ι _{ΟL} = 100 μΑ	1.65	_	0	0.1	_	0.1	
					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1		0.1	-
					4.5	_	0	0.1	_	0.1	
				I _{OL} = 4 mA	1.65	_	80.0	0.24	_	0.24	
				$I_{OL} = 8 \text{ mA}$	2.3	_	0.1	0.3		0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4		0.4	
				$I_{OL} = 24 \text{ mA}$	3.0	_	0.22	0.55		0.55	
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage	Input leakage current I_{IN} $V_{IN} = 5.5 \text{ V or GND}$			0~5.5	_	_	±1	_	±10	μΑ	
Power off leakage current		l _{OFF}	V_{IN} or $V_{OUT} = 5.5 \text{ V}$		0.0	_	_	1	_	10	μΑ
Quiescent su	pply current	Icc	V _{IN} = 5.5 V or GND		1.65~5.5	_	_	1	_	10	μΑ



AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	1.8 ± 0.15	1.8	4.4	9.5	2.0	10.0	- ns
			2.5 ± 0.2	1.2	3.0	5.1	1.2	5.6	
			3.3 ± 0.3	0.8	2.2	3.4	0.8	3.8	
			5.0 ± 0.5	0.5	1.8	2.8	0.5	3.1	
	t _{pHL} C _L = 5	$C_{I} = 50 \text{ pF}, R_{I} = 500 \Omega$	3.3 ± 0.3	1.2	2.9	4.5	1.2	5.0	
		CL = 50 pF, KL = 500 12	5.0 ± 0.5	0.8	2.3	3.6	0.8	4.0	
Input capacitance	C _{IN}	_	0~5.5	_	3.0	_	_	_	pF
Power dissipation capacitance	0	(Note)	3.3		18		_		pF
	C _{PD} (Note)		5.5		23	1	_	-	PΓ

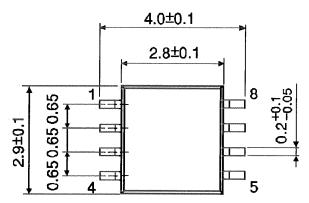
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

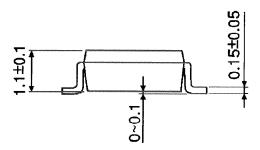
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

Package Dimensions

SSOP8-P-0.65 Unit: mm

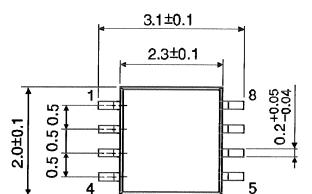




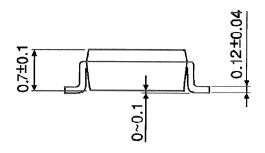
Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A



Unit: mm



Weight: 0.01 g (typ.)

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