

January 2005 Revised June 2005

### FSUSB22

# Low Power 2 Port Hi-Speed USB 2.0 (480Mbps) Switch

#### **General Description**

FSUSB22 is a low power high bandwidth switch specially designed for applications of the switching of high speed USB 2.0 signals in handset and consumer applications such as cell phone, digital camera, and notebook with hubs or controllers of limited USB I/O. The wide bandwidth (750MHz) of this switch allows signals to pass with minimum edge and phase distortion. Superior channel-to-channel crosstalk results in minimal interference. It is compatible with USB2.0 Hi-Speed standard.

#### **Features**

- -40dB OFF Isolation at 250MHz
- -40dB non-adjacent channel crosstalk at 250MHz
- $4.5\Omega$  typical On Resistance (R<sub>ON</sub>)
- -3dB bandwidth: 750MHz
- Low power consumption (1uA max)
- Control input: TTL compatible
- Bidirectional operation
- USB Hi-Speed and Full Speed signaling capability

#### **Applications**

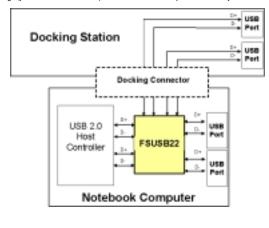
• Cell phone, PDA, digital camera, and notebook

#### **Ordering Code:**

Order Number	Package Number	Package Description
FSUSB22BQX	MLP016E	16-Terminal Depopulated Quad Very-Thin Flat Pack No Leads (DQFN), JEDEC MO-241, 2.5 x 3.5mm
FSUSB22QSC	MQA16	16-Lead Quarter Size Outline Package (QSOP), JEDEC MO-137, 0.150" Wide
FSUSB22QSCX_NL (Note 1)	MQA16	Pb-Free 16-Lead Quarter Size Outline Package (QSOP), JEDEC MO-137, 0.150" Wide
FSUSB22MTC	MTC16	16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide
FSUSB22MTCX_NL (Note 1)	MTC16	Pb-Free 16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

Devices also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering code. Pb-Free package per JEDEC J-STD-020B.

Note 1: "\_NL" indicates Pb-Free package (per JEDEC J-STD-020B). Device available in Tape and Reel only.

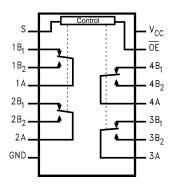


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# FSUSB22

# **Analog Symbol**



# **Pin Descriptions**

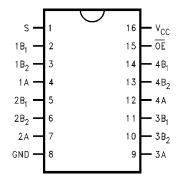
Pin Name	Description
ŌĒ	Bus Switch Enable
S	Select Input
A	Bus A
B <sub>1</sub> -B <sub>2</sub>	Bus B

#### **Truth Table**

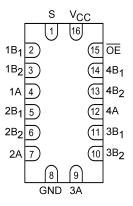
s	OE	Function				
Х	Н	Disconnect				
L	L	A = B <sub>1</sub>				
Н	L	A = B <sub>2</sub>				

# **Connection Diagrams**

Pin Assignments for QSOP and TSSOP



Pad Assignments for DQFN



#### **Absolute Maximum Ratings**(Note 2)

 $\begin{array}{lll} \mbox{Supply Voltage (V$_{CC}$)} & -0.5\mbox{V to } +4.6\mbox{V} \\ \mbox{DC Switch Voltage (V$_{S}$)} & -0.5\mbox{V to } V_{CC} +0.05\mbox{V} \\ \end{array}$ 

DC Switch Voltage (V<sub>S</sub>)  $-0.5 \text{V to V}_{\text{CC}} + 0.05 \text{V}$  DC Input Voltage (V<sub>IN</sub>) (Note 3) -0.5 V to +4.6 V

DC V<sub>CC</sub>/GND Current (I<sub>CC</sub>/I<sub>GND</sub>)  $\pm 100$  mA Storage Temperature Range (T<sub>STG</sub>)  $-65^{\circ}$ C to +150 °C

ESD

Human Body Model

# Recommended Operating Conditions (Note 4)

Power Supply Operating (V<sub>CC</sub>) 3.0V to 3.6V

Input Voltage ( $V_{IN}$ ) 0V to  $V_{CC}$ Output Voltage ( $V_{OUT}$ ) 0V to  $V_{CC}$ 

Input Rise and Fall Time (t<sub>r</sub>, t<sub>f</sub>)

Switch Control Input 0 ns/V to 5 ns/V
Switch I/O 0 ns/V to DC

Free Air Operating Temperature ( $T_A$ )  $-40~^{\circ}C$  to  $+85~^{\circ}C$ 

4kV Note 2: The Absolute Maximum Ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum rating. The Recommended Operating Conditions tables will define the conditions for actual device operation.

**Note 3:** The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Note 4: Unused control inputs must be held HIGH or LOW. They may not float

#### **DC Electrical Characteristics**

		v <sub>cc</sub>	T <sub>A</sub> =	–40 °C to +	85 °C		
Symbol	Parameter	(V)	Min	Typ (Note 5)	Max	Units	Conditions
V <sub>IK</sub>	Clamp Diode Voltage	3.0			-1.2	V	I <sub>IN</sub> = -18 mA
V <sub>IH</sub>	HIGH Level Input Voltage	3.0 - 3.6	2.0			V	
V <sub>IL</sub>	LOW Level Input Voltage	3.0 - 3.6			0.8	V	
I <sub>I</sub>	Input Leakage Current	3.6			±1.0	μА	$0 \le V_{IN} \le 3.6V$
I <sub>OFF</sub>	OFF-STATE Leakage Current	3.6			±1.0	μА	$0 \le A, B \le V_{CC}$
R <sub>ON</sub>	Switch On Resistance (Note 6)	3.0		5.0	7.0	Ω	$V_{IN} = 0.8V$ $I_{ON} = 8 \text{ mA}$
		3.0		4.5	6.5	Ω	V <sub>IN</sub> = 3.0V I <sub>ON</sub> = 8 mA
$\Delta R_{ON}$	Delta R <sub>ON</sub>	3.0		0.3		Ω	$V_{IN} = 0.8V$ , $V_{IN} = 0V - 1.5V$ , $I_{ON} = 8 \text{ mA}$
R <sub>FLAT(ON)</sub>	On Resistance Flatness (Note 7)	3.0		1.0		Ω	I <sub>OUT</sub> = 8 mA
I <sub>CC</sub>	Quiescent Supply Current	3.6			1.0	μА	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$

Note 5: Typical values are at V<sub>CC</sub> = 3.0V and T<sub>A</sub> = +25 °C

Note 6: Measured by the voltage drop between A and B pins at the indicated current through the switch. On Resistance is determined by the lower of the voltages on the two (A or B) pins.

Note 7: Flatness is defined as the difference between the maximum and minimum value On Resistance over the specified range of conditions.

# **AC Electrical Characteristics**

Symbol	Parameter	V <sub>CC</sub> (V)	$T_A = -40$ °C to $+85$ °C				Figure	
			Min	Typ (Note 8)	Max	Units	Conditions	Number
t <sub>ON</sub>	Turn ON Time S-to-Bus B	3.0 to 3.6		4.5	6.0	ns		Figures 5, 6
t <sub>OFF</sub>	Turn OFF Time S-to-Bus B	3.0 to 3.6		2.5	4.0	ns		Figures 5, 6
t <sub>PD</sub>	Propagation Delay	3.0 to 3.6		0.25		ns	C <sub>L</sub> = 10 pF	Figure 10
O <sub>IRR</sub>	Non-Adjacent OFF-Isolation	3.0 to 3.6		-30.0		dB	$f = 250MHz, R_L = 50\Omega$	Figure 7
X <sub>TALK</sub>	Non-Adjacent Channel Crosstalk	3.0 to 3.6		-38.0		dB	$R_L = 50\Omega$ , $f = 250MHz$	Figure 8
BW	-3dB Bandwidth	3.0 to 3.6		750		MHz	$R_L = 50\Omega$	Figure 9

Note 8: Typical values are at  $V_{CC} = 3.3V$  and  $T_A = +25^{\circ}C$ 

## **USB Related AC Electrical Characteristics** (Note 9)

Symbol	Parameter	V <sub>CC</sub>	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	Figure	
		(V)	Min	Тур	Max	Oilita	Conditions	Number
t <sub>SK(O)</sub>	Channel-to-Channel Skew	3.0 to 3.6		0.051		ns	C <sub>L</sub> = 10 pF	Figures 10, 11
t <sub>SK(P)</sub>	Skew of Opposite Transition of the Same Output	3.0 to 3.6		0.020		ns	C <sub>L</sub> = 10 pF	Figures 10, 11
TJ	Total Jitter	3.0 to 3.6		0.210			$R_L = 50\Omega$ , $C_L = 10 \text{ pF}$ $t_R = t_F = 750 \text{ps}$ at 480 Mbps	

Note 9: Typical values are at  $V_{CC} = 3.3V$  and  $T_A = +25^{\circ}C$ 

#### Capacitance (Note 10)

Symbol	Parameter	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	Units	Conditions
	r al allietei	Тур	Onits	
C <sub>IN</sub>	Control Pin Input Capacitance	2.5	pF	V <sub>CC</sub> = 0V
C <sub>ON</sub>	A/B ON Capacitance	12.0	pF	V <sub>CC</sub> = 3.3V, <del>OE</del> = 0V
C <sub>OFF</sub>	Port B OFF Capacitance	4.5	pF	V <sub>CC</sub> and $\overline{OE}$ = 3.3V

Note 10: Typical values are at  $V_{CC} = 3.3V$  and  $T_A = +25^{\circ}C$ 

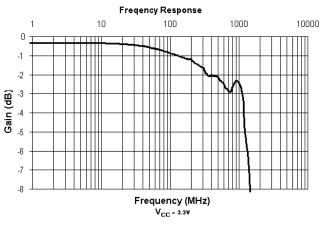
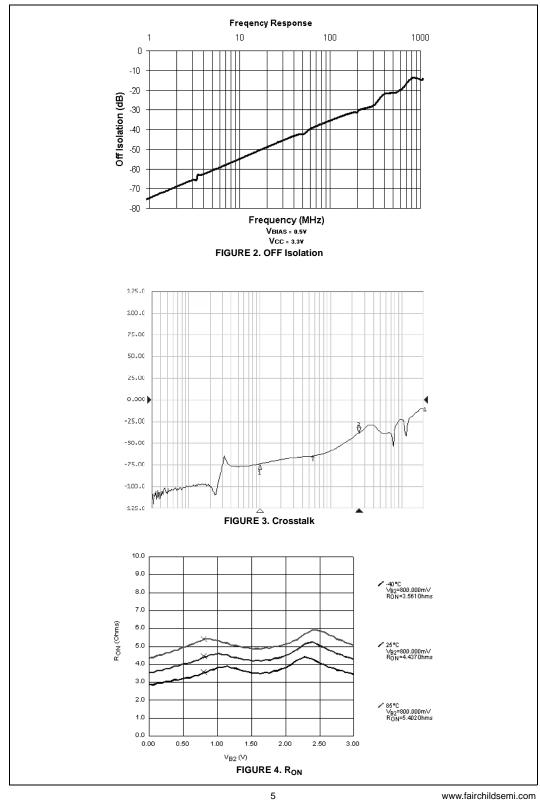
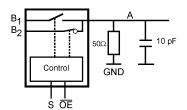


FIGURE 1. Gain vs. Frequency



# **AC Loading and Waveforms**



Note: Input driven by  $50\Omega$  source terminated in  $50\Omega$  Note:  $C_L$  includes load and stray capacitance

Note: Input PRR = 1.0 MHz,  $t_W$  = 500 ns

#### FIGURE 5. AC Test Circuit

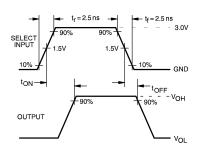


FIGURE 6. AC Waveforms

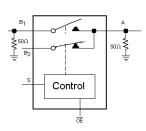


FIGURE 7. OFF Isolation Test

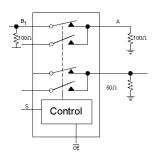


FIGURE 8. Crosstalk Test

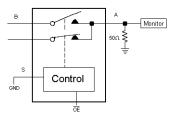
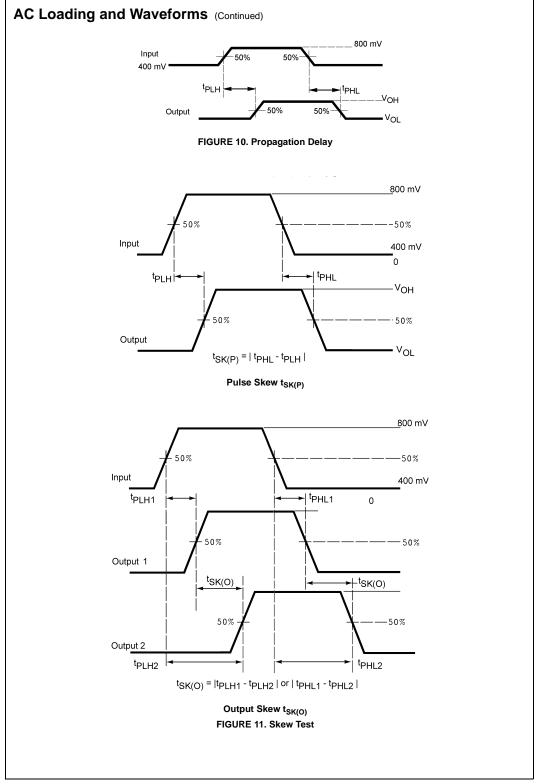
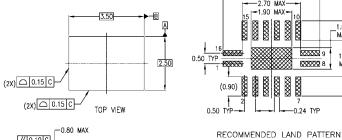


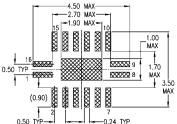
FIGURE 9. Bandwidth Test

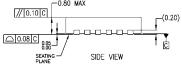


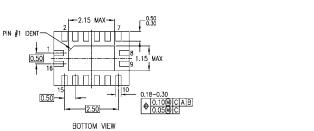
#### **Tape and Reel Specification** Tape Format for DQFN Package Number Tape Cavity Cover Tape Designator Section Cavities Status Status Leader (Start End) 125 (typ) Sealed Empty BQX2500/3000 Filled Sealed Carrier Trailer (Hub End) 75 (typ) **Empty** Sealed TAPE DIMENSIONS inches (millimeters) Во Tc D<sub>1</sub> Ao User Direction of Feed Dimensions are in millimeters P<sub>1</sub> TYP Wc TYP Package Ko +/-0.1 TYP +/-0.3 8 +/-0.10 min 1.0 TYP +/-0.005 +/-0.1 2.30 0.3 2.0 2.0 2.0 2.0 2.0 2.0 2.0 2.0 1.55 1.55 5.5 5.5 2.5x2.5 2.5x3.0 2.80 2.80 2.80 3.30 1.5 1.5 1.75 1.75 0.9 0.3 0.3 0.07 12 12 12 12 12 12 12 9.3 9.3 0.07 1.55 1.55 1.75 1.75 0.9 0.3 9.3 9.3 2.5x3.5 2.80 3.80 0.07 1.5 1.5 1.5 1.5 2.5x4.5 2.80 4.80 5.5 0.07 3.5x4.5 1.75 1.75 1.75 0.9 0.9 1.1 0.3 0.3 0.3 2.80 1.55 5.5 2.5x3.0 3.30 0.07 9.3 4.35 4.35 1.55 9.3 4 x 4 0.07 5 x 5 6 x 6 5.35 6.30 5.35 6.30 1.55 1.55 1.75 1.75 1.1 1.1 2.0 2.0 0.07 0.07 12 16 9.3 13.3 8 12 Notes: Ao, Bo, and Ko dimensions are determined with respect to the EIA /Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C). 1.0 mm maxim<u>um</u> → Typical component cavity center line 1.0 mm Typical component maximu 10 deg maximum component rotation center line Sketch A (Side or Front Sectional View) Component Rotation Sketch C (Top View) Component lateral movement Sketch B (Top View) Component Rotation W1 Measured at Hub **Shipping Reel Dimension** W2 max Measured at Hub **B** Min Dia D Dia A Dia N max DETAIL AA See detail AA W3 Dimensions are in millimeters Tape Width Dia A Dim B Dia D Dim W1 Dim W2 Dim W3 Dia C Dia N .5/-.2 +2/-0 LSL - USL) 8 330 1.5 13 20.2 178 8.4 7.9~10.4 11.9~15.4 12 330 1.5 13 20.2 178 12.4 18.4 330 20.2 16 13 178 16.4 22.4 15.9~19.4 1.5

# Physical Dimensions inches (millimeters) unless otherwise noted







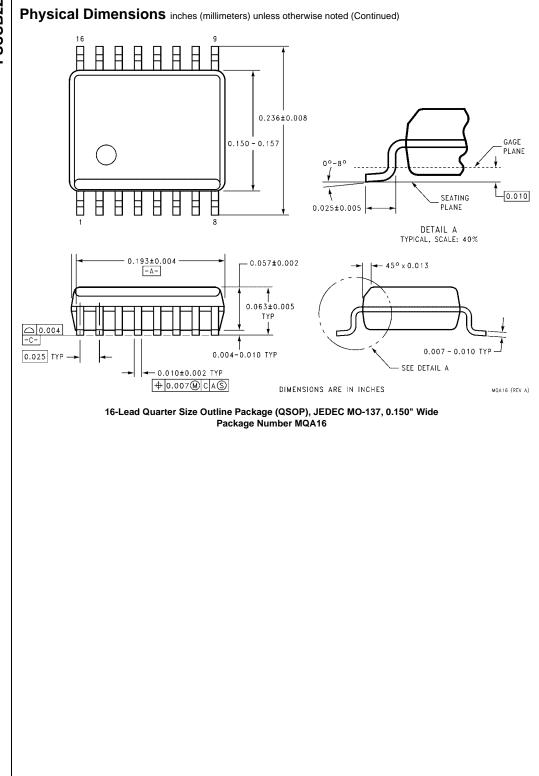


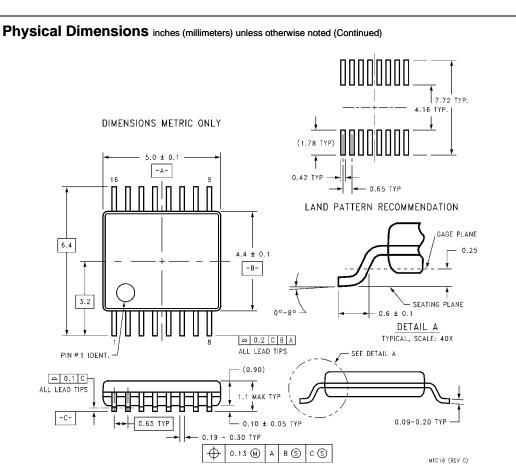
#### NOTES:

- CONFORMS TO JEDEC REGISTRATION MO-241, VARIATION AB
   DIMENSIONS ARE IN MILLIMETERS.
   DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994

MLP016ErevA

16-Terminal Depopulated Quad Very-Thin Flat Pack No Leads (DQFN), JEDEC MO-241, 2.5 x 3.5mm Package Number MLP016E





16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide Package Number MTC16

#### **Technology Description**

The Fairchild Switch family derives from and embodies Fairchild's proven switch technology used for several years in its 74LVX3L384 (FST3384) bus switch product.

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