

Chip tantalum capacitors (Bottom surface electrode type)

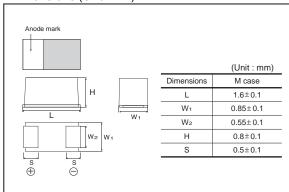
TC Series M Case

●Features (M)

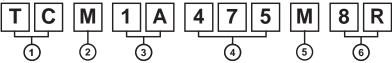
With an original bottom surface electrode structure.

- 1) Excellent adhesion.
- 2) Easy visual recognition of fillets.
- 3) Large capacitance, low ESR.

●Dimensions (Unit: mm)



●Part No. Explanation



- 1 Series name
- 2 Case style
- (3) Rated voltage

١	Rated voltage (V)	2.5	4	6.3	10	16
ı	CODE	0E	0G	0J	1A	1C

(4) Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

(5) Capacitance tolerance

M: ±20%

- 6 Taping
 - 8 : Tape width
 - R : Positive electrode on the side opposite to sprocket hole

Rated table

		ı	Rated vo	ltage (V)	
(μF)	2.5 0E	4 0G	6.3 0J	10 1A	16 1C	20 1D
0.47 (474)			M*	М*		
0.68 (684)						
1.0 (105)				М	М	
1.5 (155)						
2.2 (225)				М	М	
3.3 (335)						
4.7 (475)		М	М	М		
6.8 (685)						
10 (106)		М	М	М		
15 (156)						
22 (226)		М	M*			
33 (336)		М				
47 (476)	М*					
68 (686)						

Remark) Case size codes (M) in the above show products line-up.

Marking

The indications listed below should be given on the surface of a capacitor.

- (1) Polarity : The polarity should be shown by □ bar. (on the anode side)
 (2) Rated DC voltage : Due to the small size of M case, a voltage code is used as shown below.
 (3) Visual typical example (1) voltage code (2) capacitance code

Voltage Code	Rated DC Voltage (V)
е	2.5
g	4
j	6.3
А	10
С	16

Capacitance	Nominal			
Code	Capacitance (μF)			
<u>s</u>	0.47			
W	0.68			
А	1.0			
Е	1.5			
J	2.2			
N	3.3			
S	4.7			
W	6.8			
а	10			
е	15			
j	22			
n	33			
S	47			

[M case] note 1) $\frac{A}{(1)}$ $\frac{s}{(2)}$



note 2) voltage code and capacitance code are variable with parts number

^{*} Under development

Characteristics

Characteri	รแ เ ร												
Item			Performance					Test conditions (based on JIS C 5101–1 and JIS C 5101–3)					
Operating Temp	oerature	-55	s°C t	to +12	5°C		Volt	Voltage reduction when temperature exceeds +85°C					
Maximum operating temperature with no voltage derating			+85°C										
Rated voltage (VDC)			2.5 4 6.3 10 16			at 8	at 85°C						
Category voltage (VDC)			1.6 2.5 4 6.3 10				at 1:	25°C					
Surge voltage (VDC)			3.2 5.2 8 13 20					at 85°C					
DC Leakage current			" Standard list "			As p	As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage : Rated voltage for 5min						
Capacitance tolerance			Shall be satisfied allowance range. ±20%			As p Mea Mea	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5.DC Measuring circuit : DC Equivalent series circuit						
Tangent of loss angle (Df, $\tan \delta$)			Shall be satisfied the voltage on "Standard list"				As p Mea Mea	As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5.DC Measuring circuit : DC Equivalent series circuit					
Impedance	Shall be satisfied the voltage on "Standard list"			As p Mea Mea	As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit								
Resistance to Soldering heat	Appearance	There should be no significant abnormality. The indications should be clear.					Asp	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3 Dip in the solder bath Solder temp : 260±5°C					
	L.C.	Less than 200% of initial limit				of initial limit							
	ΔC / C	Within ±20% of initial value				itial value		Duration : 5±0.5s					
	Df (tan δ)		s th	an 200)% c	of initial limit	Afte	 Repetition : 1 After the specimens, leave it at room temperature for over 24h and then measure the sample. 					
Temperature cycle	Appearance	There should be no significant abnormality. The indications should be clear.						As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3					
	L.C.	Less than 200% of initial limit						Repetition: 5 cycles (1 cycle: steps 1 to 4) without discontinuation.					
	ΔC / C	Wit	hin ±	±20%	of in	itial value			Temp. Time				
	Df (tan δ)	Les	s th	an 200)% (of initial limit		1	−55±3°C 30±3min.				
	, ,							2	Room temp. 3min.or less				
								3	125±2°C 30±3min.				
								4	Room temp. 3min.or less				
						After the specimens, leave it at room temperature for over 24h and then measure the sample.							
Moisture resistance	Appearance					no significant abnormal nould be clear.	ty. As p	oer 4.	22 JIS C 5101-1 12 JIS C 5101-3				
	L.C.	Les	s th	an 200)% (of initial limit			ving the sample under such atmospheric that the temperature and humidity are				
	ΔC / C	Wit	hin ±	±20%	of in	itial value			and 90 to 95% RH,respectively, for 500±12h				
	Df (tan δ)	Within ±20% of initial value Less than 200% of initial limit				of initial limit	tem	leave it at room temperature for over 24h and then measure the sample.					

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Iter	n	Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3)			
Temperature	Temp.	_55°C	As per 4.29 JIS C 5101-1 As per 4.13 JIS C 5101-3			
Stability	ΔC / C	TCM0G336□: Within 0/–30% of initial value Others: Within 0/–15% of initial value	1 AS per 4.13 JIS C 5101-3			
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "				
	L.C.	_				
	Temp.	+85°C				
	ΔC / C	TCM0G336□: Within +15/-5% of initial value Others: Within +15/0% of initial value				
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "				
	L.C.	TCM0G336□: Less than 1.0CV Others: 5µA or 0.1CV whichever is greater				
	Temp.	+125°C				
	ΔC / C TCM0G336□: Within +20/-5% of initial value Others: Within +20/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "				
	L.C.	TCM0G336□: Less than 1.25CV Others: 6.3µA or 0.125CV whichever is greater				
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1			
	L.C.	Less than 200% of initial limit	As per 4.14JIS C 5101-3 Apply the specified surge voltage every 5±0.5 min.			
	ΔC / C	Within ±20% of initial value	for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times.			
	Df (tan δ)	Less than 200% of initial limit	After the specimens, leave it at room temperature for over 24h and then measure the sample.			
Loading at	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1			
High temperature	L.C.	Less than 200% of initial limit	As per 4.15 JIS C 5101-3 After applying the rated voltage for 1000+36/0 h without			
	ΔC / C	TCM0G336□: Within ±30% of initial value Others: Within ±20% of initial value	discontinuation via the serial resistance of 3Ω or less at a temperature of 85±2°C, leave the sample at room temperature / humidity for over 24h and measure the value.			
	Df (tan δ)	Less than 200% of initial limit				
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1			
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure below) (Unit: mm) F (Apply force) thickness=1.6mm			

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Ite	em	Performance	Test conditions (JIS C 5101-1 and JIS C 5101-3)		
Adhesivene	ess	The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.		
Dimensions	3	Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.		
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.		
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed=25±2.5mm / s Pre-treatment(accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration: 3±0.5s Solder: M705 Flux: Rosin 25% IPA 75%		
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm		
Appeara		There should be no significant abnormality.	Time: 2h each in X and Y directions Mounting: The terminal is soldered on a print circuit board.		

• Standard products list, TC series M case

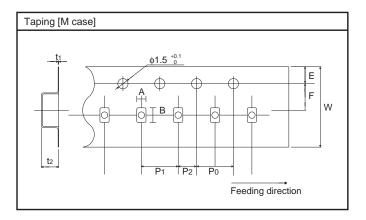
Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	oltage 120Hz 1		Leakage current 25°C			Impedance 100kHz	
	(V)	(V)	(V)	(μF)	(%)	1WV.300s (μA)	–55°C	25°C 85°C	125°C	(Ω)
TC M 0G 475□	4	2.5	5.2	4.7	±20	0.5	30	20	30	9.0
TC M 0G 106□	4	2.5	5.2	10	±20	0.5	30	20	30	9.0
TC M 0G 226□	4	2.5	5.2	22	±20	0.9	30	20	30	9.0
TC M 0G 336□	4	2.5	5.2	33	+20	13.0	60	30	40	9.0
TC M 0J 474□	6.3	4	8	0.47	±20	0.5	15	10	15	15.0
TC M 0J 475□	6.3	4	8	4.7	±20	0.5	30	20	30	9.0
TC M 0J 106□	6.3	4	8	10	±20	0.6	30	20	30	9.0
TC M 0J 226□	6.3	4	8	22	±20	13.0	60	30	40	9.0
TC M 1A 474□	10	6.3	13	0.47	±20	0.5	15	10	15	15.0
TC M 1A 105□	10	6.3	13	1.0	±20	0.5	15	10	15	15.0
TC M 1A 225□	10	6.3	13	2.2	±20	0.5	30	20	30	13.5
TC M 1A 475□	10	6.3	13	4.7	±20	0.5	30	20	30	9.0
TC M 1A 106□	10	6.3	13	10	±20	10.0	30	20	30	9.0
TC M 1C 105□	16	10	20	1.0	±20	0.5	15	10	15	15.0
TC M 1C 225□	16	10	20	2.2	±20	0.5	30	20	30	13.5

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 \square =Tolerance (M: $\pm 20\%$)

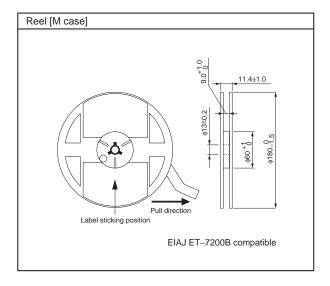
Packaging specifications

Ì	Case code	A±0.1	B±0.1	W±0.2	E±0.1	F±0.05	P₁±0.1	P ₂ ±0.05	Po±0.1	t1±0.05	t2±0.1
ı	М	1.0	1.8	8.0	1.75	3.5	4.0	2.0	4.0	0.20	1.0



Packaging style

Case code	Packaging	Packag	ging style	Symbol	Basic ordering units
M case	Taping	plastic taping	∮180mm Reel	R	4,000pcs



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