Features



Low-Power Digital Potentiometers

General Description

The MAX5160/MAX5161 linear-taper digital potentiometers perform the same function as a mechanical potentiometer or a variable resistor. They consist of a fixed resistor and a wiper contact with 32 tap points that are digitally controlled by three lines for the 8-pin MAX5160 or by two lines for the 6-pin MAX5161.

These parts are ideal for applications requiring digitally controlled resistors. Three resistance values are available for each part type: $50k\Omega$, $100k\Omega$, and $200k\Omega$. A nominal resistor temperature coefficient of 50ppm/°C end-to-end and only 5ppm/°C ratiometric makes the MAX5160 ideal for applications requiring a low-temperature-coefficient variable resistor, such as low-tempco, adjustable-gain circuit configurations.

The MAX5160 is available in an 8-pin µMAX package, and the MAX5161 is available in a 6-pin SOT23 package. Both devices are guaranteed over the extendedindustrial temperature range (-40°C to +85°C).

♦ 32 Tap Positions

- ♦ 50kΩ, 100kΩ, and 200kΩ Resistance Values
- **♦ 400**Ω Wiper Resistance
- ♦ ±25% Resistance Tolerance
- ♦ 3-Wire Serial Data Input
- ♦ ±1LSB DNL
- ♦ ±0.5LSB INL
- **♦ 100nA Supply Current**
- ♦ Single-Supply Operation: +2.7V to +5.5V
- ♦ Power-On Reset: Wiper Goes to Midscale (position 16)
- ♦ ±2kV ESD Protection
- ♦ Small-Footprint Packages 6-Pin SOT23 (MAX5161) 8-Pin µMAX (MAX5160)
- **♦** Glitchless Switching Between the Resistor Taps

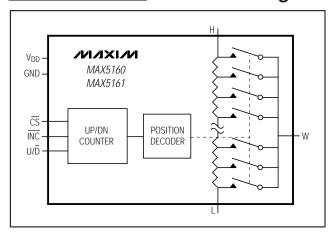
Applications

LCD Screen Adjustment Volume Control Mechanical Potentiometer Replacement

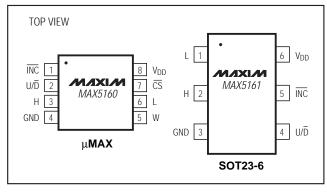
Ordering Information

PART	TEMP. RANGE	PIN- PACKAGE	R (kΩ)
MAX5160NEUA	-40°C to +85°C	8 µMAX	200
MAX5160MEUA	-40°C to +85°C	8 µMAX	100
MAX5160LEUA	-40°C to +85°C	8 µMAX	50
MAX5161NEUT	-40°C to +85°C	6 SOT23-6	200
MAX5161MEUT	-40°C to +85°C	6 SOT23-6	100
MAX5161LEUT	-40°C to +85°C	6 SOT23-6	50

Functional Diagram



Pin Configurations



NIXIN

Maxim Integrated Products 1

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ABSOLUTE MAXIMUM RATINGS

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{DD} = +2.7V \text{ to } +5.5V, V_{H} = V_{DD}, V_{L} = 0, T_{A} = T_{MIN} \text{ to } T_{MAX}. \text{ Typical values are at } V_{DD} = 5V, T_{A} = +25^{\circ}C, \text{ unless otherwise noted.})$

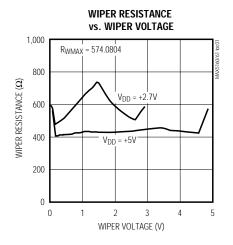
PARAMETER	SYMBOL	CON	DITIONS	MIN	TYP	MAX	UNITS
DC PERFORMANCE	•						
Resolution					3		%
Integral Nonlinearity (Note 1)	INL					±1/2	LSB
Differential Nonlinearity (Note 1)	DNL					±1	LSB
End-to-End Resistor Tempco	TCR				50		ppm/°C
Ratiometric Resistor Tempco					5		ppm/°C
Full-Scale Error						-0.1	LSB
Zero-Scale Error						+0.1	LSB
Wiper Resistance	Rw				400	1700	Ω
Wiper Capacitance	Cw				10		pF
		MAX516_NE		150	200	250	
End-to-End Resistance	HL	MAX516_ME		75	100	125	kΩ
		MAX516_LE		37.5	50	62.5	
DIGITAL INPUTS	'			<u>'</u>			
Input High Voltage	VIH			0.7 • V _{DD})		V
Input Low Voltage	VIL					0.3 • V _{DD}	V
Input Leakage Current						±1	μΑ
Input Capacitance					5		pF
TIMING CHARACTERISTICS (F	igure 6)						
CS to INC Setup Time	tcı			25			ns
CS to INC Hold Time	tıc			0			ns
INC Low Period	tıL			25			ns
INC High Period	tıH			25			ns
U/D to INC Hold	tiD			0			ns
U/D to INC Setup	t _{DI}			50			ns
Wiper-Settling Time	tıw				1		μs
INC Frequency	fIMAX					7	MHz
POWER SUPPLIES	1	I					
Supply Voltage	V _{DD}			2.7		5.5	V
Supply Current	1	$\overline{CS} = \overline{INC} = U/\overline{D} =$	$V_{DD} = 5V$		0.6	10	μΑ
	IDD	V _{DD} or GND	$V_{DD} = 2.7V$		135		nA

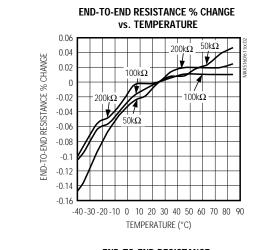
Note 1: For the MAX5160, linearity is defined in terms of H to L code-dependent resistance.

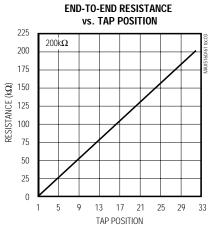
2 ______ /N/XI/M

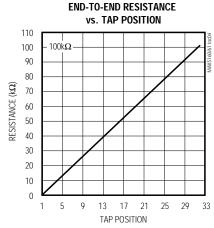
Typical Operating Characteristics

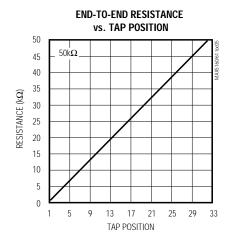
 $(V_{DD} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$







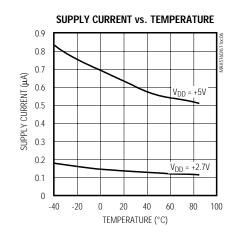


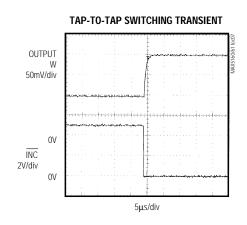


MIXIM

Typical Operating Characteristics (continued)

 $(V_{DD} = +5V, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$





Pin Description

PIN		NAME	FUNCTION	
MAX5160	MAX5161	NAIVIE	FUNCTION	
1	5	ĪNC	Wiper Increment Control Input. With $\overline{\text{CS}}$ low, a high-to-low transition increments (U/ $\overline{\text{D}}$ high) or decrements (U/ $\overline{\text{D}}$ low) the wiper position.	
2	4	U/D	Up/Down Control Input. With $\overline{\text{CS}}$ low, a high-to-low $\overline{\text{INC}}$ transition increments (U/ $\overline{\text{D}}$ high) or decrements (U/ $\overline{\text{D}}$ low) the wiper position.	
3	2	Н	High Terminal of Resistor	
4	3	GND	Ground	
5	_	W	Wiper Terminal of Resistor	
6	1	L	Low Terminal of Resistor	
7	_	CS	Chip-Select Input. Drive low to change the wiper position via $\overline{\text{INC}}$ and $\overline{\text{U/D}}$.	
8	6	V _{DD}	Power Supply	

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Detailed Description

The MAX5160/MAX5161 consist of resistor arrays with 31 resistive elements. Thirty-two tap points are accessible to the wiper along the resistor string between H and L. Logic inputs \overline{CS} , U/\overline{D} , and \overline{INC} determine the position of the wiper. With \overline{CS} low and U/\overline{D} high, a high-to-low transition on \overline{INC} increments the internal counter, increasing the resistance between W and L. When both \overline{CS} and U/\overline{D} are low, a high-to-low \overline{INC} transition decrements the internal counter, decreasing the resistance between W and L. At either end (maximum or minimum positions), additional transitions in the direction of the end points will not change the counter value (the counter will not wrap around).

The H and L terminals of the MAX5160 are similar to the two end terminals of a mechanical potentiometer. The tap W is equivalent to the variable tap (wiper) of the potentiometer.

The MAX5161 is similar to the MAX5160 except that $\overline{\text{CS}}$ internally connects to ground and the wiper terminal (W) is shorted to the high terminal (H). The MAX5161 acts as

a variable resistor (a potentiometer with the wiper and one end terminal shorted together).

The MAX5160/MAX5161 feature power-on reset circuitry that sets the wiper position to midscale at power-up.

Applications Information

The MAX5160/MAX5161 are intended for circuits requiring digitally controlled adjustable voltage or adjustable gain, such as LCD contrast control, where voltage biasing adjusts the display contrast.

Controlling a Switch-Mode LCD Bias Generator

Figure 1 shows an application where the MAX5161 is used with a MAX1771 to make an adjustable positive LCD-bias circuit. The output of the MAX1771 is a positive voltage that is digitally controlled through the MAX5160/MAX5161. Similarly, Figure 2 shows the application of the MAX5161 in a digitally controlled negative LCD-bias circuit along with the MAX774/MAX775/MAX776.

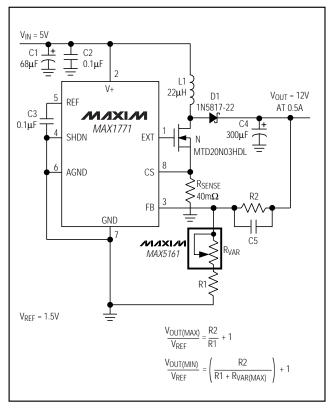


Figure 1. Adjustable Positive LCD Bias

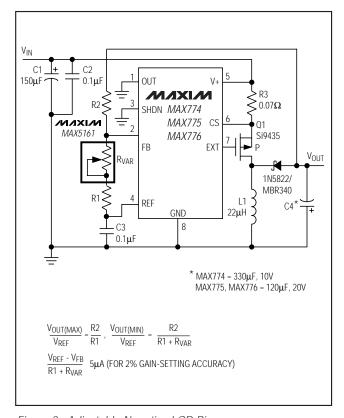


Figure 2. Adjustable Negative LCD Bias



Alternative Positive LCD Bias Control

Alternatively, use an op amp to provide buffering and gain to the output of the MAX5160/MAX5161. Connect the MAX5160 to the positive input of a noninverting op amp (Figure 3) to select a portion of the input signal by digitally controlling the wiper terminal. Figure 4 shows a similar circuit for the MAX5161.

Adjustable Gain

Figure 5 shows how to use the MAX5161 to digitally adjust the gain of a noninverting op amp configuration. Connect the MAX5161 in series with a resistor to ground to form the adjustable gain control of a noninverting amplifier. The MAX5160/MAX5161 have a low 5ppm/°C ratiometric tempco that allows for a very stable adjustable gain configuration over temperature.

Serial Interface

Figure 6 is the serial-interface timing diagram.

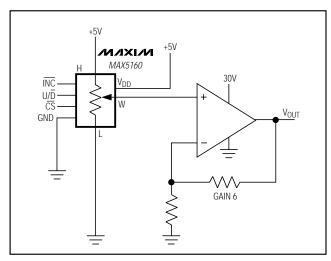


Figure 3. MAX5160 Positive LCD Bias Control

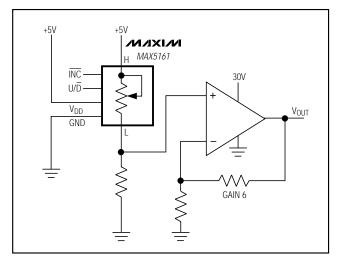


Figure 4. MAX5161 Positive LCD Bias Control

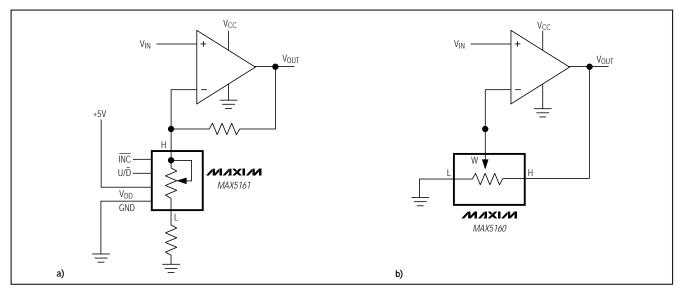


Figure 5. Adjustable Gain Circuit: a) MAX5161; b) MAX5160

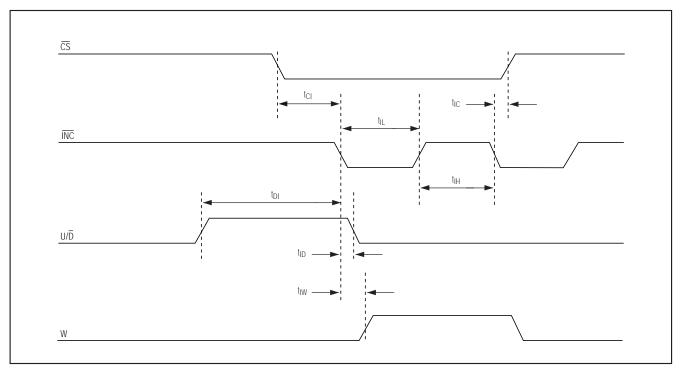


Figure 6. Serial-Interface Timing Diagram

Truth Table

CS	U/D	ĪNC	Rw
Н	xx-X	X	0
L	L	1	0
L	Н	1	0
L	L	\	_
L	Н	\	+

X = Don't care

O = Previous state

+ = Increment

- = Decrement

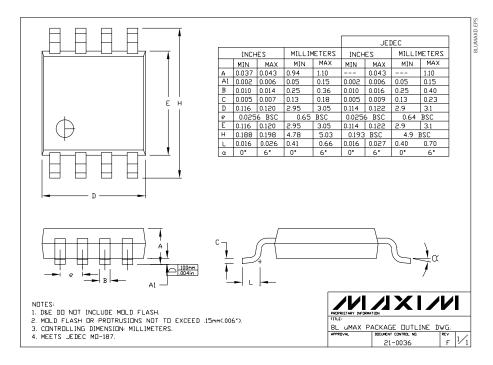
↓ = High-to-Low Transition

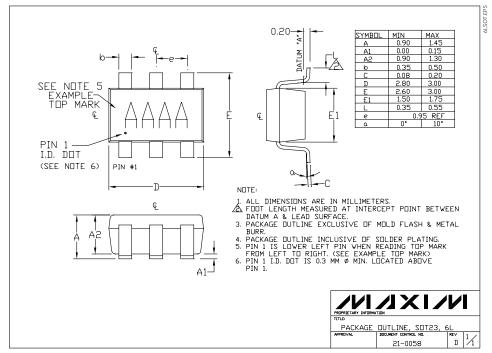
↑ = Low-to-High Transition

_Chip Information

TRANSISTOR COUNT: 969

Package Information





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