

HD74LV1GW57A

Configurable Multiple-Function Gate

REJ03D0081-0200 Rev.2.00 May 19, 2006

Description

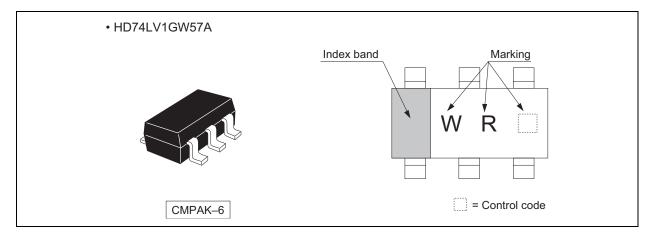
The HD74LV1GW57A has configurable multiple—function gate in a 6 pin package. The Output state is determined by eight patterns of 3—bit input. The user can choose the logic functions AND, NAND, NOR, EX–NOR. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range: 1.65 to 5.5 V Operating temperature range: -40 to +85°C
- $\begin{array}{l} \bullet \quad \mbox{All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 \ V \ to 5.5 \ V)$} \\ \mbox{All outputs V_O (Max.) = 5.5 V (@V_{CC} = 0 \ V)$} \end{array}$
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LV1GW57ACME	ICMPAK-6 pin	PTSP0006JA-A (CMPAK-6V)	СМ	E (3,000 pcs / Reel)	

Outline and Article Indication

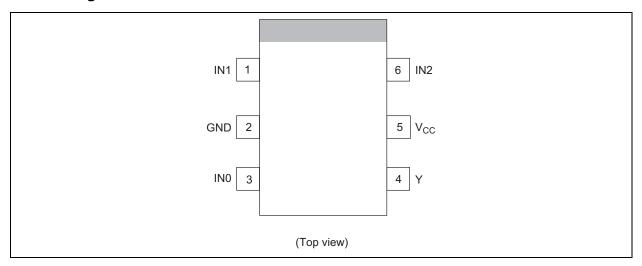


Function Table

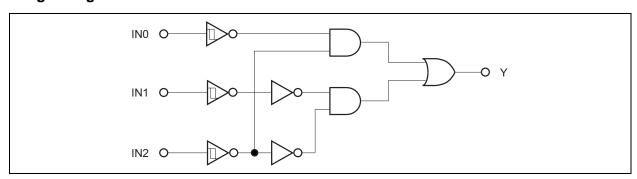
	Inputs		Output
IN2	IN1	IN0	Υ
L	L	L	Н
L	L	H	L
L	Н	L	Н
L	Н	Н	L
Н	L	L	L
Н	L	Н	L
Н	Н	L	Н
Н	Н	Н	Н

H : High level L : Low level

Pin Arrangement



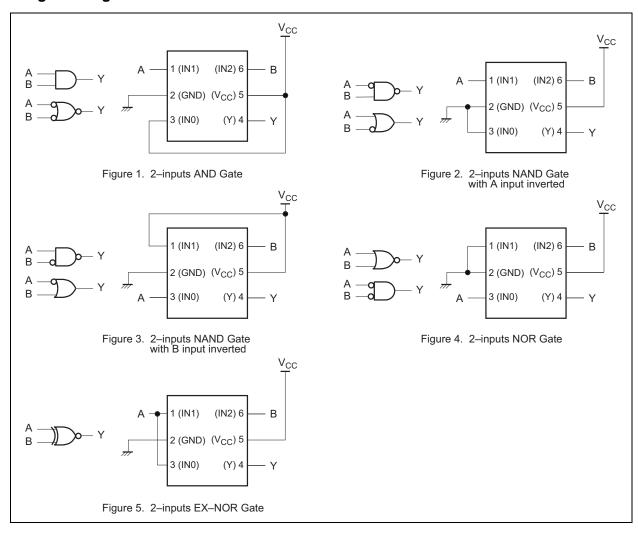
Logic Diagram



Function Selection Table

Logic Function	Figure No.
2–input AND	1
2-input AND with both inputs inverted	4
2-input NAND with one input inverted	2, 3
2-input OR with one input inverted	2, 3
2-input NOR	4
2-input NOR with both inputs inverted	1
2-input EX-NOR	5

Logic Configurations



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
Output voltage range	Vo	-0.5 to 7.0	_ v	V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V _I < 0
Output clamp current	lok	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I ₀	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

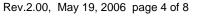
Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150 $^{\circ}$ C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	Vcc	V	
		_	1		V _{CC} = 1.65 to 1.95 V
	I.a.	_	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
	l _{OL}	_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
Output ourrant		_	12	mA	$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Output current	I _{OH}	_	-1		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		_	-2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
		0	300		V _{CC} = 1.65 to 1.95 V
Input transition rise or fall rate	Δt / Δν	0	200	ns / V	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
Input transition rise or fall rate	Δι / Δν	0	100	TIS / V	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.



Electrical Characteristic

Ta = -40 to $85^{\circ}C$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	_	_	V _{CC} ×0.75		
	V _T ⁺	2.5	_	_	1.75		
	VT	3.3	_	_	2.31		
		5.0	_	_	3.50		
		1.65 to 1.95	V _{CC} ×0.25	_	_		
Threshold voltage	V _T	2.5	0.75	_	_	V	
	VT	3.3	0.99	_	_	V	
		5.0	1.5	_	_		
		1.65 to 1.95	0.1	_	V _{CC} ×0.4		
	۸۱/_	2.5	0.25	_	1.0		
	ΔV_{T}	3.3	0.33		1.32		
		5.0	0.5		2.0		
		Min to Max	V _{CC} -0.1		_		I _{OH} = -50 μA
		1.65	1.4		_		$I_{OH} = -1 \text{ mA}$
	V_{OH}	2.3	2.0		_		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_		$I_{OH} = -6 \text{ mA}$
Output voltage		4.5	3.8	_	_	V	$I_{OH} = -12 \text{ mA}$
Output voltage		Min to Max	_		0.1	V	$I_{OL} = 50 \mu A$
		1.65			0.3		$I_{OL} = 1 \text{ mA}$
	V_{OL}	2.3			0.4		$I_{OL} = 2 \text{ mA}$
		3.0	_		0.44		$I_{OL} = 6 \text{ mA}$
		4.5	_		0.55		$I_{OL} = 12 \text{ mA}$
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	V _{IN} = 5.5 V or GND
Quiescent supply current	I _{CC}	5.5	_	_	10	μА	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I _{OFF}	0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

 $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	ТО
item Symbol	Min	Тур	Max	Min	Max	Oilit	Conditions	(Input)	(Output)	
Propagation	t _{PLH}	_	15.8	29.4	1.0	33.0	nc	C _L = 15 pF	IN	>
delay time	t _{PHL}		22.6	40.9	1.0	45.0	ns	$C_L = 50 pF$	IIN	ī

 $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	ТО
item		Min	Тур	Max	Min	Max	Oilit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	9.4	17.6	1.0	21.0	ns	$C_L = 15 pF$	IN	V
delay time	t _{PHL}		12.6	22.6	1.0	26.5	_	$C_L = 50 pF$	IIN	Ī

 $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	ТО
item		Min	Тур	Max	Min	Max	Oilit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.0	11.0	1.0	13.0	nc	C _L = 15 pF	IN	V
delay time	t _{PHL}	_	9.5	14.5	1.0	16.5	ns	$C_L = 50 pF$	IIN	ī

 $V_{CC}=5.0{\pm}0.5~V$

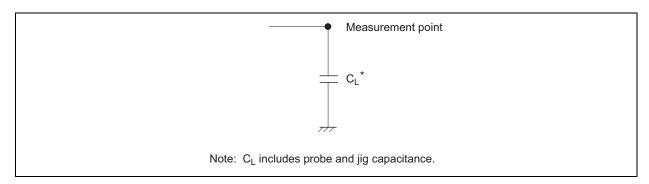
Item	Symbol	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	ТО
item		Min	Тур	Max	Min	Max	Oilit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	4.8	6.8	1.0	8.0	nc	C _L = 15 pF	IN	V
delay time	t _{PHL}	_	6.3	8.8	1.0	10.0	ns	$C_L = 50 pF$	IIN	I

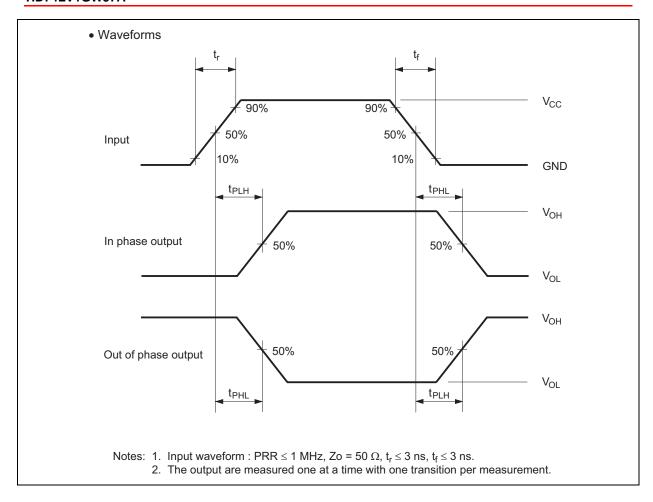
Operating Characteristics

 $C_L = 50 \text{ pF}$

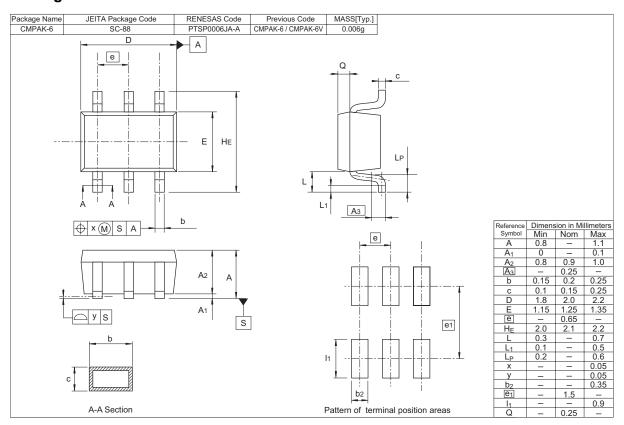
Item	Symbol	V _{cc} (V)		Ta = 25°C		Unit	Test Conditions	
iteiii	Symbol V _{CC} (V		Min	Тур	Max	Offic	rest conditions	
Power dissipation	C	3.3	_	8.5	_	pF	f = 10 MHz	
capacitance	C _{PD}	5.0	_	10.0	_	pΓ	I = 10 MHZ	

Test Circuit





Package Dimensions



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