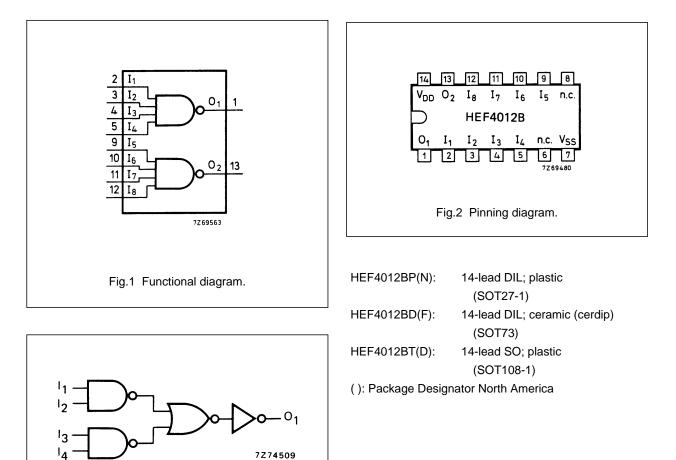
## **Dual 4-input NAND gate**

## HEF4012B gates

## DESCRIPTION

The HEF4012B provides the positive dual 4-input NAND function. The outputs are fully buffered for highest noise immunity and pattern insensitivity of output impedance.



## FAMILY DATA, I<sub>DD</sub> LIMITS category GATES

Fig.3 Logic diagram (one gate).

see Family Specifications

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