AP1186

### 1.5A Ultra Low Dropout Positive Adjustable or Fixed-mode Regulator

## Feature

- Adjustable or Fixed output voltage $1.5 \mathrm{~V}, 1.8 \mathrm{~V}$, $2.5 \mathrm{~V}, 3.3 \mathrm{~V}, 5.0 \mathrm{~V}$
- 0.5 V Maximum Dropout voltage at 1.5 A Load current
- Built-in Thermal Shutdown
- Output Current Limiting
- Fast transient response
- Good noise rejection
- Package : TO220-5L, TO263-5L


## General Description

AP1186 is a 1.5 A regulator with extremely low dropout voltage. This product is specifically designed to provide well regulated supply for applications requiring 2.8 V or lower voltages from 3.3V ATX power supplies where high efficiency of a switcher can be achieved without the cost and complexity associated with switching regulator. One such application is the new graphic chip sets that require anywhere from 2.4 V to 2.7 V supply.

## Ordering Information



## Typical Application



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## Block Diagram



## Package Connect Diagram



## Pin Descriptions

| Name | I/O | Description |
| :---: | :---: | :--- |
| Adj <br> (GND) |  | A resistor divider from this pin to the Vout pin and ground sets the output voltage. <br> (GND only for fixed mode) |
| Vsense | I | This pin is the positive side of the reference that allows remote load sensing to <br> achieve excellent load regulation. A minimum of 10uF capacitor must be connected <br> from this pin to ground to insure stability. |
| Vin | I | The input of the regulator. Typically a large storage capacitor is connected from this <br> pin to ground to insure that the input voltage does not sag below the minimum <br> dropout voltage during the load transient response. This pin must always be higher <br> than Vout in order for the device to regulate. |
| Vctrl | I | This pin is the supply pin for the internal control circuit as well as the base drive for <br> the pass transistor. This pin must always by higher than the Vout pin in order the <br> device to regulate. A minimum of 100uF capacitor must be connected from this pin <br> to ground to insure stability. |
| NC |  | Not used. |
| Vout | O | The output of the regulator. A minimum of 100uF capacitor must be connected from <br> this pin to ground to insure stability. |

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- Absolute Maximum Ratings

| Symbol | Parameter | Rating | Unit |
| :---: | :--- | :---: | :---: |
| Vin | Input Voltage | 16 | V |
| Vctrl | Control Input Voltage | 18 | V |
| PD | Power Dissipation | Internally Limited |  |
| $\mathrm{T}_{\mathrm{ST}}$ | Storage Temperature | -65 to +150 | ${ }^{\circ} \mathrm{C} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{OP}}$ | Operating Junction Temperature Range | 0 to +150 | ${ }^{\circ} \mathrm{C}$ |

## Electrical Characteristics

Unless otherwise specified, these specifications apply over, Cin=1uF, Cout=10uF, and $\mathrm{Tj}=0$ to $150^{\circ} \mathrm{C}$. Typical value refer to $\mathrm{Tj}=25^{\circ} \mathrm{C}$. Vout $=$ Vsense.

| SYM. | Parameter |  | Test Condition | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $V_{\text {REF }}$ | Reference Voltage | AP1186-Adj | $\begin{aligned} & \left\lvert\, \begin{array}{l} T_{J}=25^{\circ} \mathrm{C},\left(\mathrm{~V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}\right)=0.5 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=10 \mathrm{~mA} \\ \mathrm{~V}_{\text {ctrl }}=\mathrm{V}_{\text {IN }}+1 \mathrm{~V} \end{array}\right. \\ & \hline \end{aligned}$ | 1.225 | 1.250 | 1.275 | V |
| $\mathrm{V}_{\text {out }}$ | Output Voltage | AP1186-1.5 | $\begin{aligned} & \mathrm{l}_{\text {lout }}=10 \mathrm{~mA}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}, 2 \mathrm{~V} \leqq \mathrm{~V}_{\text {IN }} \leqq 12 \mathrm{~V} \\ & \mathrm{~V}_{\text {ctr }}=\mathrm{V}_{\mathrm{IN}}+1 \mathrm{~V} \end{aligned}$ | 1.470 | 1.500 | 1.530 | V |
|  |  | AP1186-1.8 | $\begin{aligned} & \text { lout }=10 \mathrm{~mA}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}, 2.3 \mathrm{~V} \leqq \mathrm{~V}_{\text {IN }} \leqq 12 \mathrm{~V} \\ & \mathrm{~V}_{\text {ctrl }}=\mathrm{V}_{\text {IN }}+1 \mathrm{~V} \end{aligned}$ | 1.764 | 1.800 | 1.836 |  |
|  |  | AP1186-2.5 | $\begin{aligned} & \text { lout }=10 \mathrm{~mA}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}, 3 \mathrm{~V} \leqq \mathrm{~V}_{\text {IN }} \leqq 12 \mathrm{~V} \\ & \mathrm{~V}_{\text {ctr }}=\mathrm{V}_{\mathrm{IN}}+1 \mathrm{~V} \end{aligned}$ | 2.450 | 2.500 | 2.550 |  |
|  |  | AP1186-3.3 | $\begin{aligned} & \mathrm{l}_{\text {OUT }}=10 \mathrm{~mA}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}, 3.8 \mathrm{~V} \leqq \mathrm{~V}_{\text {IN }} \leqq 12 \mathrm{~V} \\ & \mathrm{~V}_{\text {ctrl }}=\mathrm{V}_{\text {IN }}+1 \mathrm{~V} \end{aligned}$ | 3.235 | 3.300 | 3.365 |  |
|  |  | AP1186-5.0 | $\begin{aligned} & \text { lout }=10 \mathrm{~mA}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}, 5.5 \mathrm{~V} \leqq \mathrm{~V}_{\text {IN }} \leqq 12 \mathrm{~V} \\ & \mathrm{~V}_{\text {ctrl }}=\mathrm{V}_{\text {IN }}+1 \mathrm{~V} \end{aligned}$ | 4.900 | 5.000 | 5.100 |  |
|  | Line Regulation | AP1186-XXX | $\begin{aligned} & \mathrm{I}_{\mathrm{O}}=10 \mathrm{~mA}, \mathrm{~V}_{\text {OUT }}+0.5 \mathrm{~V}<\mathrm{V}_{\text {IN }}<12 \mathrm{~V} \\ & \mathrm{~V}_{\text {ctrl }}=\mathrm{V}_{\text {IN }}+1 \mathrm{~V} \end{aligned}$ |  |  | 0.2 | \% |
|  | Load Regulation | AP1186-Adj | $\begin{aligned} & 0 \mathrm{~mA}<\mathrm{lo}_{0}<1.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ & \left(\mathrm{~V}_{\text {IN }}-\mathrm{V}_{\text {OUT }}\right)=2 \mathrm{~V}, \mathrm{~V}_{\text {ctrl }}=\mathrm{V}_{\text {IN }}+1 \mathrm{~V} \end{aligned}$ |  |  | 1 | \% |
|  |  | AP1186-1.5 | $\begin{aligned} & \mathrm{V}_{\text {IN }}=3 \mathrm{~V}, 0 \mathrm{~mA}<\mathrm{lo}<1.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ & \mathrm{~V}_{\mathrm{ctr\mid} \mid}=4 \mathrm{~V} \end{aligned}$ |  | 12 | 15 | mV |
|  |  | AP1186-1.8 | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}, 0 \mathrm{~mA}<\mathrm{lo}<1.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ & \mathrm{~V}_{\mathrm{ctr} \mathrm{I}}=4.3 \mathrm{~V} \end{aligned}$ |  | 15 | 18 | mV |
|  |  | AP1186-2.5 | $\begin{aligned} & \mathrm{V}_{\text {IN }}=4 \mathrm{~V}, 0 \mathrm{~mA}<\mathrm{lo}<1.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ & \mathrm{~V}_{\text {ctrl }}=5 \mathrm{~V} \end{aligned}$ |  | 20 | 25 | mV |
|  |  | AP1186-3.3 | $\begin{aligned} & \mathrm{V}_{\text {IN }}=5 \mathrm{~V}, 0 \leqq \mathrm{I}_{\mathrm{OUT}} \leqq 1.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}, \\ & \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \mathrm{~V}_{\mathrm{ctrl}}=6 \mathrm{~V} \end{aligned}$ |  | 26 | 33 | mV |
|  |  | AP1186-5.0 | $\begin{aligned} & \mathrm{V}_{\text {IN }}=8 \mathrm{~V}, 0 \leqq \mathrm{I}_{\text {OUT }} \leqq 1.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}, \\ & \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \mathrm{~V}_{\text {ctrl }}=9 \mathrm{~V} \end{aligned}$ |  | 40 | 50 | mV |
|  | Dropout Voltage (Vctrl-Vout) | Vadj=0V for all conditions below. Vin=2.05V, $\mathrm{lo}=1.5 \mathrm{~A}$ |  |  |  | 1.23 | V |
|  | Dropout Voltage (Vin-Vout) | Vadj $=0 \mathrm{~V}$ for all conditions below. <br> Vctrl=3V, lo=1.5A |  |  | 0.26 | 0.38 | V |
|  | Current Limit | Vctrl $=3 \mathrm{~V}$, Vin $=2.05 \mathrm{~V}, \Delta \mathrm{Vo}=100 \mathrm{mV}$, Vadj $=0 \mathrm{~V}$ |  | 1.6 |  |  | A |
|  | Minimum Load Current | Vctrl=5V, Vin $=3.3 \mathrm{~V}$, Vadj $=0 \mathrm{~V}$ |  |  | 5 | 10 | mA |

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| SYM. | Parameter | Test Condition | Min. | Typ. | Max. | Unit |
| :---: | :--- | :--- | :---: | :---: | :---: | :---: |
|  | Thermal <br> Regulation | 30 ms Pulse |  | 0.01 | 0.02 | $\% \mathrm{~W}$ |
|  | Ripple Rejection | Vctrl =5V, Vin=5V, Io=1.5A, Vadj=0V, Tj=25, <br> Vripple=1Vpp at 120Hz | 60 | 70 |  | dB |
|  | Control Pin <br> Current | Vadj=0V for all conditions below. <br> Vctrl=2.75V, Vin=2.05V, Io=1.5A |  | 6 | 25 | mA |
| Iadj | Adjust Pin Current | Vctrl=2.75V, Vin=2.05,Vadj=0 |  | 50 | 150 | $\mu \mathrm{~A}$ |
| Jc | Thermal <br> Resistance <br>  | TO-263: Control Circuitry/Power Transistor <br> TO-220: Control Circuitry/Power Transistor | 2.5 |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |

NOTE:
AP1186-ADJ incorporates an internal thermal shutdown that protects the device when the junction temperature exceeds the allowable maximum junction temperature.

| Package | Max Pd. |  |
| :---: | :---: | :--- |
| TO-263/TO-220 | $2.4 \mathrm{~W} \sim 4.4 \mathrm{~W}$ | With heat sink or amount of copper board needed. |

## Functional Descriptions

## Introduction

The AP1186 regulator is a 5-terminal device designed specifically to provide extremely low dropout voltages comparable to the PNP type without the disadvantage of the extra power dissipation due to the base current associated with PNP regulators. This is done by bringing out the control pin of the regulator that provides the base current to the power NPN and connecting it to a voltage that is greater than the voltage present at the Vin pin. This flexibility makes the AP1186 ideal for applications where dual inputs are available such as a computer motherboard with an ATX style power supply that provides 5 V and 3.3 V to the board. One such application is the new graphic chip sets that require anywhere from 2.4 V to 2.7 V supply. The AP1186-ADJ can easily be programmed with the addition of two external resistors to any voltages within the range of 1.25 V to 15.5 V . Another major requirement of these graphic chips is the need to
switch the load current from zero to several amps in tens of nanoseconds at the processor pins, which translates to an approximately 300 to 500 ns of current step at the regulator. In addition, the output voltage tolerances are also extremely tight and they include the transient response as part of the specification.
The AP1186 is specifically designed to meet the fast current transient needs as well as providing an accurate initial voltage, reducing the overall system cost with the need for fewer number of output capacitors. Another feature of the device is its true remote sensing capability that allows accurate voltage setting at the load rather than at the device.

## Output Voltage Setting

The AP1186-ADJ can be programmed to any voltages in the range of 1.25 V to 15.5 V with the addition of R1 and R2 external resistors according to the following formula:

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## Functional Descriptions(Continued)



The AP1186-ADJ keeps a constant 1.25 V between the Vsense pin and the Adj pin. By placing a resistor R1 across these two pins and connecting the Vsense ans Vout pin together, a constant current flows through R1, adding to the ladj current and into the R2 resistor producing a voltage equal to the $(1.25 / R 1)^{*} R 2+$ ladj* $R 2$. This voltage is then added to the 1.25 V to set the output voltage. This is summarized in the above equation.

Since the minimum load current requirement of the AP1186-ADJ is $10 \mathrm{~mA}, \mathrm{R} 1$ is typically selected to be a $121 \Omega$ resistor so that it automatically satisfies this condition. Notice that since the ladj is typically in
the range of 50uA it only adds a small error to the output voltage and should be considered when very precise output voltage setting is required.

## Load Regulation

Since the AP1186 has separate pins for the output (Vout) and the sense (Vsense), it is ideal for providing true remote sensing of the output voltage at the load. This means that the voltage drops due to parasitic resistance such as PCB traces between the regulator and the load are compensated for using remote sensing. Figure following shows a typical application of the AP1186 with remote sensing.


## Stability

The AP1186-XXX requires the use of an output capacitor as part of the frequency compensation in order to make the regulator stable. Typical designs for the microprocessor applications use standard electrolytic capacitors with typical ESR in the range of 50 to $100 \mathrm{~m} \Omega$ and an output capacitance of 100uF to 1000 uF . Fortunately as the capacitance
increases, the ESR decreases resulting in a fixed RC time constant. The AP1186-XXX takes advantage of the phenomena in making the overall regulator loop stable.
For most applications a minimum of 100 uF aluminum electrolytic capacitor insures both stability and good transient response.

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## Functional Descriptions (Continued)

## Thermal Design

The AP1186-XXX incorporates an internal thermal shutdown that protects the device when the junction temperature exceeds the allowable maximum junction temperature. Although this device can operate with junction temperatures in the range of $150^{\circ} \mathrm{C}$, it is recommended that the selected heat sink be chosen such that during maximum continuous load operation the junction temperature is kept below this number. The example below shows the steps in selecting the proper surface mount package.

Assuming, the following conditions:
Vout $=2.5 \mathrm{~V}$
Vin=3.3V
Vctrl=5V
lout=1.5A DC Avg.
Calculate the maximum power dissipation using the following equation:

Pd=lout*(Vin-Vout)+(lout/60)*(Vctrl-Vout)
$\mathrm{Pd}=2^{*}(3.3-2.5)+(2 / 60)^{*}(5-2.5)=1.68 \mathrm{~W}$
Using table below select the proper package and the amount of copper board needed.

| Package | Copper Area | $\boldsymbol{\Theta}_{\mathrm{JA}}\left({ }^{\circ} \mathrm{C} / \mathrm{W}\right)$ | $\operatorname{Max~Pd~}\left(\mathrm{Ta}=\mathbf{2 5}{ }^{\circ} \mathrm{C}\right)$ | $\operatorname{Max~Pd~}\left(\mathrm{Ta}=45^{\circ} \mathrm{C}\right)$ |
| :---: | :---: | :---: | :---: | :---: |
| TO263 | Pad Size-1.4"X1.4" | $25-45$ | $2.4 \mathrm{~W}-4.4 \mathrm{~W}$ | $2.0 \mathrm{~W}-3.6 \mathrm{~W}$ |

Note: Above table is based on the maximum junction temperature of $135^{\circ} \mathrm{C}$
As shown in the above table, any of the two packages will do the job.

## Marking Information



TO263-5L,TO220-5L

### 1.5A Ultra Low Dropout Positive Adjustable or Fixed-mode Regulator

## Package Information

(1)Package Type : TO220-5L


| Symbol | Dimensions In Millimeters |  |  | Dimensions In Inches |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. | Min. | Nom. | Max. |
| A | 4.07 | 4.45 | 4.82 | 0.160 | 0.175 | 0.190 |
| b | 0.76 | 0.89 | 1.02 | 0.030 | 0.035 | 0.040 |
| C | 0.36 | 0.50 | 0.64 | 0.014 | 0.020 | 0.025 |
| D | 14.22 | 14.86 | 15.50 | 0.560 | 0.585 | 0.610 |
| E | 9.78 | 10.16 | 10.54 | 0.385 | 0.400 | 0.415 |
| e | 1.57 | 1.71 | 1.85 | 0.062 | 0.067 | 0.073 |
| e1 | 6.68 | 6.81 | 6.93 | 0.263 | 0.268 | 0.273 |
| F | 1.14 | 1.27 | 1.40 | 0.045 | 0.050 | 0.055 |
| H1 | 5.46 | 6.16 | 6.86 | 0.215 | 0.243 | 0.270 |
| J1 | 2.29 | 2.74 | 3.18 | 0.090 | 0.108 | 0.125 |
| L | 13.21 | 13.97 | 14.73 | 0.520 | 0.550 | 0.580 |
| Øp | 3.68 | 3.81 | 3.94 | 0.145 | 0.150 | 0.155 |
| Q | 2.54 | 2.73 | 2.92 | 0.100 | 0.107 | 0.115 |

### 1.5A Ultra Low Dropout Positive Adjustable or Fixed-mode Regulator

(2) Package Type: TO263-5L


| Symbol | Dimensions In Millimeters |  | Dimensions In Inches |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. | Min. | Nom. | Max. |
| A | 4.07 | 4.46 | 4.85 | 0.160 | 0.176 | 0.191 |
| B | 0.66 | 0.84 | 1.02 | 0.026 | 0.033 | 0.040 |
| C | 0.36 | 0.50 | 0.64 | 0.014 | 0.020 | 0.025 |
| C2 | 1.14 | 1.27 | 1.40 | 0.045 | 0.050 | 0.055 |
| D | 8.65 | 9.15 | 9.65 | 0.341 | 0.360 | 0.380 |
| E | 9.78 | 10.16 | 10.54 | 0.385 | 0.400 | 0.415 |
| e | 1.57 | 1.71 | 1.85 | 0.062 | 0.068 | 0.073 |
| L | 14.61 | 15.24 | 15.88 | 0.575 | 0.600 | 0.625 |
| L1 | 2.29 | 2.54 | 2.79 | 0.090 | 0.100 | 0.110 |
| L2 | - | - | 2.92 | - | - | 0.115 |

