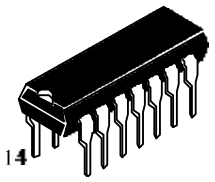
Quad Single Supply Comparator

The SL339 consists of four independent precision voltage comparators with an offset voltage specification as low as 2.0 mV max for four comparators which were designed specifically to operate from a single power supply over a wide range of voltages.

Application areas include limit comparators, simple analog to digital converters; pulse, squarewave and time delay generators; wide range VCO; MOS clock timers; multivibrators and high voltage digital logic gates.

- Single or Split Supply Operation
- Low Input Bias Current
- Low Input Offset Current
- Input Common Mode Voltage Range to Gnd
- Low Output Saturation Voltage
- TTL and CMOS Compatible



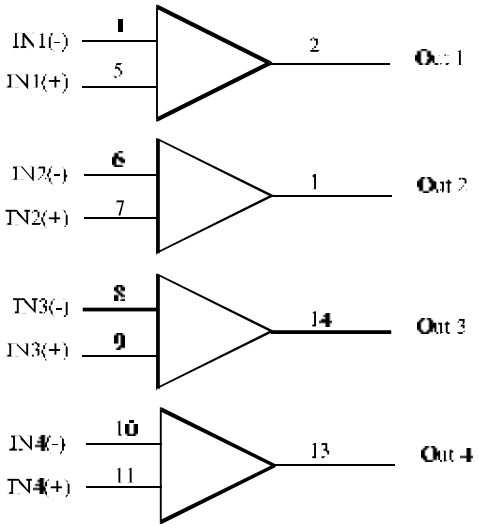
N SUFFIX
PLASTIC

D SUFFIX
SOIC

ORDERING INFORMATION

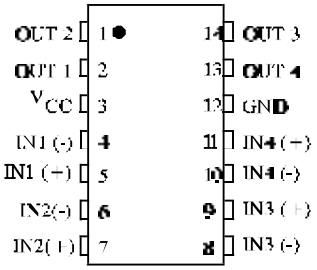
SL339N Plastic
SL339D SOIC
T_A = 0° to 70° C for all packages.

LOGIC DIAGRAM



PIN 3 = V_{CC}
PIN 12 = GND

PIN ASSIGNMENT



MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
V_{CC}	Power Supply Voltages Single Supply Split Supplies	 36 ±18	 V
V_{IDR}	Input Differential Voltage Range	36	V
V_{ICR}	Input Common Mode Voltage Range (1)	-0.3 to V_{CC}	V
I_{SC}	Output Short Circuit to Ground	Continuous	
I_{IN}	Input Current, per pin (2)	50	mA
T_J	Junction Temperature Plastic Packages	 150	 °C
Tstg	Storage Temperature	-65 to +150	°C
T_L	Lead Temperature, 1mm from Case for 10 Seconds	260	°C
P_D	Power Dissipation @T_A=25°C Plastic Package Derate above 25°C	 1.0 8.0	 W mW/°C

*Maximum Ratings are those values beyond which damage to the device may occur.
Functional operation should be restricted to the Recommended Operating Conditions.
Notes:
1. Split Power Supplies.
2. V_{IN}<-0.3V. This input current will only exist when voltage at any of the input leads is driven negative.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage	±2.5 or 5.0	±15 or 30	V
T_A	Operating Temperature, All Package Types	0	+70	°C

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V_{IN} and V_{OUT} should be constrained to the range GND≤(V_{IN} or V_{OUT})≤V_{CC}.

Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or V_{CC}). Unused outputs must be left open.

DC ELECTRICAL CHARACTERISTICS (T_A=0 to +70°C)

Symbol	Parameter	Test Conditions	Guaranteed Limit			Unit
			Min	Typ	Max	
V_{IO}	Input Offset Voltage	V_0=1.4V V_{CC}=5.0-30V;R_S≤100Ω V_{ICR}=0V - (V_{CC}-1.5)V	-		9.0	mV
I_{IB}	Input Bias Current	V_0=1.4V V_{CC}=5.0-30V V_{ICR}=0V - (V_{CC}-1.5)V	-		400	nA
I_{IO}	Input Offset Current	V_0=1.4V V_{CC}=5.0-30V V_{ICR}=0V - (V_{CC}-1.5)V	-		±150	nA
V_{ICR}	Input Common Mode Voltage Range	V_{CC}=5.0-30V	0		V_{CC}-2.0V	V
I_{CC}	Supply Current	R_L=∞,V_{CC}=5.0 R_L=∞,V_{CC}=30V	- -		2.0* 2.5*	mA
A_{VOL}	Voltage Gain	V_{CC}=15V, R_L=15KΩ	-	200*	-	V/mV
t_1	Large Signal Response Time	V_{IN}=TTL Logic Swing, V_{ref}=1.4V, V_{CC}=5.0V, R_L=5.1KΩ, V_{RL}=5.0V	-	300*	-	ns
t_2	Response Time	V_{CC}=5.0V, R_L=5.1KΩ, V_{RL}=5.0V	-	1.3*	-	µs
I_{sink}	Output Sink Current	V_I(-)=1.0V, V_I(+)=0V, V_0≤1.5V, V_{CC}=5.0V	6.0*	-	-	mA
V_{sat}	Saturation Voltage	V_I(-)=1.0V, V_I(+)=0V, I_{sink}≤4.0mA, V_{CC}=5.0V	-	-	700	mV
I_{OL}	Output Leakage Current	V_I(+)=1.0V, V_I(-)=0V, V_0=5.0V V_0=30V		0.1*	 1000	nA
V_{IDR}	Differential Input Voltage Range	All V_{IN}≥GND or V-Supply (if used)			V_{CC}*	V

*=@25°C

TYPICAL PERFORMANCE CHARACTERISTICS

(V_{CC}=1.5V, T_A=+25°C, (each comporator))

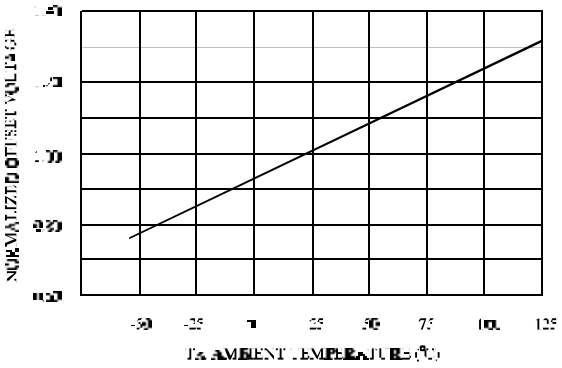


Figure 1. Normalized Input Offset Voltage

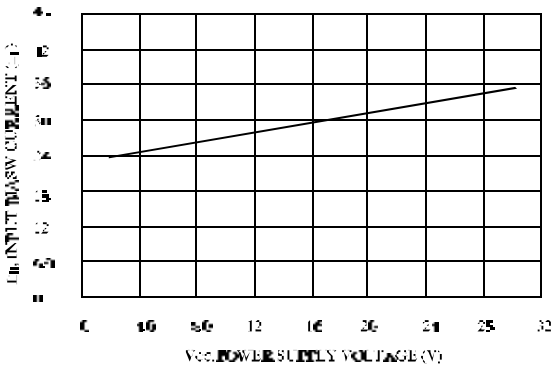


Figure 2. Input Bias Current

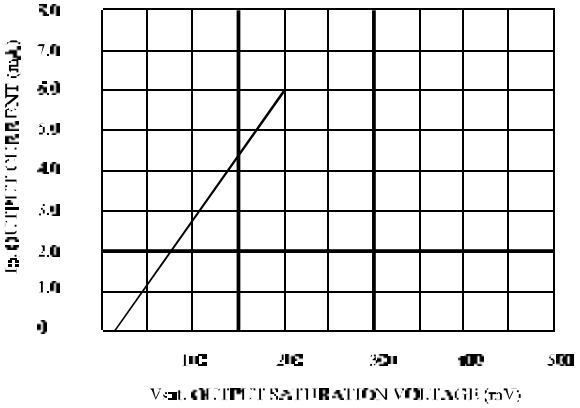


Figure 3. Output Sink Current versus Output Saturation Voltage