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Variable Gain Low Frequency Voltage Amplifier

Specifications	Test Conditions	$Vs = \pm 15 V$, Ta = 25°C			
Gain	Gain Values	20, 40, 60, 80 dB indicated by four LEDs			
	Gain Accuracy	± 0.1 % (between settings)			
	Gain Flatness	± 1 % (overall) ± 0.1 dB			
Frequency Response	Lower Cut-Off Frequency Upper Cut-Off Frequency Upper Cut-Off Frequency Rolloff	DC, switchable to 1.5 Hz 100 kHz, switchable to 1 kHz 12 dB/Oct.			
Time Response	Rise / Fall Time (10% - 90%)	3.5 μs (@ BW = 100 kHz) 350 μs (@ BW = 1 kHz)			
Input	Input Impedance Input Voltage Drift Equivalent Input Voltage Noise Equivalent Input Current Noise 1/f-Noise Corner Input Bias Current Input Bias Current Drift Input Offset Voltage	1 TΩ 1.3 μV/K <u>Gain Setting DLPVA-100-F–S DLPVA-100-F–D</u> 60, 80 dB 5.5 nV/√Hz 6.9 nV/√Hz 40 dB 8 nV/√Hz 10 nV/√Hz 20 dB 60 nV/√Hz 60 nV/√Hz 1.6 fA/√Hz 80 Hz 1 pA Factor 2.3 / 10 °C ± 5 mV, adjustable by offset trimmer and			
	<i>Single Ended Input, Model "DLP</i> V Input Voltage Range for linear Arr				
	<i>True Differential Input, Model "DL</i> Common Mode Voltage Range CMRR	LPVA-100-F-D" only: ± 5 V 120 dB (@ 100 Hz) 100 dB (@ 10 kHz) 80 dB (@ 60 kHz)			
Output	Output Impedance Output Voltage Range For Linear Amplification Output Current (max.) Output Overload Recovery Time	50 Ω (terminate with > 10 kΩ load for best performance) \pm 10 V (@ > 10 kΩ load) \pm 20 mA 0.5 ms (after 20x overload)			
		Page 2			
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	Low Frequency	Voltage Amplifier			
Overload LED	The amplifier features a LED to signalize an overload condition. The Overload LED will turn on if the signal level within the signal path exceeds the linear operating range. In order to ensure the correct operation of the amplifier without signal distortions reduce the gain setting until the Overload LED turns off.				
	The Overload LED may also turn on under the following operating conditions:				
	- The amplifier is operated with open input or with a high source impedance. For proper operation please use a source impedance of less than 100 $M\Omega$ or switch to a lower gain setting.				
	- When using a DLPVA-F-D with differential input stage the Overload LED may turn on if the common mode input voltage exceeds ± 5 V or if the source is totally floating with respect to the amplifier ground. For proper operation make sure that the common mode voltage stays within ± 5 V with respect to the amplifier ground and make a valid connection between the source ground and the amplifier ground to ensure that the inputs cannot drift outside the tolerable common mode range.				
Remote Offset Control	Offset Control Voltage Range Offset Control Input Impedance	\pm 10 V, corresponds to \pm 5 mV input offset 200 k Ω			
Remote Digital Control	Control Input Voltage Range Control Input Current Overload Output	Low: - 0.8+ 0.8 V High: + 1.8 + 12 V, TTL / CMOS compatible 0 mA @ 0 V, 1.5 mA @ + 5 V, 4.5 mA @ + 12 V Non active: + 5 V, max. 1 mA, active: 0.8 V, max10 mA			
Power Supply	Supply Voltage Supply Current	\pm 15 V (\pm 14.5 V to \pm 16 V) \pm 75 mA typ. (depends on operating conditions, recommended power supply capability minimum 150 mA)			
Case	Weight Material	0.32 kg (0.7 lbs) AlMg4.5Mn, nickel-plated			
Temperature Range	Storage Temperature Operating Temperature	- 40 °C to + 100 °C 0 °C to + 60 °C			
Absolute Maximum Ratings	Power Supply Voltage Control Input Voltage	± 21 V + 16 V / - 5 V			
	Signal Input Voltage Transient Input Voltage	\pm 15 Vp \pm 3 kV (discharge from 5 nF source)			
		Page 3			
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Amplifier Input, Model "DLPVA-100-F-S": ial Input, Model "DLPVA-100-F-D": S, 4-pin fixed socket non inverting input inverting input GND N.C. S, 3-pin fixed socket + 15V - 15V GND
 <i>Input, Model "DLPVA-100-F-D":</i> S, 4-pin fixed socket non inverting input inverting input GND N.C. PIN 1 PIN 1 PIN 4 S, 3-pin fixed socket + 15V - 15V
S, 4-pin fixed socket non inverting input inverting input GND N.C. PIN 1 PIN 4 S, 3-pin fixed socket + 15V - 15V
S, 3-pin fixed socket + 15V - 15V
+ 15V - 15V
+ 15V - 15V
O PIN 1 +Vs PIN 3 GND
female, qual. class 2 +12 V (stabilized power supply output, max. 100 mA) -12 V (stabilized power supply output, max. 100 mA) AGND (analog ground) +5 V (stabilized power supply output, max. 50 mA) digital output: overload NC NC offset control voltage input DGND (ground f. digital control Pin 10 - 25 NC digital control input: gain, LSB digital control input: gain, MSB digital control input: AC/DC digital control input: 100 kHz / 1 kHz NC

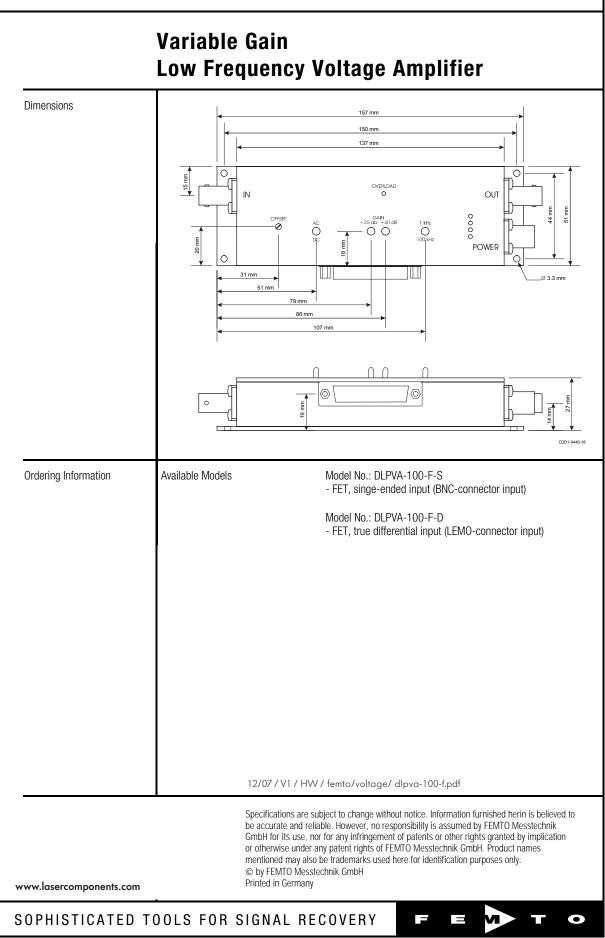
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Datasheet

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Remote Control Operation	General	by logical OR to loca a switch setting, set "0 dB", "AC" and "1 via a bit-code at the Mixed operation, e.g	t bits are opto-isolated a al switch setting. For rem the corresponding local kHz" and select the war corresponding digital in g. local gain setting and i h setting, is also possible	ote control switch to nted setting puts. remote
	Gain Setting	Gain Pin 1	1 Pin 12	
		20 dBlow40 dBhigh60 dBlow80 dBhigh	low low high high	
	AC/DC Setting	Coupling Pin 1	3	
	Bandwidth Setting	AC low DC high		
		Bandwidth Pin 1	4	
		1 kHz low 100 kHz high		
Characteristics	Gev 1 1 1 1 1 1 1 1 1 1 1 1 1		10 k 100 [Hz]	0k 1 M D01-0440-17
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