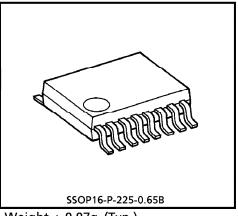
TOSHIBA BI-CMOS INTEGRATED CIRCUIT SILICON MONOLITHIC

T B 3 1 2 0 2 F N

PLL FREQUENCY SYNTHESIZER

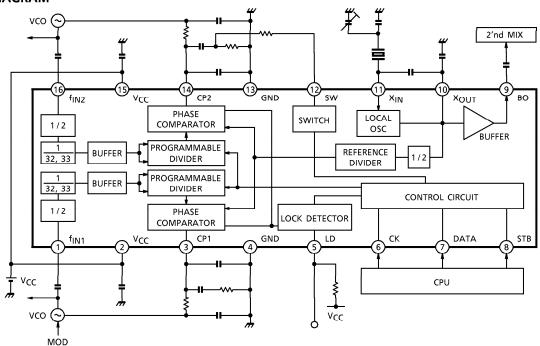
FEATURES

- One packaging two systems prescaler and PLL for receiver and transmitter
- Low operating power supply voltage : $V_{CC} = 2.0 \sim 5.5V$ (Temperature $\geq -10^{\circ}C$: $V_{CC} = 1.9 \sim 5.5V$)
- Low current consumption
 I_{CC} = 8mA (Typ.)
- Input frequency : f_{IN} = 200~520MHz
- High input sensitivity $: V_{IN} = 93 \sim 107 dB_{\mu}V$
- Charge pump is constant current type, and is able to change output current by serial data
- Reference oscillation circuit is adopted circuit of bipolar, so getting the stable X'tal oscillation circuit
- Available standby control for receiver and transmitter independent of each other
- The very small package
- : SSOP16pin (0.65mm pitch)



Weight : 0.07g (Typ.)

BLOCK DIAGRAM



961001EBA2

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PIN No.	PIN NAME	FUNCTION	INTERNAL EQUIVALENT CIRCUIT	
1	fIN1	Input terminal of RF oscillation signal.	V _{CC} 1/16 100Ω 1kΩ	
16	fin2		GND	
2	V _{CC}	Terminal of power supply.		
15	V _{CC}	Pin 2 and pin 15 are cannected in IC.		
3	CP1	Output terminal of charge pump. Charge pump is constant current outp		
14	CP2	output current is varied by input seria		
4	GND	Terminal of GND.		
13	GND	Pin 4 and pin 13 are cannected in IC.		
5	LD	Output terminal of lock detection. It is drain output.		
12	SW	Switchover terminal for constant of loo It is the open drain output. When don't switch constant of loop fi general output.		
6	СК	Input terminal of clock.		$\begin{pmatrix} 6 \\ \cdot \end{pmatrix}$ 1k Ω N
7	DATA	Indul lenningi of senai ugla. I	the serial data ontrolling IC.	
8	STB	Input terminal of strobe signal.	Ŭ,	
9	BO	Output terminal of buffer amplifier. The signal of local oscillation is outpu- buffer amplifier.		
10	XOUT	Output terminal of local oscillation sig		
11	X _{IN}	Input terminal of local oscillation signa In case of external input, connecting in terminal.		

PIN FUNCTION (The values of resistor and capacitor are typical.)

961001EBA2'

- 8
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DESCRIPTION OF FUNCTION AND OPERATION

1. Entry of serial data

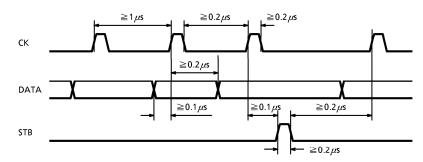
- Serial data used to control the IC is input through three terminals, CK, DATA and STB.
 - 1 During the rise of a clock pulse, data is fed to the shift register in the IC in order from the LSB.
 - ② Upon the reception of all data, the strobe signal (STB) is made "H".
 - ③ After the reception of a strobe signal (STB) of the "H" level, the data stored in the shift register is transferred to the latch in the block selected by the group code, whereby the IC is controlled.
- The three terminal, CK, DATA and STB, contains Schmitt trigger circuits to prevent the data errors by noise, etc.

○ Serial data group and group code

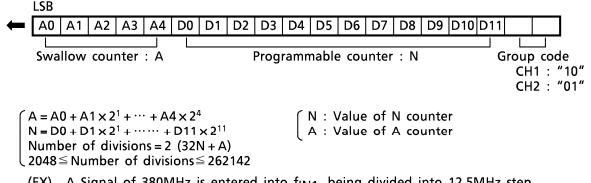
• The IC has control divided into four groups so that they may be controlled independent of one another. Each group is identified by a 2bit group code attached at the data end.

CODE	ITEM						
10	Number of divisions by CH1 programmable divider (f _{IN1})						
01	Number of divisions by CH2 programmable divider (f _{IN2})						
11	Number of divisions by reference divider (X _{IN})						
00	Optional control						

○ Serial data input timing



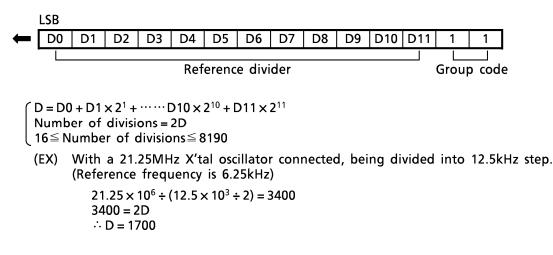
- 2. Programmable dividers (CH1, CH2)
 - These programmable dividers are composed of a 5bit swallow counter (5bit programmable divider), a 12bit programmable counter, and a two-modulars prescaler providing 64 and 66 divisions.
 - The strategy of a swallow counter is used to set high reference frequency.
 - Sending certain data to the swallow counter and the programmable counter allows the setting of any of 2048 to 262142 divisions (multiple of two).
 - The programmable counter and swallow counter are set by each channel. Each channel is specified by a group code.



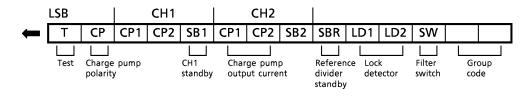
(EX) A Signal of 380MHz is entered into $f_{\rm IN1},$ being divided into 12.5MHz step. (Reference frequency is 6.25kHz)

 $380 \times 10^6 \div (12.5 \times 10^3 \div 2) = 60800$ 60800 = 2 (32N + A) $\therefore N = 950, A = 0$

- 3. Reference divider
 - This block generates the reference frequency for the PLL.
 - The reference divider is composed of a 12bit reference divider and a half fixed divider.
 - Sending certain data to the reference divider allows the setting of any of 16 to 8190 divisions (multiple of two).



- 4. Optional control
 - The optional control below is available.
 - 1) Test mode (Usually set up T = "0").
 - ② Control and polarity control of the charge pump output current for each channel.
 - ③ Output terminal for Lock detector.
 - ④ Standby control of each channel and reference divider.
 - ⑤ Control of filter switch.



- T : Bit for test mode
 - CP : Switchover bit for charge pump output polarity
 - CP1, 2 : Switchover bit for charge pump output current
 - SB1, 2 : Standby control bit for CH1, CH2
 - SBR : Standby control bit for reference divider
 - LD1, 2 : Control bit for lock detector output
- SW : Control bit for filter switch
- Description of options including their control
 - ① Test mode (T)

Bit "T" is for test mode. In other than the test mode, set this bit at "0".

② Control of charge pump output current (CP1, CP2)

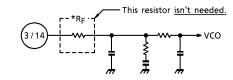
This IC uses a constant current output type charge pump circuit. Output current is varied by controlling "CP1" and "CP2".

CONTR	OL BIT	CHARGE PUMP						
CP1	CP2	OUTPUT CURRENT						
0	0	± 100μA						
0	1	±200μA						
1	0	±400μA						
1	1	±800μA						

CHARGE PUMP OUTPUT CURRENT

High speed lock up is possible by switching charge pump output current.

(Note)



Charge pump output polarity (CP)

Bit "CP" can be reversed charge pump output polarity.

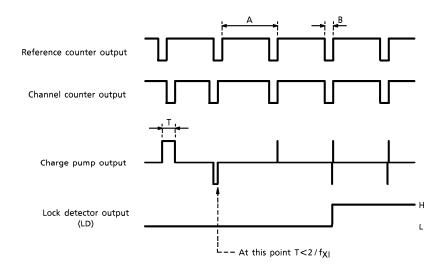
	CHARGE	PUMP	OUTPUT	POLARITY
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СР	OUTPUT POLARITY
0	Normal
1	Reverse

③ Lock detector output

When phase comparator detects phase difference, LD terminal (pin 5) outputs "L". When phase comparator locks, LD terminal outputs "H". On standby, outputs "H". LD terminal output is controlled by "SB1", "SB2", "LD1" and "LD2".

	CONTR	OL BIT		LOCK DETECTOR]
SB1	SB2	LD1	LD2	OUTPUT STATE	
		0	0	L	
0	0	0	1	CH2 only detect]
0		1	0	CH1 only detect	Logical multiply (AND) of
		1	1	CH1 * CH2	→ CH1, CH2
		0	0	L	
0	1	0	1	Н	
1		1	0	CH1 only detect	
		1	1	CH1 only detect	About SB1, SB2 bit
	0	0	0	L	0 : Normal operation
		0	1	CH2 only detect	L1 : Standby
		1	0	н	
		1	1	CH2 only detect	
		0	0	L]
1	1	0	1	Н]
		1	0	Н]
		1	1	Н]



- f_{XI} : X_{IN} operating frequency (LOCAL OSC)
- T : The time difference of the pulse between reference counter output and channel counter output.

$$A = \frac{\text{Number of divisions by reference divider}}{f_{XI}}$$
 (s)
$$B = 2 / f_{XI}$$
 (s)

When the situation that T is less than $2/f_{XI}$ (T< $2/f_{XI}$) continues more than three cycles of reference counter output, lock detector outputs "H".

④ Standby control (SB1, SB2, SBR)

Standby control by three bits (SB1, SB2, SBR).

Bits "SB1" and "SB2" do standby control of CH1, CH2. Bit "SBR" does standby control of reference divider.

C	ONTROL BI	Т	STATE			
SB1	SB2	SBR	CH1	CH2	REF	
0	0	*	ON	ON	ON	
0	1	*	ON	OFF	ON	Interlocking mode
1	0	*	OFF	ON	ON	
1	1	0	OFF	OFF	ON	– REF ON mode
1	1	1	OFF	OFF	OFF	

⑤ Filter switch control (SW)

Control of SW terminal by bit "SW".

This terminal is for switching constant of loop filter.

Output type of this terminal is open drain output. Switching the register of loop filter by this terminal with switching charge pump output current, high mode and normal mode can operate PLL by ideal braking factor.

When constant of loop filter don't change switch, available general output.

FILTER SWITCH CONTROL

SW	OUTPUT
0	OFF
1	ON

5. Reference frequency oscillation circuit and buffer amplifier

This IC has a stable oscillation circuit composed of bipolar.

In case of inputting the external reference frequency directly, use X_{IN} terminal (pin 11).

For the common use of X'tal of the reference frequency oscillation circuit for the PLL and X'tal of local oscillation to 2'nd MIX, output terminal of local oscillation signal with buffer amplifier (pin 9) may be used.

This terminal (pin 9) is provied with a buffer amplifier.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Supply Voltage	Vcc	6	V
Power Dissipation	PD	560	mW
Operating Temperature	T _{opr}	- 30~85	°C
Storage Temperature	T _{stg}	- 55~150	°C

ELECTRICAL CHARACTERISTIC

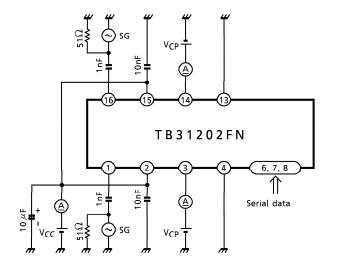
(Unless otherwise specified, $V_{CC} = 2.2V$, $Ta = 25^{\circ}C$)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Operating Power	Mar		Ta = - 30~85°C	2.0	2.2	5.5	V	
Supply Voltage	Vcc		Ta = - 10~85°C	1.9	2.2	5.5	V	
Operating Current Consumption	lcco		CH1 = CH2 = 300MHz, 107dBµV input	5.0	8.0	11.0	mA	
Current Consumption	lccQ		At standby mode	_	0	10	μΑ	
f _{IN} Operating	fIN1		V _{IN1} = 93dBµV	200		520	MHz	
Frequency	finz		V _{IN2} = 93dBμV	200	—	520		
f _{IN} Input Sensitivity	V _{IN1}		f _{IN1} = 200~520MHz	93	—	107	dBμV	
IN Input sensitivity	V _{IN2}		f _{IN2} = 200~520MHz	93	—	107	ubμv	
X _{IN} Operating Frequency	fXI		$V_X = 0.5V_{p-p}$, Sin-wave	5	21.25	25	MHz	
X _{IN} Input Voltage	VXI		f _{XI} = 21.25MHz	102	107	112	dBμV	
	VIH		STB, DATA, CK	V _{CC} ×0.8	Vcc	5.7	v	
Input Voltage	VIL		STB, DATA, CK	- 0.2	0	V _{CC} × 0.2		
CK Input Frequency	^f ск		СК	_	—	1	MHz	
	ICP1		"CP1" = 0, "CP2" = 0, V _{CP} = 1.1V	_	± 100	_		
Charge Pump Output	ICP2		"CP1" = 0, "CP2" = 1, V _C P = 1.1V		±200	_	μΑ	
Current	I _{CP3}		"CP1" = 1, "CP2" = 0, V _{CP} = 1.1V		± 400			
	ICP4		"CP1" = 1, "CP2" = 1, V _{CP} = 1.1V	_	± 800			
Charge Pump OFF Leak Current	CPOFF		Standby mode, V _{CP} = 1.1V	—	_	± 1.0	μΑ	
SW Terminal ON Resistance	Rsw		sw _{ON}	_	500	_	Ω	
LD Terminal ON Resistance	R _{LD}		LD _{ON}	_	500	_	Ω	
SW Terminal OFF Leak Current	SWOFF		SWOFF	_	_	± 1.0	μA	
LD Terminal OFF Leak Current	LDOFF		LDOFF			± 1.0	μA	

REFERENCE DATA (Typ.)

CH1	CH2	REFERENCE DIVIDER	CURRENT CONSUMPTION	UNIT	
N	N	ON	8.0	mA	
N	S	ON	4.5	mA	
S	N	ON	4.5	mA	
S	S	ON	800	μA	A : Normal operation
S	S	OFF	0	μA	S : Srandby state

TEST CIRCUIT



-25°C

Both CH on

5

_

2

100*µ*A

200*µ*A

400 µA

_ 800μA

POWER SUPPLY VOLTAGE V_{CC} $\langle V \rangle$

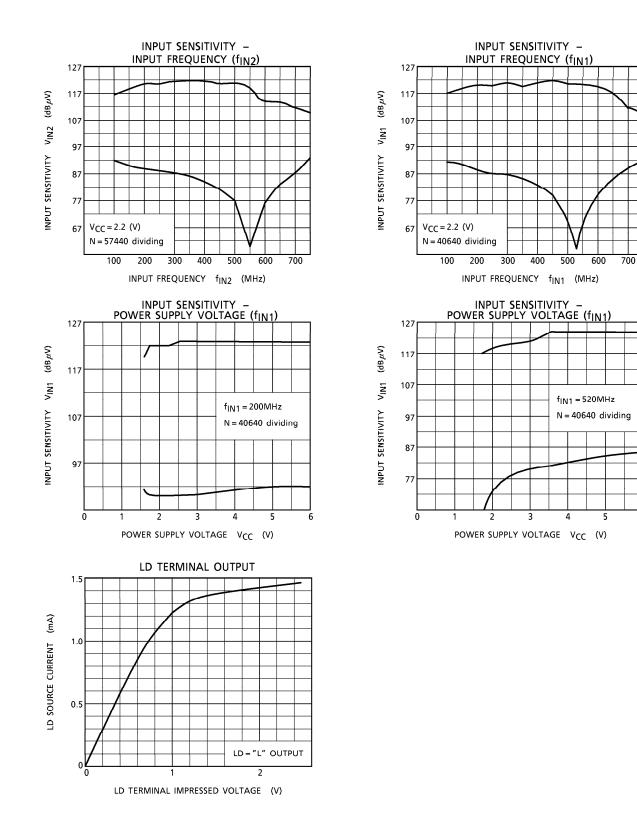
6

6

- 30°C

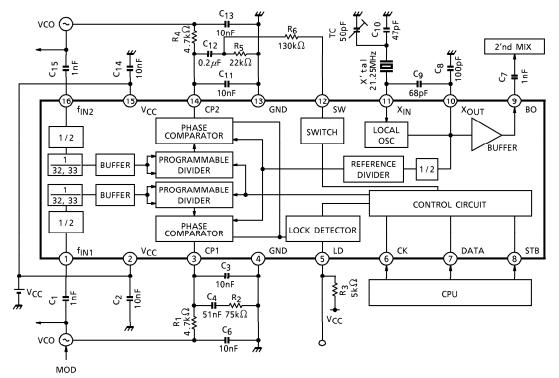
CURRENT CONSUMPTION – POWER SUPPLY VOLTAGE (TEMPERATURE) CURRENT CONSUMPTION -POWER SUPPLY VOLTAGE 10 12 (mA) (mA) 10 85°C -Both CH on 8 <u>८</u> <u></u>2 8 CURRENT CONSUMPTION CURRENT CONSUMPTION 6 CH1 CH₂ 2 Both CH off 0L 0L 0 2 3 4 5 6 2 3 4 POWER SUPPLY VOLTAGE VCC (V) POWER SUPPLY VOLTAGE VCC (V) CHARGE PUMP OUTPUT CURRENT -CHARGE PUMP VOLTAGE (N-CH) CHARGE PUMP OUTPUT CURRENT CHARGE PUMP VOLTAGE (P-CH) 1000 ٥ (Y7) (All) 800 µA 800 - 100 <u>8</u> <u>8</u> 100 µA 700 - 200 PUMP OUTPUT CURRENT CURRENT -200 µA 600 - 300 500 400 PUMP OUTPUT 400 400 µA 400 µA - 500 300 - 600 200 200 µA -700CHARGE CHARGE 100 800 µA 100*µ*A 800 0 2 0 CHARGE PUMP VOLTAGE VCP (V) CHARGE PUMP VOLTAGE VCP (V) CHARGE PUMP OUTPUT CURRENT – POWER SUPPLY VOLTAGE (N-CH) CHARGE PUMP OUTPUT CURRENT – POWER SUPPLY VOLTAGE (P-CH) n (Ynd) (Fe) 1100 ¹/₂ Vcc VCP - 100 800 µA 1000 <u>0</u> <u>8</u> - 200 900 - 300 PUMP OUTPUT CURRENT CURRENT 800 - 400 700 - 500 600 OUTPUT 400 UA - 600 500 - 700 400 PUMP -800300 200 µA - 900 200 CHARGE CHARGE 100 1000 $V_{CP} = \frac{1}{2} V_{CC}$ 100 µA 0 2 6 0 5 1 3 4

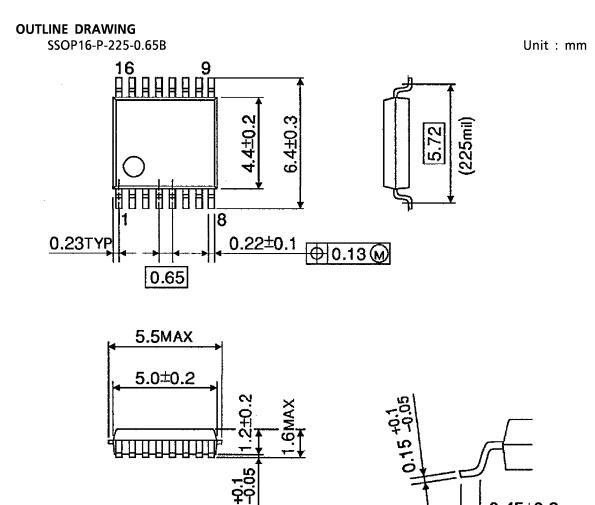
POWER SUPPLY VOLTAGE V_{CC} $\langle V \rangle$



6

APPLICATION CIRCUIT





Weight : 0.07g (Typ.)

0.45±0.2