CONNOR WINFIELD

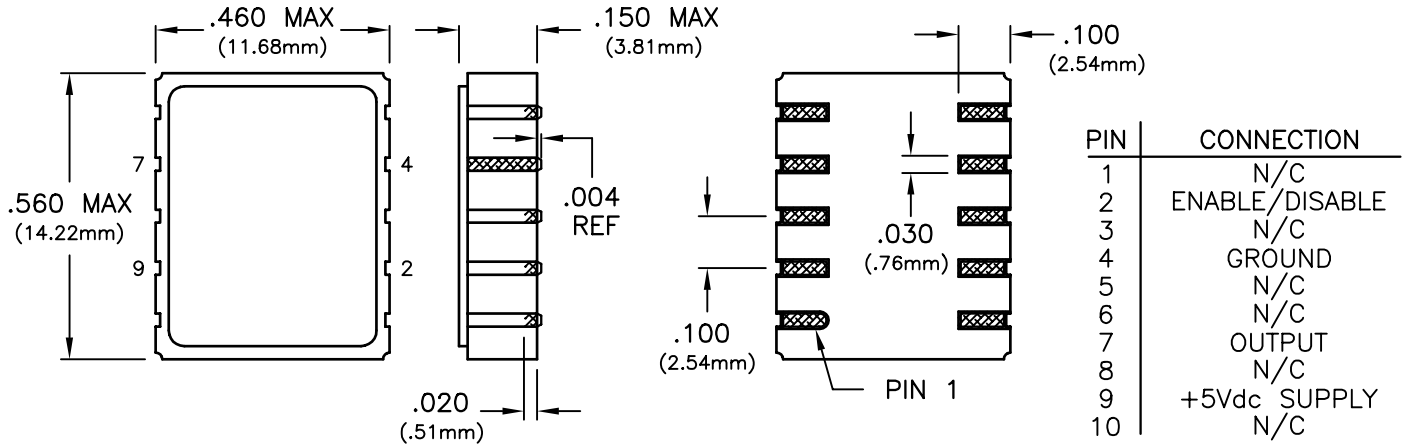
AURORA, IL 60505
PHONE (630) 851-4722
FAX (630) 851-5040
www.conwin.com

CRYSTAL CONTROLLED OSCILLATORS

SM ACMOS ENABLE/ DISABLE TRISTATE OUTPUT

SPECIFICATIONS			ASM51R	ASM52R	ASM53R	ASM54R
Frequency Range			14MHz to 125MHz			
Frequency Stability			±25ppm	±50ppm	±100ppm	±20ppm
			(Inclusive of calibration tolerance at 25°C and operating temperature range)			
Temperature Range			0°C to +70°C			
Output	Waveform		CMOS Squarewave , TTL Compatible			
	Load		50pF			
	Voltage	Voh	4.40V Minimum			
		Vol	0.44V Maximum			
	Current	Ioh	−24mA			
		Iol	24mA			
	Duty Cycle		45/55 Maximum			
	Rise/Fall Time		3nS			
Input	Output E/D Time		100mS Maximum			
	Enable	Vih	2.0V Minimum			
	Disable	Vil	0.8V Maximum			
			Oscillator output is enabled with no connection on pin 2			
Start Up Time			10mS Maximum			
Supply Voltage			+5Vdc ±10%			
Supply Current			150mA Maximum			
Package			Hermetically sealed, leadless ceramic package			

WARNING! Do not insert oscillator into a hot circuit. Failure to comply will result in damage to oscillator.



PIN	CONNECTION
1	N/C
2	ENABLE/DISABLE
3	N/C
4	GROUND
5	N/C
6	N/C
7	OUTPUT
8	N/C
9	+5Vdc SUPPLY
10	N/C

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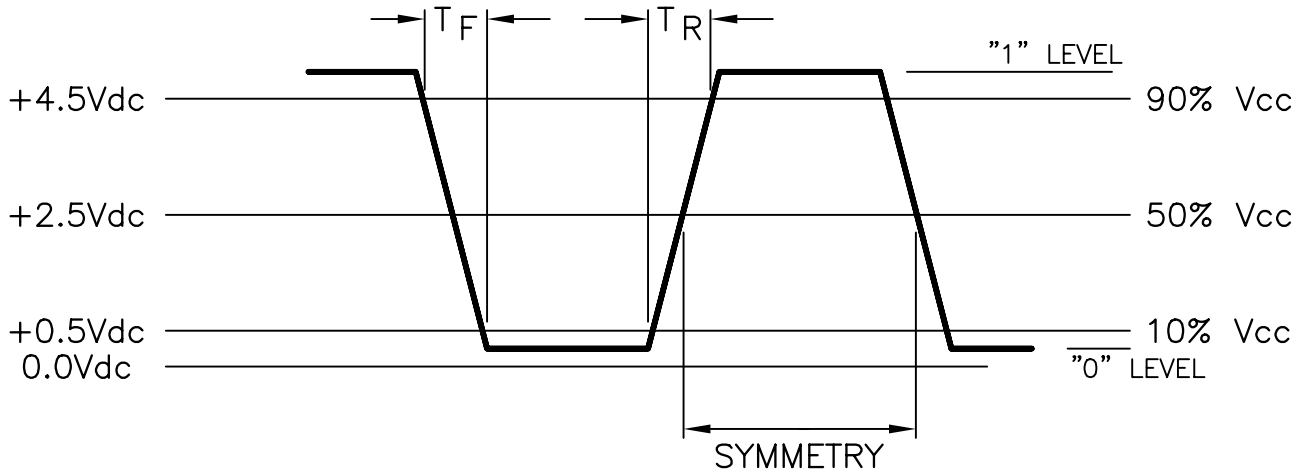
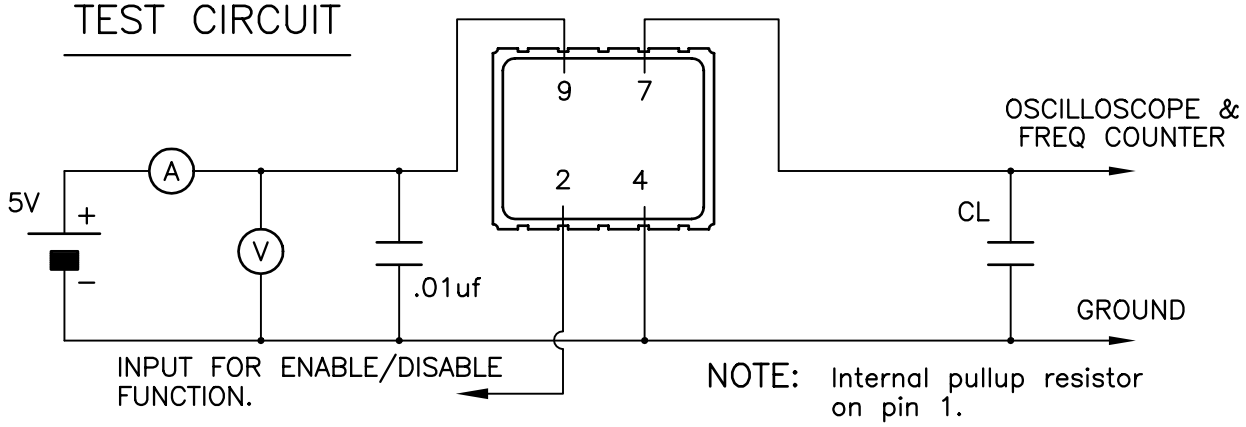
ADVANCED CMOS SURFACE MOUNT

OUTPUT WAVEFORM

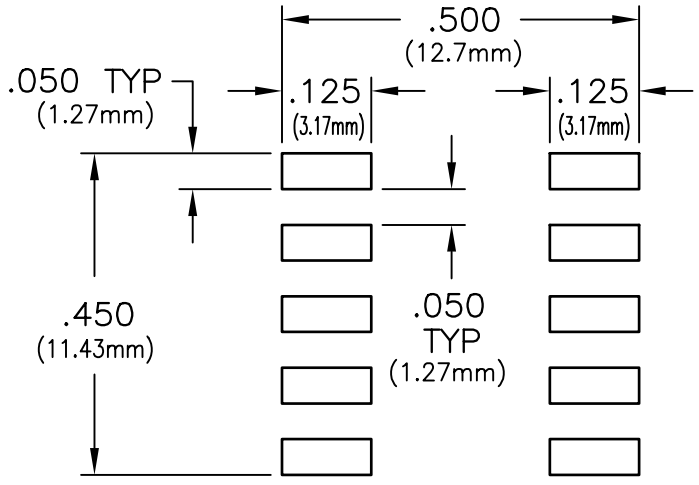


TEST CIRCUIT



NOTE: Internal pullup resistor on pin 1.

SUGGESTED PAD LAYOUT



BULLETIN #: AC018	REV: 04	Specifications subject to change without notice.	C-W © 1999
PAGE 2 OF 2	DATE: 9/20/99	ISSUED BY:	Dimensional Tolerance: ±.02" ±.005"

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