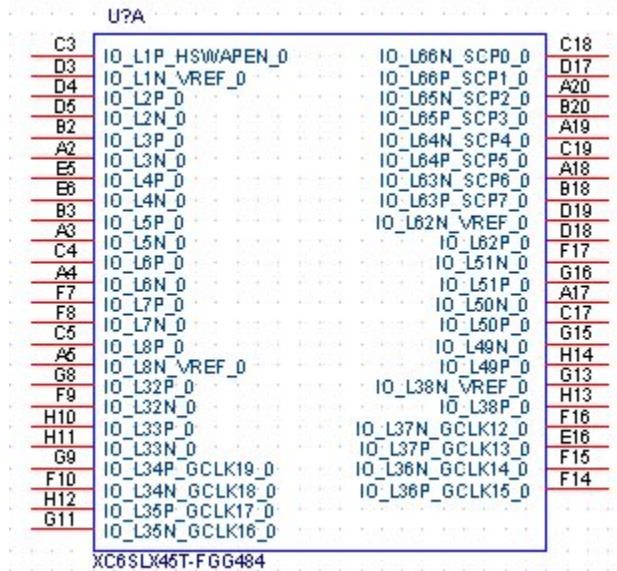


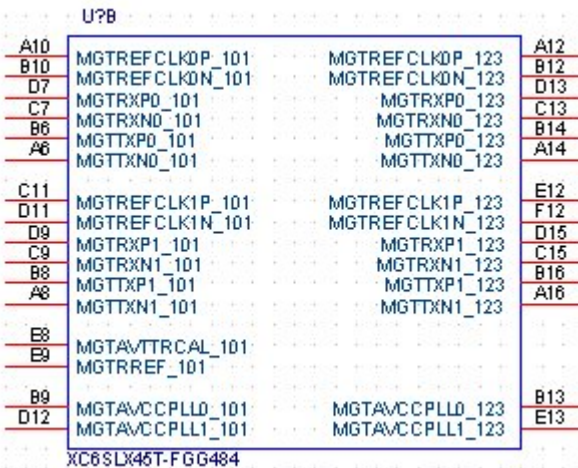
Schematic Symbol for XC6SLX45T-FGG484

The symbol consists of 7 heterogeneous parts, each of them listed below:

1. I/O Bank 0



2. MGT 101 and 123 (BANK0)



3. I/O Bank 1

U?C			
F18	IO_L1P_A25_1	IO_L74N_DOUT_BUSY_1	V20
F19	IO_L1N_A24_VREF_1	IO_L74P_AWAKE_1	V19
H16	IO_L9P_1	IO_L73N_1	T18
H17	IO_L9N_1	IO_L73P_1	T19
B21	IO_L10P_1	IO_L72N_1	T17
B22	IO_L10N_1	IO_L72P_1	R17
J16	IO_L19P_1	IO_L71N_1	P18
J17	IO_L19N_1	IO_L71P_1	P17
C20	IO_L20P_1	IO_L70N_1	R16
C22	IO_L20N_1	IO_L70P_1	R15
L15	IO_L21P_1	IO_L61N_1	M18
K16	IO_L21N_1	IO_L61P_1	M17
D21	IO_L28P_1	IO_L60N_1	P16
D22	IO_L28N_VREF_1	IO_L60P_1	N16
G19	IO_L29P_A23_M1A13_1	IO_L59N_1	T20
F20	IO_L29N_A22_M1A14_1	IO_L59P_1	U19
H18	IO_L30P_A21_M1RESET_1	IO_L58N_1	N15
H19	IO_L30N_A20_M1A11_1	IO_L58P_1	M16
F21	IO_L31P_A19_M1CKE_1	IO_L53N_VREF_1	R19
F22	IO_L31N_A18_M1A12_1	IO_L53P_1	P19
E20	IO_L32P_A17_M1A8_1	IO_L52N_M1DQ15_1	Y22
E22	IO_L32N_A16_M1A9_1	IO_L52P_M1DQ14_1	Y21
J19	IO_L33P_A15_M1A10_1	IO_L51N_M1DQ13_1	W22
H20	IO_L33N_A14_M1A4_1	IO_L51P_M1DQ12_1	W20
K19	IO_L34P_A13_M1WE_1	IO_L50N_M1UDQS1_1	V22
K18	IO_L34N_A12_M1BA2_1	IO_L50P_M1UDQS_1	V21
G20	IO_L35P_A11_M1A7_1	IO_L49N_M1DQ11_1	U22
G22	IO_L35N_A10_M1A2_1	IO_L49P_M1DQ10_1	U20
K17	IO_L36P_A9_M1BA0_1	IO_L48N_M1DQ9_1	T21
L17	IO_L36N_A8_M1BA1_1	IO_L48P_HDC_M1DQ8_1	T22
H21	IO_L37P_A7_M1A0_1	IO_L47N_LDC_M1DQ1_1	R22
H22	IO_L37N_A6_M1A1_1	IO_L47P_FWE_B_M1DQ0_1	R20
K20	IO_L38P_A5_M1CLK_1	IO_L46N_FOE_B_M1DQ3_1	P22
L19	IO_L38N_A4_M1CLKN_1	IO_L46P_FCS_B_M1DQ2_1	P21
J20	IO_L39P_M1A3_1	IO_L45N_A0_M1LDQSN_1	N22
J22	IO_L39N_M1ODT_1	IO_L45P_A1_M1LDQS_1	N20
M20	IO_L40P_GCLK11_M1A5_1	IO_L44N_A2_M1DQ7_1	M22
M19	IO_L40N_GCLK10_M1A6_1	IO_L44P_A3_M1DQ6_1	M21
K21	IO_L41P_GCLK9_TRDY1_M1RASN_1	IO_L43N_GCLK4_M1DQ5_1	L22
K22	IO_L41N_GCLK8_M1CASN_1	IO_L43P_GCLK5_M1DQ4_1	L20
P20	IO_L42P_GCLK7_M1UDM_1	IO_L42N_GCLK6_TRDY1_M1LDM_1	N19

XC6SLX45T-FGG484

4. I/O Bank2 (Contains the Programming Interface)

U?D			
V18	CMPCS_B_2	TDO	G17
AB21	DONE_2	TMS	D20
Y20	IO_L1P_CCLK_2	TDI	E18
AA21	IO_L1N_M0_CMPMISO_2	TCK	A21
V17	IO_L2P_CMPCLK_2		
W18	IO_L2N_CMPMOSI_2		
AA20	IO_L3P_D0_DIN_MISO_MISO1_2	SUSPEND	AA22
AB20	IO_L3N_MOSI_CSI_B_MIS0D_2	NC	T16
U16	IO_L4P_2	NC	P15
V15	IO_L4N_VREF_2	NC	U17
W17	IO_L5P_2		
Y18	IO_L5N_2	PROGRAM_B_2	AB2
AA14	IO_L6P_2	IO_L65N_CSO_B_2	AA3
AB14	IO_L6N_2	IO_L65P_INIT_B_2	Y4
R13	IO_L12P_D1_MISO2_2	IO_L64N_D9_2	U6
T14	IO_L12N_D2_MISO3_2	IO_L64P_D8_2	T7
Y19	IO_L13P_M1_2	IO_L63N_2	AB4
AB19	IO_L13N_D10_2	IO_L63P_2	AA4
AA18	IO_L14P_D11_2	IO_L62N_D6_2	AB5
AB18	IO_L14N_D12_2	IO_L62P_D5_2	Y5
Y17	IO_L15P_2	IO_L60N_2	Y6
AB17	IO_L15N_2	IO_L60P_2	W6
U14	IO_L16P_2	IO_L59N_2	R8
U13	IO_L16N_VREF_2	IO_L59P_2	R9
Y16	IO_L17P_2	IO_L58N_2	W8
W15	IO_L17N_2	IO_L58P_2	V7
V13	IO_L18P_2	IO_L57N_2	U8
W13	IO_L18N_2	IO_L57P_2	T8
AA16	IO_L19P_2	IO_L50N_2	V9
AB16	IO_L19N_2	IO_L50P_2	U9
W14	IO_L20P_2	IO_L49N_D4_2	AB6
Y14	IO_L20N_2	IO_L49P_D3_2	AA6
Y15	IO_L21P_2	IO_L48N_RDWR_B_VREF_2	Y8
AB15	IO_L21N_2	IO_L48P_D7_2	W9
R11	IO_L22P_2	IO_L47N_2	AB7
T11	IO_L22N_2	IO_L47P_2	Y7
T15	IO_L23P_2	IO_L46N_2	U10
U15	IO_L23N_2	IO_L46P_2	T10
T12	IO_L29P_GCLK3_2	IO_L45N_2	AB8
U12	IO_L29N_GCLK2_2	IO_L45P_2	AA8
Y13	IO_L30P_GCLK1_D13_2	IO_L44N_2	Y10
AB13	IO_L30N_GCLK0_USERCCLK_2	IO_L44P_2	W10
AA12	IO_L31P_GCLK3T_D14_2	IO_L43N_2	AB9
AB12	IO_L31N_GCLK3D_D15_2	IO_L43P_2	Y9
Y11	IO_L32P_GCLK29_2	IO_L42N_2	W11
AB11	IO_L32N_GCLK28_2	IO_L42P_2	V11
W12	IO_L40P_2	IO_L41N_VREF_2	AB10
Y12	IO_L40N_2	IO_L41P_2	AA10

XC6SLX45T-FGG484

5. I/O Bank 3

U?E		
R7	IO_L1P_3	IO_L83N_VREF_3
P8	IO_L1N_VREF_3	IO_L83P_3
W4	IO_L2P_3	IO_L82N_3
Y3	IO_L2N_3	IO_L82P_3
T6	IO_L7P_3	IO_L81N_3
T5	IO_L7N_3	IO_L81P_3
V5	IO_L8P_3	IO_L80N_3
V3	IO_L8N_3	IO_L80P_3
P5	IO_L9P_3	IO_L80N_3
P4	IO_L9N_3	IO_L80P_3
AA2	IO_L10P_3	IO_L80P_3
AA1	IO_L10N_3	IO_L80P_3
N6	IO_L23P_3	IO_L59P_3
N7	IO_L23N_3	IO_L59N_3
U4	IO_L24P_3	IO_L58P_3
T4	IO_L24N_3	IO_L57N_VREF_3
P6	IO_L25P_3	IO_L57P_3
P7	IO_L25N_3	IO_L55N_M3A14_3
T3	IO_L26P_3	IO_L55P_M3A13_3
R4	IO_L26N_3	IO_L54N_M3A11_3
M7	IO_L31P_3	IO_L54P_M3RESET_3
M6	IO_L31N_VREF_3	IO_L53N_M3A12_3
Y2	IO_L32P_M3DQ14_3	IO_L53P_M3CKE_3
Y1	IO_L32N_M3DQ15_3	IO_L52N_M3A9_3
W3	IO_L33P_M3DQ12_3	IO_L52P_M3A8_3
W1	IO_L33N_M3DQ13_3	IO_L51N_M3A4_3
V2	IO_L34P_M3UDQ5_3	IO_L51P_M3A10_3
V1	IO_L34N_M3UDQ5N_3	IO_L50N_M3BA2_3
U3	IO_L35P_M3DQ10_3	IO_L50P_M3WE_3
U1	IO_L35N_M3DQ11_3	IO_L49N_M3A2_3
T2	IO_L36P_M3DQ8_3	IO_L49P_M3A7_3
T1	IO_L36N_M3DQ9_3	IO_L48N_M3BA1_3
R3	IO_L37P_M3DQ0_3	IO_L48P_M3BA0_3
R1	IO_L37N_M3DQ1_3	IO_L47N_M3A1_3
P2	IO_L38P_M3DQ2_3	IO_L47P_M3A0_3
P1	IO_L38N_M3DQ3_3	IO_L46N_M3CLKN_3
N3	IO_L39P_M3LDQ5_3	IO_L46P_M3CLK_3
N1	IO_L39N_M3LDQ5N_3	IO_L45N_M3DDT_3
M2	IO_L40P_M3DQ6_3	IO_L45P_M3A3_3
M1	IO_L40N_M3DQ7_3	IO_L44N_GCLK20_M3A6_3
L3	IO_L41P_GCLK27_M3DQ4IG_L43N_GCLK22_IRDY2_M3CASN_3	IO_L44P_GCLK21_M3A6_3
L1	IO_L41N_GCLK26_M3DQ5_3	IO_L43P_GCLK23_M3RASN_3
P3	IO_L42P_GCLK25_TRDY2_M3UDM10_L42N_GCLK24_M3LDM_3	

XC6SLX45T-FGG484

7. Power

U?G		
C10	MGTAVCC_101	
E10	MGTAVCC_123	
D8	MGTAVTRX_101	
D14	MGTAVTRX_123	
A7	MGTAVTTX_101	
A15	MGTAVTTX_123	
	VCC0_3	W2
	VCC0_3	U5
	VCC0_3	R2
	VCC0_3	N5
	VCC0_3	L7
F11	VCCAUX	VCC0_3
G12	VCCAUX	VCC0_3
H15	VCCAUX	VCC0_3
H9	VCCAUX	VCC0_3
K15	VCCAUX	VCC0_3
L8	VCCAUX	VCC0_3
M15	VCCAUX	VCC0_3
N8	VCCAUX	VCC0_2
R10	VCCAUX	VCC0_2
R12	VCCAUX	VCC0_2
R6	VCCAUX	VCC0_2
U11	VCCAUX	VCC0_2
V6	VCCAUX	VCC0_2
	VCC0_2	W5
	VCC0_2	V8
	VCC0_2	V16
	VCC0_2	V12
	VCC0_2	T9
	VCC0_2	T13
	VCC0_2	AB3
	VCC0_2	AA7
	VCC0_2	AA19
	VCC0_2	AA15
	VCC0_2	AA11
J10	VCCINT	VCC0_1
J12	VCCINT	VCC0_1
J14	VCCINT	VCC0_1
J8	VCCINT	VCC0_1
K11	VCCINT	VCC0_1
K13	VCCINT	VCC0_1
K9	VCCINT	VCC0_1
L10	VCCINT	VCC0_1
L12	VCCINT	VCC0_1
L14	VCCINT	VCC0_1
M11	VCCINT	VCC0_1
M13	VCCINT	VCC0_1
M9	VCCINT	VCC0_1
N10	VCCINT	VCC0_1
N12	VCCINT	VCC0_0
N14	VCCINT	VCC0_0
P11	VCCINT	VCC0_0
P13	VCCINT	VCC0_0
P9	VCCINT	VCC0_0
R14	VCCINT	VCC0_0
	VCC0_0	W21
	VCC0_0	U18
	VCC0_0	R21
	VCC0_0	N18
	VCC0_0	L21
	VCC0_0	L16
	VCC0_0	J18
	VCC0_0	G21
	VCC0_0	E19
	VCC0_0	C21
	VCC0_0	G14
	VCC0_0	G10
	VCC0_0	F6
	VCC0_0	E17
	VCC0_0	B4
	VCC0_0	B19

XC6SLX45T-FGG484

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Notes:

1. The dedicated pins DONE_2 and PROGRAM_B are powered by Bank2.
2. The JTAG pins and SUSPEND are powered by VCCAUX.
3. When SUSPEND is not used, connect this pin to GND.
4. CMPCS_B_2 –Reserved Input. Connect high or leave unconnected.
5. The following parts in this package have similar but not identical pinout: LX25T, LX45T, LX75T, LX100T and LX150T. If migration between different component densities is desired, please pay attention to the NC pins on each of the devices that are targeted for implementation. For details please check the UG385, “Spartan 6 Packaging and Pinouts” User Guide that can be found at:
http://www.xilinx.com/support/documentation/user_guides/ug385.pdf

Document Revision History

	Revision	Date	By	Comments
1	1.00	Mar 15, 2010	LD	Initial Release –Uses Xilinx Pinout ASCII File -02/22/2010. Check the Xilinx website for updates.