



256 Kb (32K x8) TIMEKEEPER $^{\scriptsize (B)}$ SRAM

PRELIMINARY DATA

- INTEGRATED ULTRA LOW POWER SRAM, REAL TIME CLOCK, POWER-FAIL CONTROL CIRCUIT, BATTERY and CRYSTAL
- BCD CODED YEAR, MONTH, DAY, DATE, HOURS, MINUTES and SECONDS
- AUTOMATIC POWER-FAIL CHIP DESELECT and WRITE PROTECTION
- WRITE PROTECT VOLTAGE (V_{PFD} = Power-fail Deselect Voltage):
 - $M48T39Y: 4.20V \le V_{PFD} \le 4.50V$
- PROGRAMMABLE INTERRUPTS and SQUARE WAVE OUTPUT
- WATCHDOG TIMER RESTARTS on OUT-OF-CONTROL PROCESSOR
- CLOCK ACCURACY IS BETTER THAN ±1 MINUTE per MONTH at 25°C
- 10 YEARS of DATA RETENTION and CLOCK OPERATION in the ABSENCE of POWER
- SELF-CONTAINED BATTERY and CRYSTAL in DIP PACKAGE
- PROGRAMMABLE ALARM OUTPUT ACTIVE in the BATTERY BACK-UP MODE
- PIN and FUNCTION COMPATIBLE with DS1386

DESCRIPTION

The M48T39Y TIMEKEEPER® RAM is a non-volatile 262,144 bit static RAM and real time clock organized as 32,768 words by 8 bits. System integration features include Programmable Alarms, Watchdog Timer and Interval Timer. The special 32-pin DIP package provides a highly integrated battery back-up memory and real time clock solution.

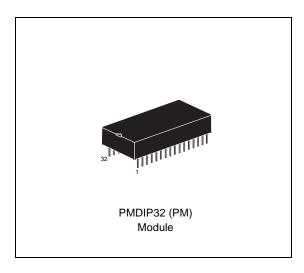
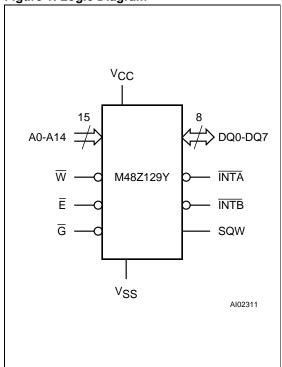


Figure 1. Logic Diagram



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This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

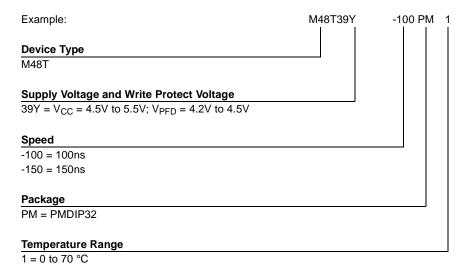
Figure 2. DIP Connections

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INTA [1	$\overline{}$	32] V _{CC}
INTB [2		31 SQW
A14 🛚 3	M48T39Y	30 J V _{CC}
A12 🛚 4		29 🕽 \overline W
A7 🛚 5		28 A13
A6 [6		27] A8
A5 [7		26 A9
A4 🛚 8		25 A11
A3 🛚 9		24 🛚 🛱
A2 [10		23 A10
A1 [11		22] Ē
A0 [12		21 DQ7
DQ0 [13		20 DQ6
DQ1 [14		19 DQ5
DQ2 [15		18 DQ4
V _{SS} [16		17 DQ3
	Al	02312

Table 1. Signal Names

A0-A14	Address Inputs			
DQ0-DQ7	Data Inputs / Outputs			
Ē	Chip Enable Input			
G	Output Enable Input			
W	Write Enable Input			
ĪNTĀ	Interrupt Output A (Open Drain)			
ĪNTB	Interrupt Output B (Open Drain)			
SQW	Square Wave Output			
Vcc	Supply Voltage			
V _{SS}	Ground			

Table 2. Ordering Information Scheme



For a list of available options (Speed, Package, etc...) or for further information on any aspect of this device, please contact the ST Sales Office nearest to you.

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Table 3. PMDIP32 - 32 pin Plastic Module DIP, Package Mechanical Data

		mm		inches		
Symb	Symb Typ	Min	Max	Тур	Min	Max
А		9.27	9.52		0.3650	0.3748
A1		0.38	-		0.0150	_
В		0.43	0.59		0.0169	0.0232
С		0.20	0.33		0.0079	0.0130
D		42.42	43.18		1.6701	1.7000
E		18.03	18.80		0.7098	0.7402
e1		2.29	2.79		0.0902	0.1098
e3		34.29	41.91		1.3500	1.6500
eA		14.99	16.00		0.5902	0.6299
L		3.05	3.81		0.1201	0.1500
S		1.91	2.79		0.0752	0.1098
N	32			32		

Drawing is not to scale.

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