TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8025F

PICK UP SENSOR INTERFACE IC

The TA8025F is an IC designed for making the output signal from electromagnetic pick up sensor and etc..., waveformshaping. The V_{th} of input has hysteresis that is division value between peak voltage of input signal and 0V.

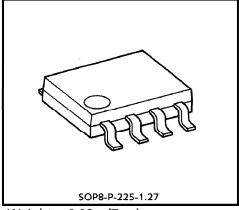
FEATURES

Input frequency : DC~50kHz

Input voltage V_{TH} : 0V⇔Vpeak×K

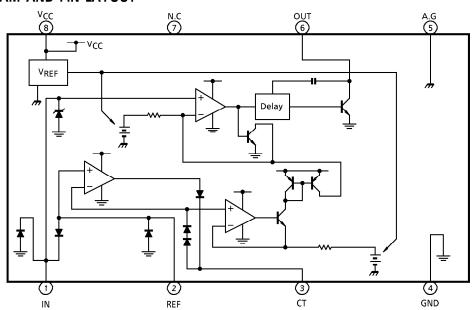
Small package : SOP 8pin

Separate GND line for output and logic control sections



Weight: 0.08g (Typ.)

BLOCK DIAGRAM AND PIN LAYOUT



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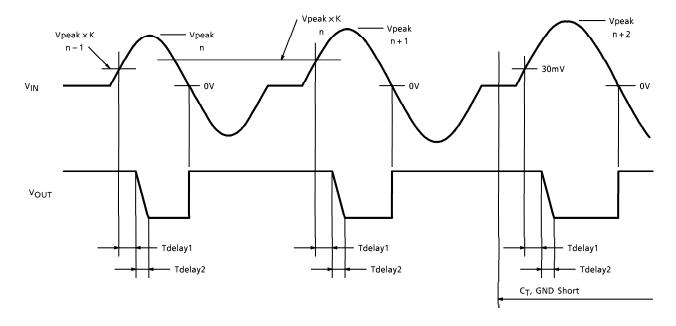
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PIN DESCRIPTION

PIN No.	SYMBOL	DESCRIPTION				
1	IN	Input pin for a signal from sensor.				
2	REF	V_{TH} setting pin. The V_{TH} value can be set according to divide the input signal with resistors.				
3	СТ	This pin hold the peak value of input signal of REF pin.				
4	GND	Grounded.				
5	A.G	Grounded pin for REF.				
6	OUT	The output is an NPN open-collector output and the input signal which is made waveform-shaping is gone out. When the output goes down, it has a slope of 1V/ μ s in order to lose the influence for the input signal.				
7	N.C	Not connected.				
8	VCC	Power supply pin.				

TIMING CHART



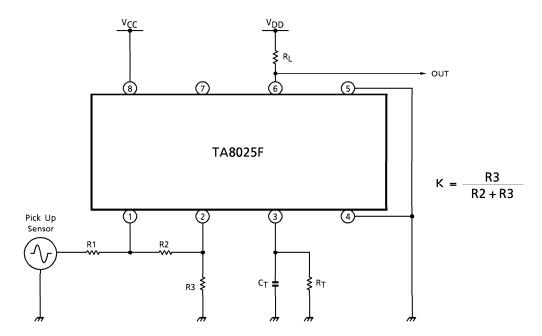
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _C C	36	V
Input Voltage	VIN	36	V
Input Current	IN	± 20	mA
Output Current	lout	10	mA
Power Dissipation	PD	280	mW
Operating Voltage	V _{opr}	4.5~30	V
Operating Temperature	T _{opr}	<i>-</i> 40∼105	°C
Storage Temperature	T _{stg}	- 55∼150	°C
Lead Temperature · Time	T _{sol}	260 (10s)	°C

ELECTRICAL CHARACTERISTICS ($V_{CC} = 4.5 \sim 16V$, $T_{C} = -40 \sim 105^{\circ}C$)

CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current	1	Vaa	-	Output : OFF	_	3.0	5.0	mA
Supply Current	Icc	VCC		Output : ON	-	4.5	8.0	
lamus Current	Lee			V _{IN} = 0V	-0.2	_	0.1	μΑ
Input Current	IN		-	$V_{IN} = V_{CC}$	- 0.1	-	0.1	
High-Side Minimum Threshold Voltage	V _{TH1}	I _N	_	V _{REF} = 0V	24	30	36	· mA
Zero-Cross Threshold Voltage	V _{TH2}				- 20	_	20	
Zener Voltage	VZ		_	I _{IN} = 1mA	24	30	36	V
Innut Cumant	IIN	REF	_	V _{IN} = 0V	-0.2	_	0.1	μΑ
Input Current				V _{IN} = V _{CC}	- 0.1	_	0.1	
Output Voltage	tput Voltage V _{OL}		_	I _{OL} = 5mA	_	_	0.5	V
Output Leakage Current	ILEAK	OUT	_	V _{OH} = 5V	- 5.0	_	5.0	μΑ
Output Delay Time	Tdelay1			V _{CC} = 16V		7.5	20.0	μs
Output Delay Time	Tdelay2		_	V _{DD} = 5V		5.0	10.0	

EXAMPLE OF APPLICATION CIRCUIT



OUTLINE DRAWING SOP8-P-225-1.27 Unit : mm 0.595TYP 1.27 0.4±0.1 0.4±0.1 0.595TYP 1.27 0.525±0.2

Weight: 0.08g (Typ.)