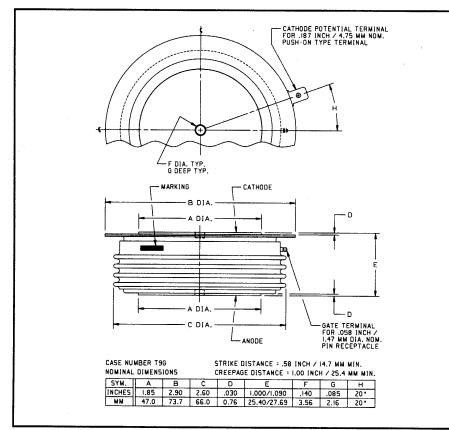
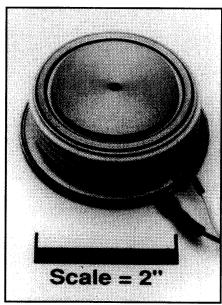


Phase Control SCR 1500 Amperes Average 2400 Volts





C451 Phase Control SCR 1500 Amperes Average, 2400 Volts

C451 (Outline Drawing)

Ordering Information:

Select the complete five or six digit part number you desire from the table, i.e. C451LD is a 2400 Volt, 1500 Ampere Phase Control SCR.

Туре	Volt	Current	
	V _{DRM} V _{RRM}	Code	T(av)
C451	1400	PD	1500
	1600	PM	
,	1800	PN	
	2000	L	
	2200	LB	
	2400	LD	

Downloaded from Elcodis.com electronic components distributor

Description:

Powerex Silicon Controlled Rectifiers (SCR) are designed for phase control applications. These are all-diffused, Press-Pak, hermetic Pow-R-Disc devices employing the field proven amplifying gate.

Features:

- Low On-State Voltage
- High di/dt Capability
- High dv/dt Capability
- Hermetic Packaging
- Excellent Surge and I²t Ratings

Applications:

- Power Supplies
- Motor Control
- VAR Generators



C451

Phase Control SCR 1500 Amperes Average, 2400 Volts

Absolute Maximum Ratings

Characteristics	Symbol	C451	Units Volts	
Non-repetitive Transient Peak Reverse Voltage	V _{RSM}	V _{RRM} + 100V		
RMS On-state Current, T _C = 64°C	I _{T(rms)}	2350	Amperes	
Average Current 180° Sine Wave, T _C = 64°C	I _{T(av)}	1500	Amperes	
RMS On-state Current, T _C = 55°C	IT(rms)	2590	Amperes	
Average Current 180° Sine Wave, T _C = 55°C	I _{T(av)}	1650	Amperes	
Peak One Cycle Surge On-state Current (Non-repetitive) 60Hz	l _{tsm}	23000	Amperes	
Peak One Cycle Surge On-state Current (Non-repetitive) 50Hz	Itsm	20800	Amperes	
Critical Rate-of-rise of On-state Current (Non-repetitive)	di/dt	400	A/µsec	
Critical Rate-of-rise of On-state Current (Repetitive)	di/dt	75	A/µsec	
I ² t (for Fusing) for One Cycle, 60Hz	l ² t	2.2 x 10 ⁶	A ² sec	
Peak Gate Power Dissipation	PGM	200	Watts	
Average Gate Power Dissipation	P _{G(av)}	5	Watts	
Operating Temperature	T _i	-40 to +125°C	°C	
Storage Temperature	T _{stg}	-40 to +150°C	°C	
Approximate Weight		1	lb.	
		454	g	
Mounting Force		5500 to 6000	lb.	
		2450 to 2670	kg.	



C451

Phase Control SCR

1500 Amperes Average, 2400 Volts

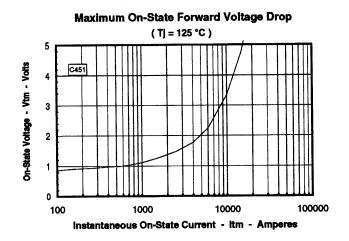
Electrical Characteristics, $T_j = 25^{\circ}C$ Unless Otherwise Specified

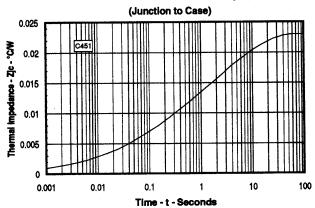
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Repetitive Peak Reverse Leakage Current	IRRM	T _j = 125°C, V _R = V _{RRM}			45	mA
Repetitive Peak Forward Leakage Current	DRM	$T_j = 125^{\circ}C, V_D = V_{DRM}$			45	mA
Peak On-state Voltage	VTM	I _{TM} = 3000A Peak			1.7	Volts
		Duty Cycle < 0.1%				
Threshold Voltage, Low-level	V _{(TO)1}	$T_j = 125^{\circ}C, I = 15\%, I_{T(av)} to \pi I_{T(av)}$		<i></i>	0.87956	Volts
Slope Resistance, Low-level	۲T1				0.2271	mΩ
Threshold Voltage, High-level		$T_j = 125^{\circ}C$, $I = \pi I_{T(av)}$ to I_{TSM}		••••••••••••••••••••••••••••••••••••••	0.59931	Volts
Slope Resistance, High-level	rT2				0.2781	mΩ
V _{TM} Coefficients, Low-level		$T_j = 125^{\circ}C, I = 15\% I_{T(av)} to \pi I_{T(av)}$				
				A	1 = 0.839	82
				B ₁ = 4.972E-04		
				$C_1 = 2.032E-04$		E-04
				D	1 = 0.002	154
V _{TM} Coefficients, High-level		$T_j = 125^{\circ}C, I = \pi I_T(av)$ to ITSM				
				A	2 = 12.127	,
					2 = -1.809	
					2 = 1.429	
Turket Data T				$D_2 = 0.064436$		436
Typical Delay Time	ťd	l _T = 50A, Gate = 20V, 20Ω,		0.7		μsec
	••••••••••••••••••••••••••••••••••••••	0.1µsec Rise				
Typical Turn-off Time	tq	T _j = 125°C, I _T = 2000A,		150		μsec
		di _R /dt = 25A/µsec Reapplied				
		dv/dt = 200V/µsec Linear to				
		$80\% V_{DRM}, V_{R} = 50V,$				
		Gate = 0V, R_{GK} = 100 Ω		·		
Minimum Critical dv/dt - Exponential to V _{DRM}	dv/dt	T _j = 125°C	400			V/µsec
Gate Trigger Current	^I GT	T _j = 25°C,			200	mA
		$V_D = 20V_{DC}, R_L = 3\Omega$				
Gate Trigger Voltage	V _{GT}	T _j = -40°C to +125°C,			5.0	Volts
		$V_D = 20V, R_L = 3\Omega$				
Non-Triggering Gate Voltage	V _{GDM}	T _j = 125℃,			0.15	Volts
		$V_{D} = V_{DRM}, R_{L} = 1000\Omega$				
Peak Forward Gate Current	^I GTM	B Brim L			10	Α
Peak Reverse Gate Voltage	VGRM				5	Volts
	<u> </u>	· · · · · · · · · · · · · · · · · · ·			`	10110
Thermal Characteristics						
Maximum Thermal Resistance, Double Sided Co	oling	· · · · · · · · · · · · · · · · · · ·				
Junction-to-Case	R _{θ(j-c)}				0.025	°C/W
Case-to-Sink						
	R _{θ(c-s)}		····		0.0075	°C/W



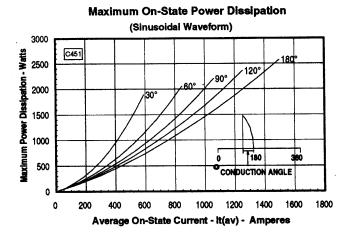
C451

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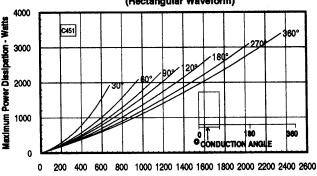




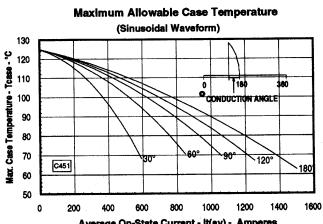
Maximum Transient Thermal Impedance



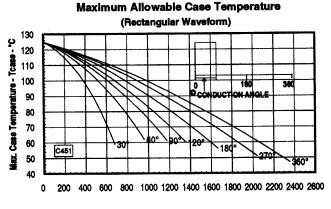








Average On-State Current - It(av) - Amperes



Average On-State Current - It(av) - Amperes