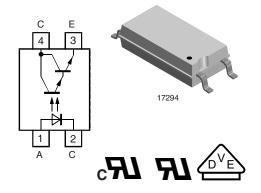


**Vishay Semiconductors** 

# Optocoupler, Photodarlington Output, SOP-4L, Long Mini-Flat Package



#### DESCRIPTION

The TCLD1000 consists of a darlington phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4-lead SO6L package.

The elements are mounted on one leadframe, provide a fixed distance between input and output for highest safety requirements.

#### AGENCY APPROVALS

- UL1577, file no. E76222 system code W, double protection
- CSA 22.2 bulletin 5A, double protection
- BSI IEC 60950 IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending

### FEATURES

- Low profile package
- Darlington output
- Extra low coupling capacity typical 0.2 pF
- High common mode rejection
- Creepage current resistance according to **RoHS** VDE 0303/IEC 60112 comparative tracking COMPLIANT index: CTI  $\geq$  175
- Thickness though insulation ≥ 0.75 mm
- Creepage distance > 8 mm
- Tested acc. 60950: AM4: 1997 clause 2.9.6.
- · Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **APPLICATIONS**

- Switch-mode power supplies
- Line receiver
- Computer peripheral interface
- Microprocessor system interface
- Reinforced isolation provides circuit protection against electrical shock (safety class II)
- Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):
  - for appl. class I IV at mains voltage  $\leq 300$  V

- for appl. class I - III at mains voltage  $\leq 600$  V according to DIN EN 60747-5-2 (VDE 0884)/ DIN EN 60747-5-5 pending.

ORDER INFORMATION	
PART	REMARKS
TCLD1000	CTR > 600 %, SMD-4

ABSOLUTE MAXIMUM RAT	INGS <sup>(1)</sup>			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT	· · · · · · · · · · · · · · · · · · ·			
Reverse voltage		V <sub>R</sub>	6	V
Forward current		l <sub>F</sub>	60	mA
Forward surge current	$t_p \le 10 \ \mu s$	I <sub>FSM</sub>	1.5	А
Power dissipation		P <sub>diss</sub>	100	mW
Junction temperature		Tj	125	°C
OUTPUT				
Collector emitter voltage		V <sub>CEO</sub>	35	V
Emitter collector voltage		V <sub>ECO</sub>	7	V
Collector current		Ι <sub>C</sub>	80	mA
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA
Power dissipation		P <sub>diss</sub>	150	mW
Junction temperature		T <sub>i</sub>	125	°C

Document Number: 83516 Rev. 1.8, 09-Jan-08 For technical questions, contact: optocoupler.answers@vishay.com

www.vishay.com

# **TCLD1000**

## **Vishay Semiconductors**



## Optocoupler, Photodarlington Output, SOP-4L, Long Mini-Flat Package

ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup>								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
COUPLER								
Isolation test voltage (RMS)		V <sub>ISO</sub>	5000	V <sub>RMS</sub>				
Total power dissipation		P <sub>tot</sub>	250	mW				
Operating ambient temperature range		T <sub>amb</sub>	- 40 to + 100	°C				
Storage temperature range		T <sub>stg</sub>	- 40 to + 125	°C				
Soldering temperature <sup>(2)</sup>		T <sub>sld</sub>	260	°C				

Note

 $^{(1)}$  T<sub>amb</sub> = 25 °C, unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(2)</sup> Refer to reflow profile for soldering conditions for surface mounted devices.

<b>ELECTRICAL CHARACTE</b>	RISTCS					
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	·					
Forward voltage	I <sub>F</sub> = 20 mA	V <sub>F</sub>		1.1	1.4	V
Junction capacitance	$V_R = 0 V, f = 1 MHz$	Cj		50		pF
OUTPUT						
Collector emitter voltage	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	32			V
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V
Collector ermitter cut-off current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}, E = 0$	I <sub>CEO</sub>		15	100	nA
COUPLER						
Collector emitter saturation voltage	$I_{F} = 20 \text{ mA}, I_{C} = 5 \text{ mA}$	V <sub>CEsat</sub>			1.0	V
Cut-off frequency	$\label{eq:Vce} \begin{array}{l} V_{CE} = 5 \ V, \ I_{F} = 10 \ mA, \\ R_{L} = 100 \ \Omega \end{array}$	f <sub>c</sub>		10		kHz
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.3		pF

#### Note

 $T_{amb} = 25 \ ^{\circ}C$ , unless otherwise specified.

Minimum and maximum values are tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
I <sub>C</sub> /I <sub>F</sub>	$V_{CE} = 2 V$ , $I_F = 1 mA$	CTR	600	800		%	

MAXIMUM SAFETY RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward current		I <sub>F</sub>			130	mA	
OUTPUT							
Power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	P <sub>diss</sub>			265	mW	
COUPLER							
Rated impulse voltage		V <sub>IOTM</sub>			8	kV	
Safety temperature		T <sub>si</sub>			150	°C	

#### Note

According to DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending (see figure 1). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.



# Optocoupler, Photodarlington Output, SOP-4L,

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**TCLD1000** 

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Long	Mini-Flat	Package

INSULATION RATED PARAMETERS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	2.0			kV
Partial discharge test voltage - lot test (sample test)	t <sub>Tr</sub> = 60 s, t <sub>test</sub> = 10 s,	VIOTM	8			kV
	(see figure 2)	V <sub>pd</sub>	1.68			kV
	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	10 <sup>11</sup>			Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>			Ω

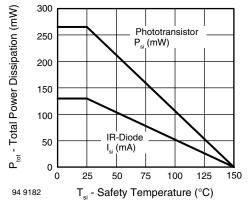


Fig. 1 - Derating Diagram

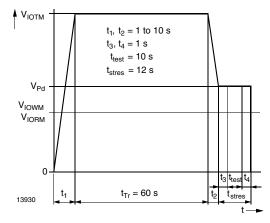
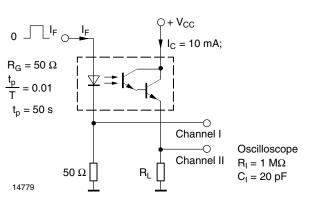
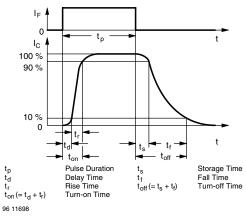


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-; IEC 60747

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	$V_{CE} = 2 \text{ V}, \text{ I}_{C} = 10 \text{ mA}, \text{ R}_{L} = 100 \Omega$ , (see figure 3)	t <sub>r</sub>		300		μs
Turn-off time	$V_{CE} = 2 \text{ V}, \text{ I}_{C} = 10 \text{ mA}, \text{ R}_{L} = 100 \Omega$ , (see figure 3)	t <sub>off</sub>		250		μs









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## Vishay Semiconductors

## Optocoupler, Photodarlington Output, SOP-4L, Long Mini-Flat Package



## **TYPICAL CHARACTERISTICS**

T<sub>amb</sub> = 25 °C, unless otherwise specified

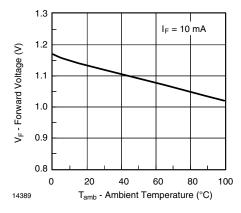


Fig. 5 - Forward Voltage vs. Ambient Temperature

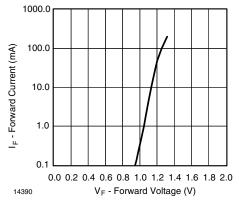


Fig. 6 - Forward Current vs. Forward Voltage

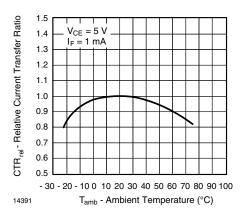


Fig. 7 - Relative Current Transfer Ratio vs. Ambient Temperature

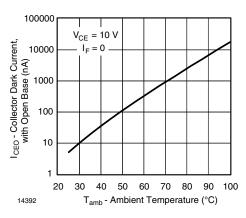


Fig. 8 - Collector Dark Current vs. Ambient Temperature

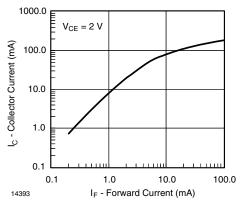


Fig. 9 - Collector Current vs. Forward Current

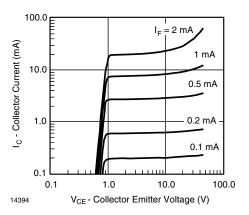


Fig. 10 - Collector Current vs. Collector Emitter Voltage



## **TCLD1000**

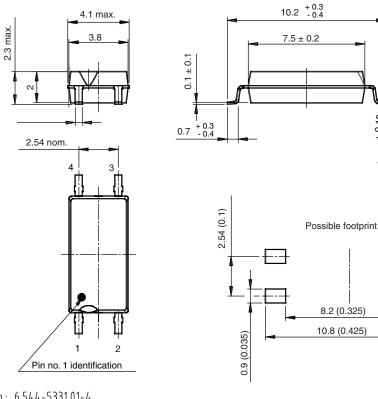
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## **PACKAGE DIMENSIONS** in millimeters



Drawing-No.: 6.544-5331.01-4 Issue: 1; 04.04.00 15243

technical drawings according to DIN specifications

**--**

0.15 + 0.10

8.2 (0.325) 10.8 (0.425) Optocoupler, Photodarlington Output,



SOP-4L,

## Long Mini-Flat Package

## **OZONE DEPLETING SUBSTANCES POLICY STATEMENT**

It is the policy of Vishay Semiconductor GmbH to

**Vishay Semiconductors** 

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

> We reserve the right to make changes to improve technical design and may do so without further notice.

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