Analog Power

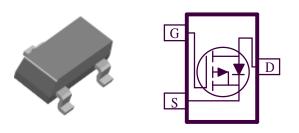
AM1331P

P-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SC70-3 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY					
V _{DS} (V)	$\mathbf{r}_{\mathrm{DS(on)}} (\mathrm{OHM}) \qquad \mathbf{I}_{\mathrm{D}} (\mathrm{A})$				
-30	$0.112 @ V_{CS} = -10V$	-1.5			
	$0.172 @ V_{CS} = -4.5V$	-1.2			



ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Maximum	Units			
Drain-Source Voltage			-30	V		
Cate-Source Voltage			±2 0	v		
Continuous Drain Current ^a	$T_A=25^{\circ}C$	T	-1.5			
	$\frac{T_{A}=25^{\circ}C}{T_{A}=70^{\circ}C}$		-1.2	А		
Pulsed Drain Current ^b		IDM	-2.5			
Continuous Source Current (Diode Conduction) ^a		Is	±0.28	Α		
	$T_A=25^{\circ}C$	D.	0.34	W		
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	тD	0.22	**		
Operating Junction and Storage Temperature Range		TJ, Tstg	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
	t <=5 sec	D	375	⁰ CN1		
Maximum Junction-to-Ambient ^a	Steady-State	R _{THJA}	430	C/w		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

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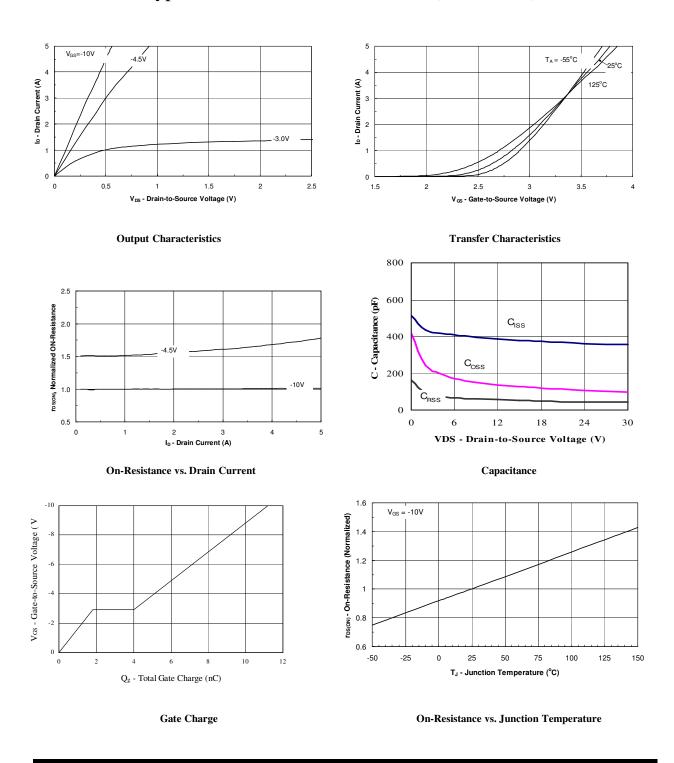
SPECIFICATIONS ($T_A = 25^{\circ}$ CUNLESS OTHERWISE NOTED)								
D	Gentel	To at Court to and	Limits					
Parameter	Symbol	Symbol Test Conditions		Тур	Max	lax Unit		
Static								
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \text{ uA}$	-1			V		
Gate-Body Leakage	IGSS	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			±100	nA		
Zene Cota Valta da Draine Gument	I	$V_{DS} = -24 V$, $V_{GS} = 0 V$			-1			
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -24 V, V_{CS} = 0 V, T_J = 55^{\circ}C$			-10	uA		
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = -5 V$, $V_{GS} = -10 V$	-5			Α		
		$V_{GS} = -10 \text{ V}, I_D = -1.5 \text{ A}$			112	mΩ		
Drain-Source On-Resistance ^A	r _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_D = -1.2 \text{ A}$			172			
Forward Tranconductance ^A	g _{ís}	$V_{DS} = -5 V$, $I_D = -1.5 A$		9		S		
Diode Forward Voltage	Vsd	$I_{\rm S} = -0.46 \text{A}, V_{\rm GS} = 0 \text{V}$		-0.65		V		
Dynamic ^b								
Total Gate Charge	Qg	V 10XXX 5X		7.2				
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 V$, $V_{CS} = -5 V$, $I_{D} = -1.5 A$		1.7		nC		
Gate-Drain Charge	Qgd	$I_D = -1.3 \text{ A}$		1.5				
Tum-On Delay Time	t _{d(on)}			10				
Rise Time	tr	$V_{DD} = -10 \text{ V}, \text{ I}_{L} = -1 \text{ A},$		9		ns		
Tum-Off Delay Time	t _{d(off)}	V_{GEN} =-4.5 V, R_{G} =6 Ω		27				
Fall-Time	t _f			11				

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.
- c. Repetitive rating, pulse width limited by junction temperature.

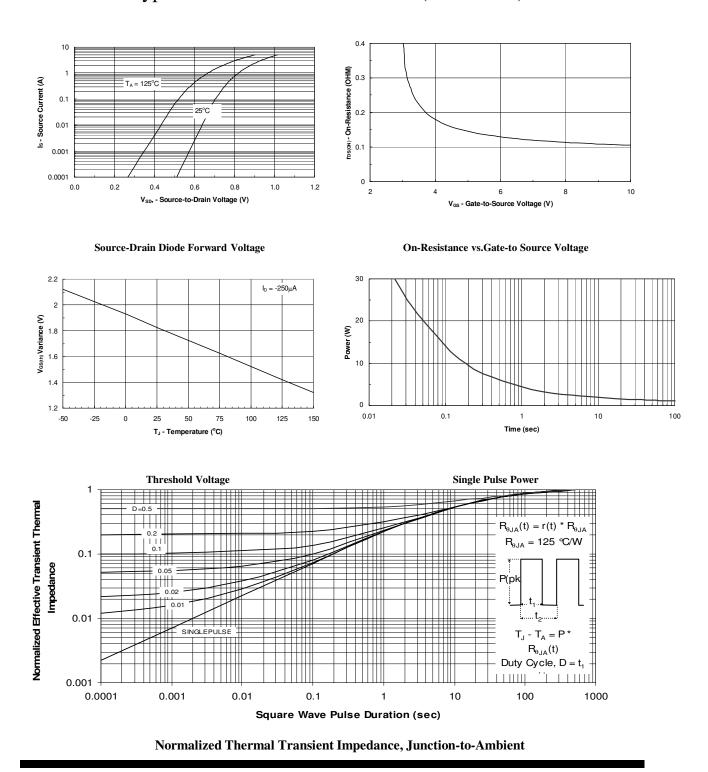
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Typical Electrical Characteristics (P-Channel)

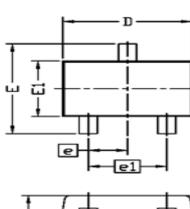


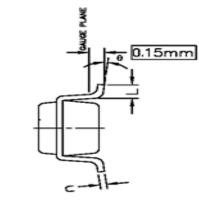
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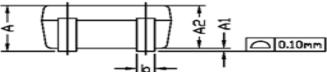
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Package Information

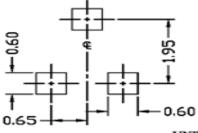
SC70 PACKAGE OUTLINE







RECOMMENDED LAND PATTERN



FYMEROLS	DIMENSIONS IN MILLIMETERS			DOMENSIONS IN INCISIS			
FIMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
Α			1.10			0.043	
A1	0.00		0.10	0.00		0.004	
A2	0.7	0.9	1.00	0.028	0.035	0.039	
b	0.15		0.30	0.006		0.012	
¢	0.08		0.22	0.003		0.009	
D	1.85	2,10	2.15	0.073	0.083	0.085	
E	1.80	2.30	2,40	0.071	0.091	0.094	
e		0.65 BSC			0.026 BSC		
el	1.30 BSC				0.051 BSC		
E1	1.1	1.30	1.4	0.043	0.051	0.055	
L	0.26	0.36	0.46	0.010	0.014	0.018	
θ	0°	4°	8°	0°	4°	8°	

UNIT: mm

NOTE

1. ALL DIMENSIONS ARE IN MILLMETERS.

2. DIMENSIONS ARE INCLUSIVE OF PLATING.

3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.

MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 3 MILS EACH.

4. DIE IS FACING UP FOR MOLD AND FACING DOWN FOR TRIM/FORM.

ie:REVERSE TRIM/FORM.

5. DIMENSION L IS MEASURED IN GAUGE PLANE,

6. CONTROLLING DIMENSION IS MILLIMETER.

CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

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