## UP03396

## Silicon NPN epitaxial planar type (Tr1) Silicon PNP epitaxial planar type (Tr2)

For digital circuits

## Features

- Two elements incorporated into one package (Transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half

Basic Part Number

- UNR111T + UNR1211

Absolute Maximum Ratings $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$

| Parameter |  | Symbol | Rating | Unit |
| :---: | :--- | :---: | :---: | :---: |
| $\operatorname{Tr} 1$ | Collector-base voltage <br> (Emitter open) | $\mathrm{V}_{\text {CBO }}$ | 50 | V |
|  | Collector-emitter voltage <br> (Base open) | $\mathrm{V}_{\text {CEO }}$ | 50 | V |
|  | Collector current | $\mathrm{I}_{\mathrm{C}}$ | 100 | mA |
|  | Collector-base voltage <br> (Emitter open) | $\mathrm{V}_{\text {CBO }}$ | -50 | V |
|  | Collector-emitter voltage <br> (Base open) | $\mathrm{V}_{\text {CEO }}$ | -50 | V |
|  | Collector current power dissipation | $\mathrm{I}_{\mathrm{C}}$ | -100 | mA |
|  | Junction temperature | $\mathrm{T}_{\mathrm{j}}$ | 125 | mW |
|  | Storage temperature | $\mathrm{T}_{\text {stg }}$ | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |



Marking Symbol: 6P
Internal Connection


Electrical Characteristics $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C} \pm 3^{\circ} \mathrm{C}$

- Tr1

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
| :--- | :---: | :--- | :---: | :---: | :---: | :---: |
| Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | $\mathrm{I}_{\mathrm{C}}=10 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{E}}=0$ | 50 |  |  | V |
| Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | $\mathrm{I}_{\mathrm{C}}=2 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$ | 50 |  |  | V |
| Collector-base cutoff current (Emitter open) | $\mathrm{I}_{\mathrm{CBO}}$ | $\mathrm{V}_{\mathrm{CB}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=0$ |  |  | 0.1 | $\mu \mathrm{~A}$ |
| Collector-emitter cutoff current (Base open) | $\mathrm{I}_{\mathrm{CEO}}$ | $\mathrm{V}_{\mathrm{CE}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{B}}=0$ |  |  | 0.5 | $\mu \mathrm{~A}$ |
| Emitter-base cutoff current (Collector open) | $\mathrm{I}_{\mathrm{EBO}}$ | $\mathrm{V}_{\mathrm{EB}}=6 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=0$ |  |  | 0.5 | mA |
| Forward current transfer ratio | $\mathrm{h}_{\mathrm{FE}}$ | $\mathrm{V}_{\mathrm{CE}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=5 \mathrm{~mA}$ | 35 |  |  | - |
| Collector-emitter saturation voltage | $\mathrm{V}_{\mathrm{CE}(\text { sat })}$ | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0.3 \mathrm{~mA}$ |  |  | 0.25 | V |
| Output voltage high-level | $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}, \mathrm{~V}_{\mathrm{B}}=0.5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ | 4.9 |  |  | V |
| Output voltage low-level | $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}, \mathrm{~V}_{\mathrm{B}}=2.5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ |  |  | 0.2 | V |
| Input resistance | $\mathrm{R}_{1}$ |  | $-30 \%$ | 10 | $+30 \%$ | $\mathrm{k} \Omega$ |
| Resistance ratio | $\mathrm{R}_{1} / \mathrm{R}_{2}$ |  | 0.8 | 1.0 | 1.2 | - |
| Transition frequency | $\mathrm{f}_{\mathrm{T}}$ | $\mathrm{V}_{\mathrm{CB}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=-2 \mathrm{~mA}, \mathrm{f}=200 \mathrm{MHz}$ |  | 150 |  | MHz |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

- Tr2

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
| :--- | :---: | :--- | :---: | :---: | :---: | :---: |
| Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | $\mathrm{I}_{\mathrm{C}}=-10 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{E}}=0$ | -50 |  |  | V |
| Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | $\mathrm{I}_{\mathrm{C}}=-2 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$ | -50 |  |  | V |
| Collector-base cutoff current (Emitter open) | $\mathrm{I}_{\mathrm{CBO}}$ | $\mathrm{V}_{\mathrm{CB}}=-50 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=0$ |  |  | -0.1 | $\mu \mathrm{~A}$ |
| Collector-emitter cutoff current (Base open) | $\mathrm{I}_{\mathrm{CEO}}$ | $\mathrm{V}_{\mathrm{CE}}=-50 \mathrm{~V}, \mathrm{I}_{\mathrm{B}}=0$ |  |  | -0.5 | $\mu \mathrm{~A}$ |
| Emitter-base cutoff current (Collector open) | $\mathrm{I}_{\mathrm{EBO}}$ | $\mathrm{V}_{\mathrm{EB}}=-6 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=0$ |  |  | -0.2 | mA |
| Forward current transfer ratio | $\mathrm{h}_{\mathrm{FE}}$ | $\mathrm{V}_{\mathrm{CE}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=-5 \mathrm{~mA}$ | 80 |  | 400 | - |
| Collector-emitter saturation voltage | $\mathrm{V}_{\mathrm{CE}(\text { sat })}$ | $\mathrm{I}_{\mathrm{C}}=-10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=-0.3 \mathrm{~mA}$ |  |  | -0.25 | V |
| Output voltage high-level | $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{V}_{\mathrm{CC}}=-5 \mathrm{~V}, \mathrm{~V}_{\mathrm{B}}=-0.5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ | -4.9 |  |  | V |
| Output voltage low-level | $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{V}_{\mathrm{CC}}=-5 \mathrm{~V}, \mathrm{~V}_{\mathrm{B}}=-2.5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ |  |  | -0.2 | V |
| Input resistance | $\mathrm{R}_{1}$ |  | $-30 \%$ | 22 | $+30 \%$ | $\mathrm{k} \Omega$ |
| Resistance ratio | $\mathrm{R}_{1} / \mathrm{R}_{2}$ |  |  | 0.47 |  | - |
| Transition frequency | $\mathrm{f}_{\mathrm{T}}$ | $\mathrm{V}_{\mathrm{CB}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=1 \mathrm{~mA}, \mathrm{f}=200 \mathrm{MHz}$ |  | 80 |  | MHz |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.
Common characteristics chart


## Characteristics charts of Tr1








## Characteristics charts of Tr2








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