

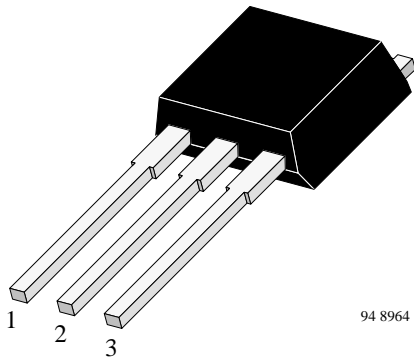
Silicon NPN High Voltage Switching Transistor

Features

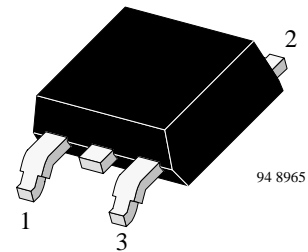
- Monolithic integrated C-E-free-wheel diode
- Simple-sWitch-Off Transistor (SWOT)
- HIGH SPEED technology
- Planar passivation
- 100 kHz switching rate
- Very low switching losses
- Very low dynamic saturation
- Very low operating temperature
- Optimized RBSOA
- High reverse voltage

Applications

Electronic lamp ballast circuits



BUD700D
1 Base 2 Collector 3 Emitter



BUD700D -SMD
1 Base 2 Collector 3 Emitter

Absolute Maximum Ratings

$T_{case} = 25^{\circ}C$, unless otherwise specified

Parameter	Test Conditions	Symbol	Value	Unit
Collector-emitter voltage		V_{CEO}	400	V
		V_{CEW}	500	V
		V_{CES}	700	V
Emitter-base voltage		V_{EBO}	11	V
Collector current		I_C	2	A
Collector peak current		I_{CM}	3	A
Base current		I_B	0.75	A
Base peak current		I_{BM}	1	A
Total power dissipation	$T_{case} \leq 50^{\circ}C$	P_{tot}	20	W
Junction temperature		T_j	150	$^{\circ}C$
Storage temperature range		T_{stg}	-65 to +150	$^{\circ}C$

Maximum Thermal Resistance

$T_{case} = 25^{\circ}C$, unless otherwise specified

Parameter	Test Conditions	Symbol	Value	Unit
Junction case		R_{thJC}	5	K/W

Electrical Characteristics

$T_{case} = 25^{\circ}C$, unless otherwise specified

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
Transistor						
Collector cut-off current	$V_{CE} = 700 V$	I_{CES}			50	μA
	$V_{CE} = 700 V; T_{case} = 150^{\circ}C$	I_{CES}			0.5	mA
Collector-emitter breakdown voltage (figure 1)	$I_C = 300 mA; L = 125 mH;$ $I_{measure} = 100 mA$	$V_{(BR)CEO}$	400			V
Emitter-base breakdown voltage	$I_E = 1 mA$	$V_{(BR)EBO}$	11			V
Collector-emitter saturation voltage	$I_C = 0.3 A; I_B = 0.1 A$	V_{CEsat}		0.1	0.2	V
Base-emitter saturation voltage	$I_C = 0.3 A; I_B = 0.1 A$	V_{BEsat}		0.9	1	V
DC forward current transfer ratio	$V_{CE} = 2 V; I_C = 10 mA$	h_{FE}	10			
	$V_{CE} = 2 V; I_C = 0.3 A$	h_{FE}	10			
	$V_{CE} = 5 V; I_C = 2 A$	h_{FE}	4	6		
Collector-emitter working voltage	$V_S = 50 V; L = 1 mH; I_C = 2 A;$ $I_{B1} = 0.7 A; -I_{B2} = 0.2 A;$ $-V_{BB} = 5 V$	V_{CEW}	500			V
Dynamic saturation voltage	$I_C = 1 A; I_B = 0.2 A; t = 1 \mu s$	$V_{CEsatdyn}$			15	V
	$I_C = 1 A; I_B = 0.2 A; t = 3 \mu s$	$V_{CEsatdyn}$			4	V
Gain bandwidth product	$I_C = 200 mA; V_{CE} = 10 V;$ $f = 1 MHz$	f_T	4			MHz
Free-wheel diode						
Forward voltage	$I_F = 0.7 A$	V_F			1.2	V

Switching Characteristics

$T_{case} = 25^{\circ}C$, unless otherwise specified

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
Application specific switching time						
measured with Nylos3		t_x			0.75	μs
Resistive load (figure 2)						
Turn on time	$I_C = 330 mA; I_{B1} = 85 mA;$ $-I_{B2} = 170 mA; V_S = 250 V$	t_{on}			0.25	μs
Storage time		t_s			3	μs
Fall time		t_f			0.4	μs

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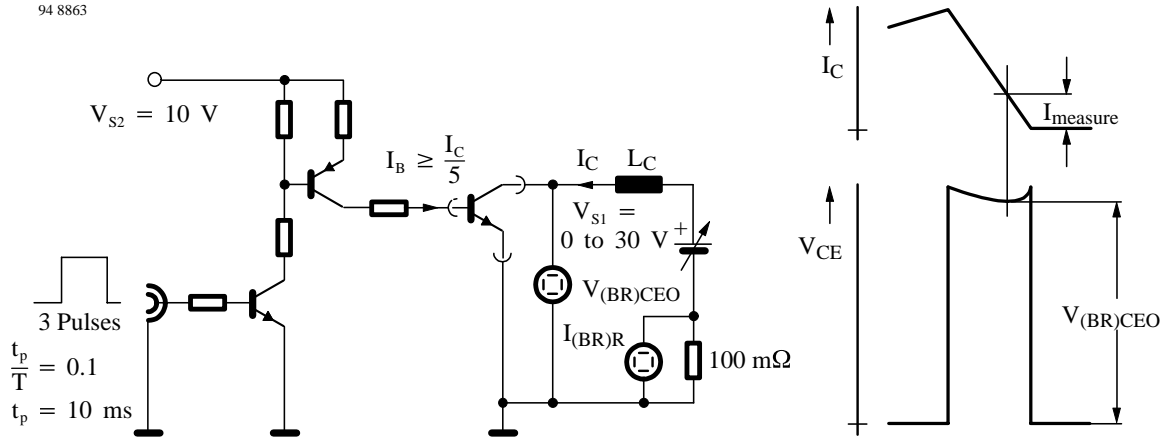
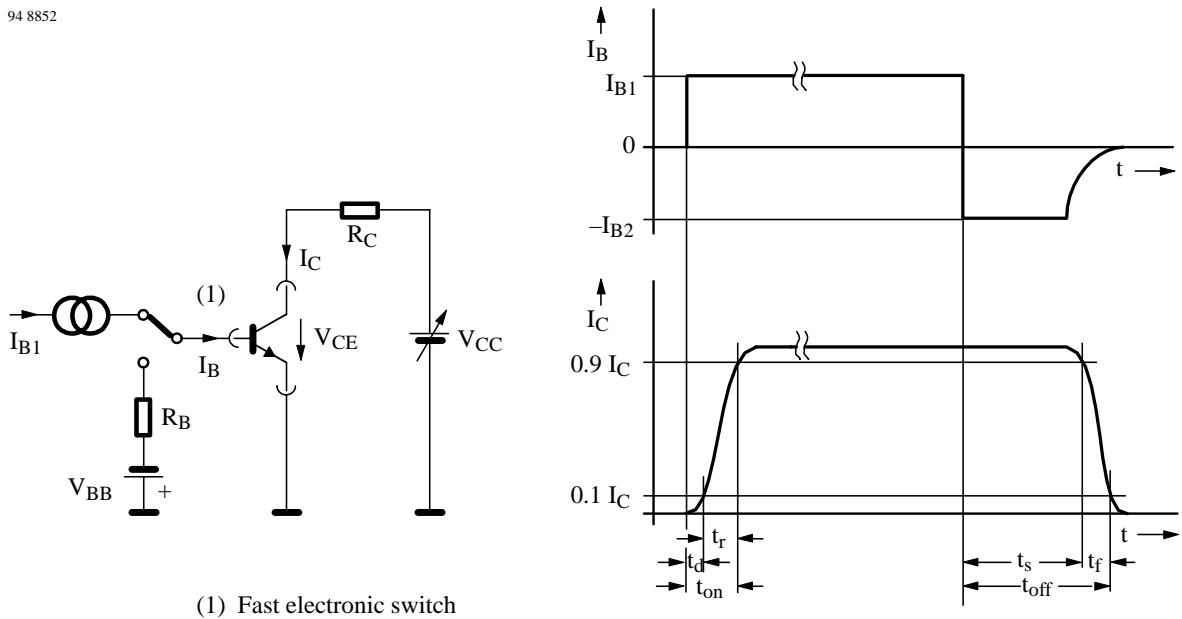


Figure 1. Test circuit for $V_{(BR)CEO}$

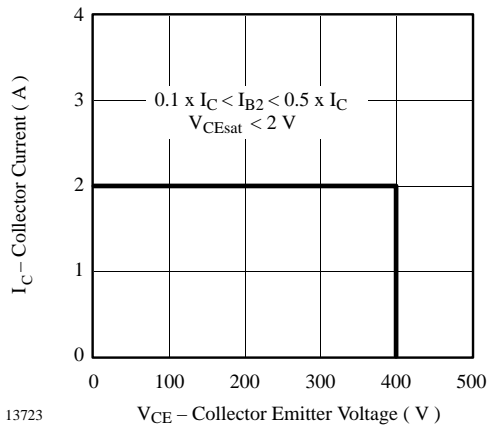
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(1) Fast electronic switch

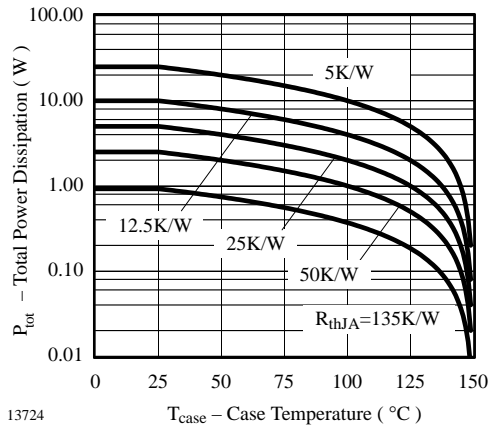
Figure 2. Test circuit for switching characteristics – resistive load

Typical Characteristics ($T_{case} = 25_C$ unless otherwise specified)



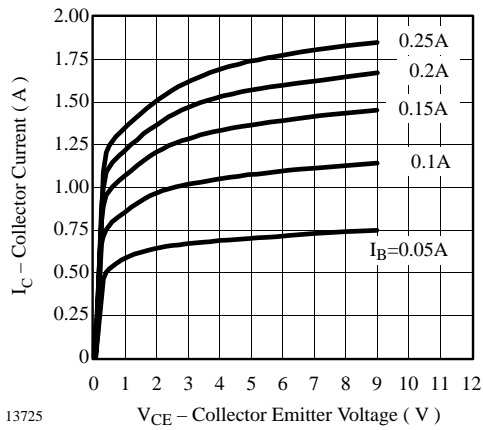
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Figure 3. V_{CEW} - Diagram



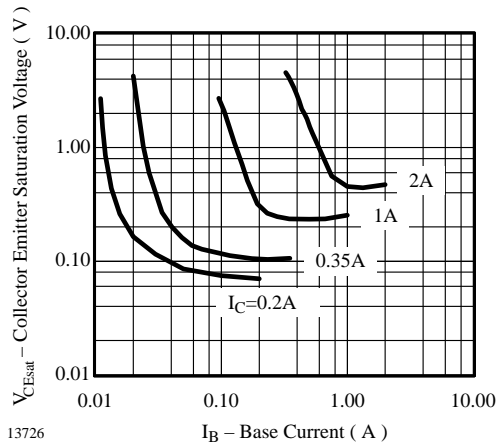
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Figure 6. P_{tot} vs. T_{case}



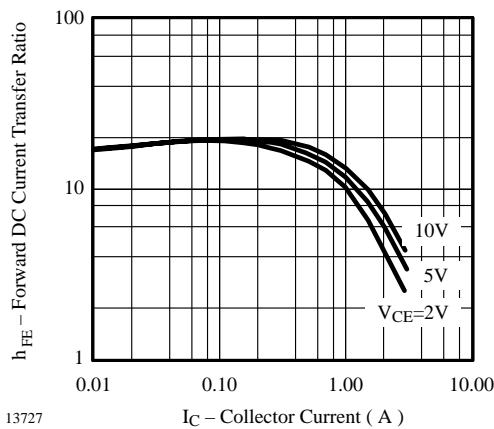
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Figure 4. I_C vs. V_{CE}



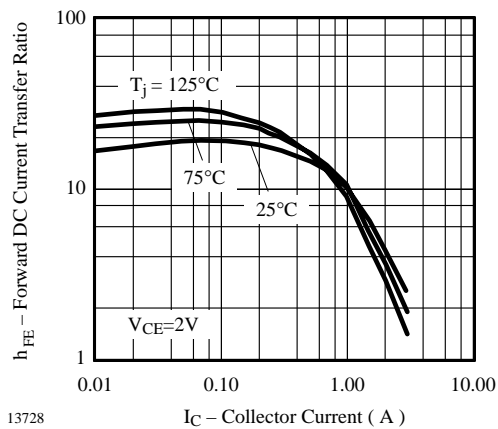
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Figure 7. V_{CEsat} vs. I_B



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Figure 5. h_{FE} vs. I_C



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Figure 8. h_{FE} vs. I_C

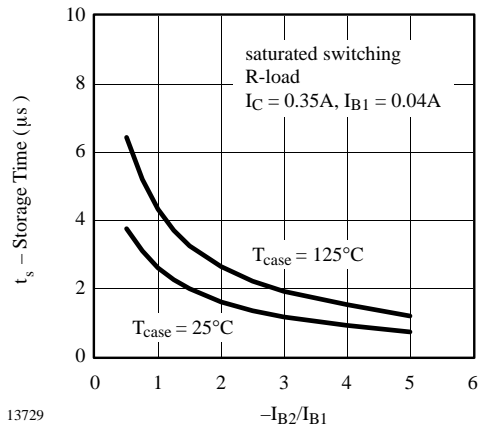


Figure 9. t_s vs. $-I_{B2}/I_{B1}$

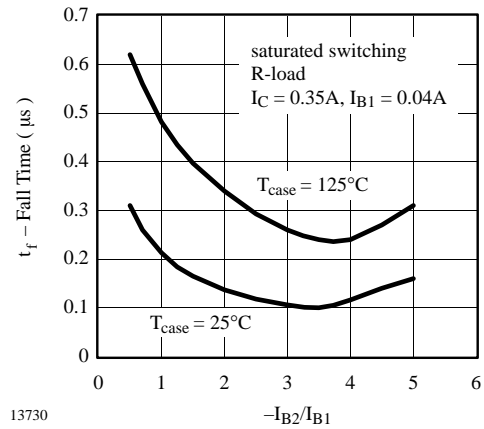


Figure 12. t_f vs. $-I_{B2}/I_{B1}$

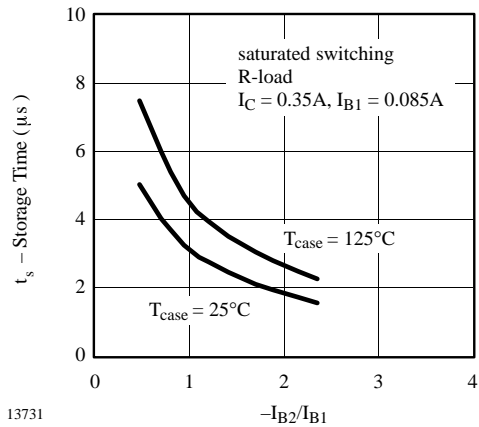


Figure 10. t_s vs. $-I_{B2}/I_{B1}$

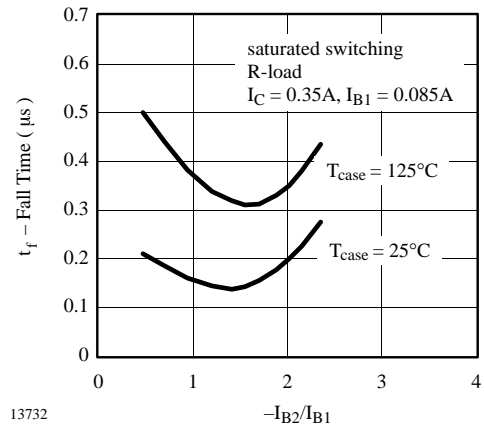


Figure 13. t_f vs. $-I_{B2}/I_{B1}$

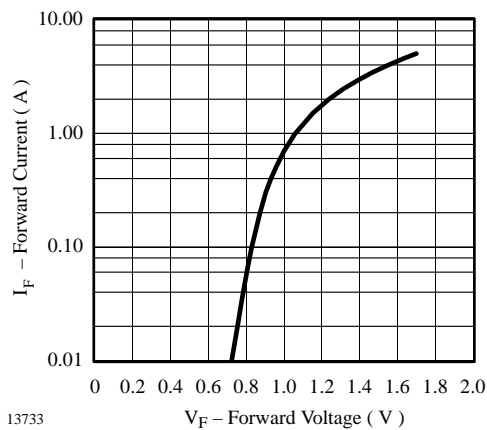
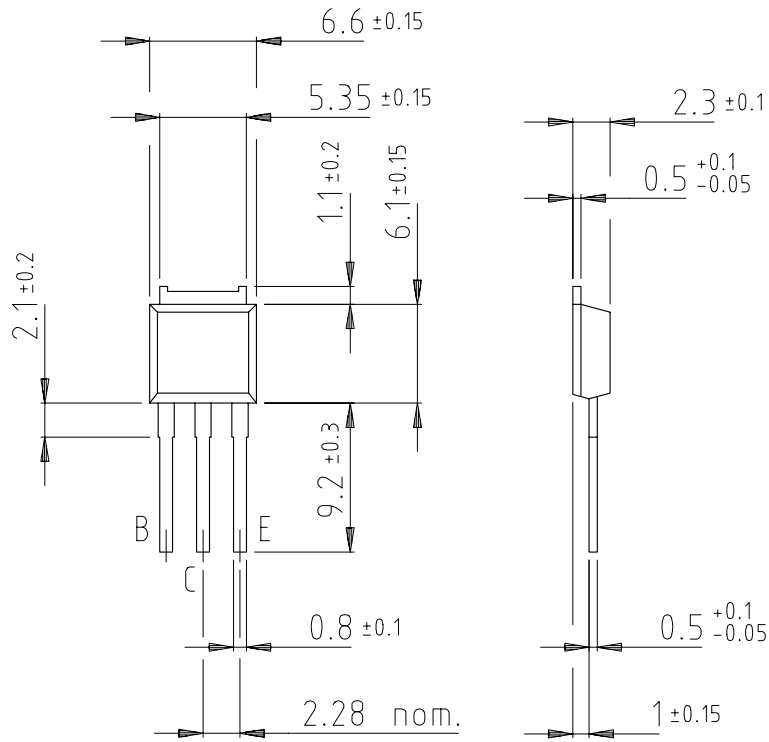
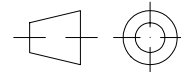


Figure 11. I_F vs. V_F

Dimensions in mm

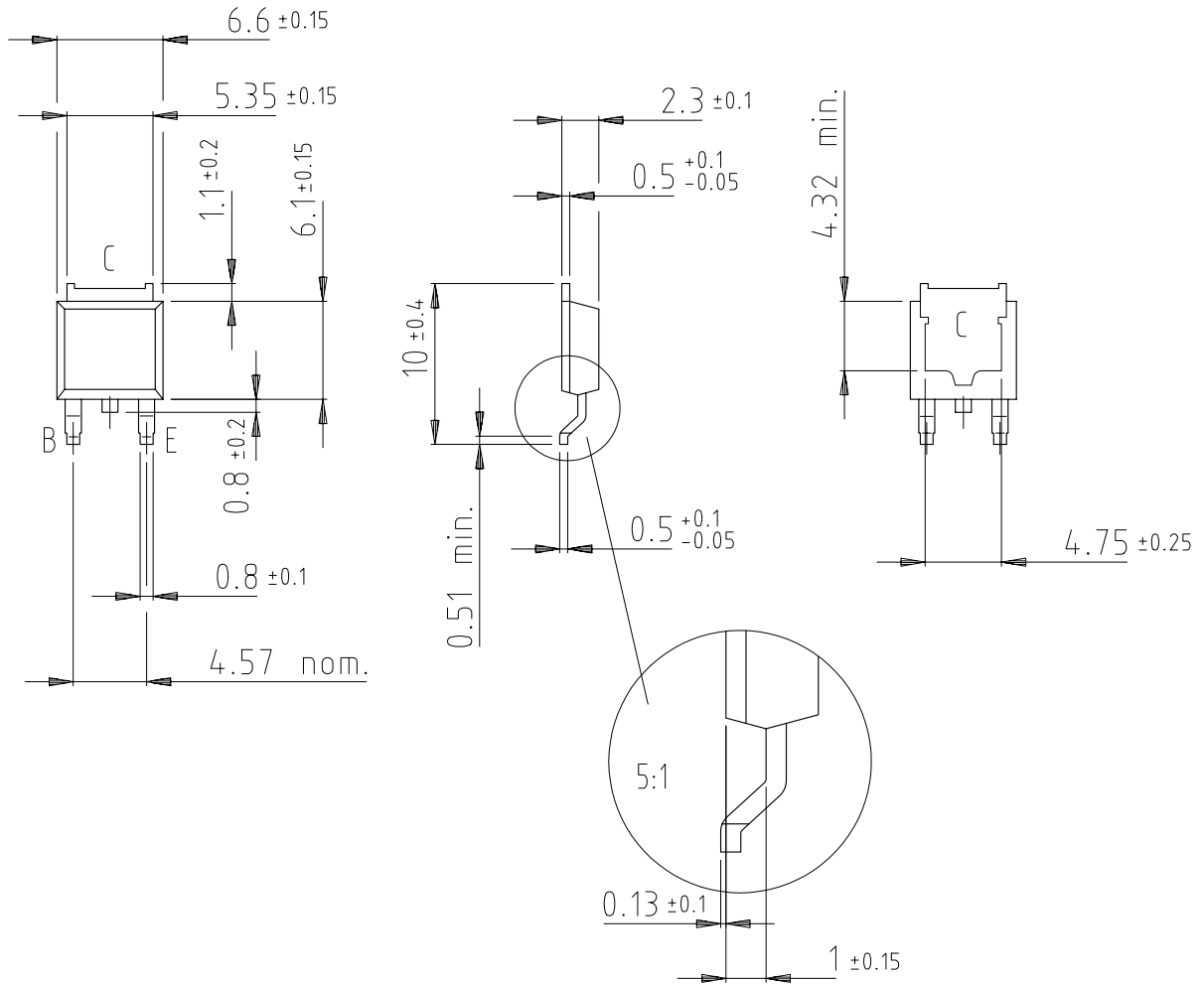


Plastic case JEDEC TO 251
Collector connected with metallic surface

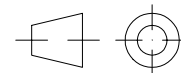


technical drawings
according to DIN
specifications

14292



Plastic case JEDEC TO 252
Collector connected with metallic surface



technical drawings
according to DIN
specifications

14293

For ordering TO 252 add SMD to the type number (i.e. BUD700D –SMD)



Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay-Telefunken products for any unintended or unauthorized application, the buyer shall indemnify Vishay-Telefunken against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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