## FEATURES

Output voltage: $3.3 \mathrm{~V}, 5 \mathrm{~V}, 12 \mathrm{~V}$ and adjustable output version

- Adjustable version output voltage range, 1.23 V to $18 \mathrm{~V}+4 \%$
- $150 \mathrm{KHz}+15 \%$ fixed switching frequency
- Voltage mode non-synchronous PWM control
- Thermal-shutdown and current-limit protection
- ON/OFF shutdown control input
- Operating voltage can be up to 26 V
- Output load current: 2A
- SOP-8L packages
- Low power standby mode
- Built-in switching transistor on chip
- Moisture Sensitivity Level 3


## APPLICATIONS

- Simple High-efficiency step-down regulator
- On-card switching regulators
- Positive to negative converter
- Negative Step-Up Converters
- Power Supply for Battery Chargers


## DESCRIPTION

The TJ1509A series are monolithic IC designed for a step-down DC/DC converter, and own the ability of driving a 2 A load without additional transistor component. Due to reducing the number of external component, the board space can be saved easily. The external shutdown function can be controlled by logic level and then come into standby mode.
The internal compensation makes feedback control have good line and load regulation without external design. Regarding protected function, thermal shutdown is to prevent over temperature operating from damage, and current limit is against over current operating of the output switch. If current limit function occurred and VFB is down to 0.5 V below, the switching frequency will be reduced.
The TJ1509A series operates at a switching frequency of 150 KHz thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Other features include a guaranteed $+4 \%$ tolerance on output voltage under specified input voltage and output load conditions, and $+15 \%$ on the oscillator frequency.
The output version included fixed $3.3 \mathrm{~V}, 5 \mathrm{~V}, 12 \mathrm{~V}$, and an adjustable type.
The packages are available in a standard 8-lead SOP-8 package.

Typical Application Circuit
(1) Fixed Type Circuit

(2) Adjustable Type Circuit


$$
\text { Vout }=\mathrm{V}_{\mathrm{FB}} \times\left(1+\frac{R 1}{R 2}\right)
$$

$$
V_{F B}=1.23 \mathrm{~V}
$$

$$
\mathrm{R} 2=1 \mathrm{~K} \sim 3 \mathrm{~K}
$$

(3) Delay Start Circuit


## BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

| SYMBOL | PARAMETER | RATING | UNIT |
| :---: | :--- | :---: | :---: |
| $V_{C C}$ | Supply Voltage | 30 | V |
| $V_{S D}$ | SD Pin Input Voltage | -0.3 to +18 | V |
| $\mathrm{~V}_{F B}$ | Feedback Pin Voltage | -0.3 to +18 | V |
| $V_{O U T}$ | Output Voltage to Ground | -1 | V |
| $P_{D}$ | Power Dissipation | Internally limited | W |
| $\mathrm{T}_{S T}$ | Storage Temperature | -65 to +150 | C |
| $\mathrm{T}_{O P}$ | Operating Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{V}_{O P}$ | Operating Voltage | +4.5 to +22 | V |

ELECTRICAL CHARACTERISTICS (All Output Voltage Versions)
Unless otherwise specified, $\mathrm{V}_{\mathbb{N}}=12 \mathrm{~V}$ for $3.3 \mathrm{~V}, 5 \mathrm{~V}$, adjustable version and $\mathrm{V}_{\mathbb{N}}=18 \mathrm{~V}$ for the 12 V version. $\mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$

| SYMBOL | PARAMETER |  | CONDITIONS |  | MIN. | TYP. | MAX | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{l}_{\text {Fb }}$ | Feedback Bias Current |  | $\mathrm{V}_{\mathrm{FB}}=1.3 \mathrm{~V}$ <br> (Adjustable version only) |  |  | -10 | $\begin{gathered} \hline-50 \\ \hline-100 \end{gathered}$ | nA |
| Fosc | Oscillator Frequency |  |  |  | 127 | 150 | 173 |  |
|  |  |  |  |  | 110 |  | 173 |  |
| $\mathrm{F}_{\text {SCP }}$ | Oscillator Frequency of Short Circuit Protect |  | When current limit occurred and $\mathrm{V}_{\mathrm{FB}}<0.5 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}$ |  | 10 | 30 | 50 | KHz |
| $V_{\text {SAT }}$ | Saturation Voltage |  | $\text { lout }=2 \mathrm{~A}$ <br> No outside circuit $\mathrm{V}_{\text {FB }}=\mathrm{OV}$ force driver on |  |  |  | 1.4 |  |
|  |  |  |  | 1.25 | 1.5 | V |
| DC | Max. Duty Cycle (ON) |  |  |  | $\mathrm{V}_{\mathrm{FB}}=0 \mathrm{~V}$ force driver on |  |  | 100 |  | \% |
|  | Min. Duty Cycle (OFF) |  | $\mathrm{V}_{\mathrm{FB}}=12 \mathrm{~V}$ force driver off |  |  | 0 |  | \% |
| $\mathrm{I}_{\mathrm{CL}}$ | Current Limit |  | Peak current No outside circuit $\mathrm{V}_{\mathrm{FB}}=0$ force driver on |  | 3 |  |  | A |
| I L | Output $=0 \mathrm{~V}$ | Output Leakag | No outside circuit $V_{F B}=12$ force driver off |  |  |  | -200 | $\mu \mathrm{A}$ |
|  | Output $=-1 \mathrm{~V}$ | ${ }^{\mathrm{e}} \mathrm{Current}$ | $\mathrm{V}_{\mathbb{1}}=22 \mathrm{~V}$ |  |  | -5 |  | mA |
| $\mathrm{I}_{Q}$ | Quiescent Current |  | $\mathrm{V}_{\mathrm{FB}}=12$ force driver off |  |  | 5 | 10 | mA |
| $I_{\text {Stay }}$ | Standby Quiescent Current |  | $\begin{aligned} & \text { SD pin }=5 \mathrm{~V} \\ & \mathrm{~V}_{\mathbb{N}}=22 \mathrm{~V} \end{aligned}$ |  |  | 70 | 150 | $\mu \mathrm{A}$ |
| $\mathrm{V}_{\text {IL }}$ | SD Pin Logic Input Threshold Voltage |  | Low (regulator ON) |  | - | 1.3 | 0.6 | V |
| $\mathrm{V}_{1 \mathrm{H}}$ |  |  | High (regulator OFF) |  | 2.0 |  | - | $\checkmark$ |
| $\mathrm{I}_{\mathrm{H}}$ | SD Pin Logic Input Current |  | $\mathrm{V}_{\text {LOGIC }}=2.5 \mathrm{~V}$ (OFF) |  |  |  | -0.01 | $\mu \mathrm{A}$ |
| L | SD Pin Input Current |  | $\mathrm{V}_{\text {LOGIC }}=0.5 \mathrm{~V}$ (ON) |  |  | -0.1 | -1 | $\mu \mathrm{A}$ |
| $\theta_{\mathrm{Jc}}$ | Thermal Resistance |  | SOP-8L | Junction to case |  | 15 |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\theta_{\text {JA }}$ | Thermal Resistance With copper area of approximately 3 in $^{2}$ |  | SOP-8L | Junction to ambient |  | 70 |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Specifications with boldface type are for full operating temperature range, the other type are for $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$.
ELECTRICAL CHARACTERISTICS (Continued)

| - | SYMBOL | PARAMETER | CONDITIONS | TYP. | LIMIT | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { TJ1509A } \\ & \text {-ADJ } \end{aligned}$ | $V_{F B}$ | Output Feedback | $\begin{gathered} 4.5 \mathrm{~V} \leq \mathrm{V}_{\mathbb{I}} \leq 22 \mathrm{~V} \\ 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 2 \mathrm{~A} \\ \mathrm{~V}_{\text {OUT }} \text { programmed for } 3 \mathrm{~V} \end{gathered}$ | 1.23 | $\begin{aligned} & 1.193 / 1.18 \\ & 1.267 / 1.28 \end{aligned}$ | $\begin{gathered} V \\ V_{\text {MIN }} \\ V_{\text {MAX }} \end{gathered}$ |
|  | $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=2 \mathrm{~A}$ | 76 |  | \% |
| $\begin{gathered} \text { TJ1509A } \\ -3.3 \mathrm{~V} \end{gathered}$ | Vout | Output voltage | $\begin{gathered} 4.75 \mathrm{~V} \leq \mathrm{V}_{\mathrm{IN}} \leq 22 \mathrm{~V} \\ 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 2 \mathrm{~A} \end{gathered}$ | 3.3 | $\begin{aligned} & 3.168 / 3.135 \\ & 3.432 / 3.465 \end{aligned}$ | $\begin{gathered} V \\ V_{\text {MIN }} \\ V_{\text {MAX }} \end{gathered}$ |
|  | $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=2 \mathrm{~A}$ | 78 |  | \% |
| $\begin{aligned} & \text { TJ1509A } \\ & -5.0 \mathrm{~V} \end{aligned}$ | $V_{\text {OUT }}$ | Output voltage | $\begin{gathered} 7 V \leq V_{\text {IN }} \leq 22 V \\ 0.2 A \leq I_{\text {LOAD }} \leq 2 A \end{gathered}$ | 5 | $\begin{aligned} & 4.8 / 4.75 \\ & 5.2 / 5.25 \end{aligned}$ | $\begin{gathered} \mathrm{V} \\ V_{\text {MIN }} \\ V_{\text {MAX }} \end{gathered}$ |
|  | $\eta$ | Efficiency | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=2 \mathrm{~A}$ | 83 |  | \% |
| $\begin{aligned} & \text { TJ1509A } \\ & -12 \mathrm{~V} \end{aligned}$ | $V_{\text {OUT }}$ | Output voltage | $\begin{gathered} 15 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 22 \mathrm{~V} \\ 0.2 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 2 \mathrm{~A} \end{gathered}$ | 12 | $\begin{aligned} & 11.52 / 11.4 \\ & 12.48 / 12.6 \end{aligned}$ | $\begin{gathered} V \\ V_{\text {MIN }} \\ V_{\text {MAX }} \end{gathered}$ |
|  | $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=15 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=2 \mathrm{~A}$ | 90 |  | \% |

Specifications with boldface type are for full operating temperature range, the other type are for $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$.

## TYPICAL PERFORMANCE CHARACTERISTICS



TJ1509A Output Voltage vs. Output Current (Vin=15V, Vout=5V, lo=2A)


TJ1509A Input Current vs. Input Voltage
(lo=0A)


TJ1509A Efficiency vs. Output Current (Vin=12V, Vout=5V, lo=2A)


TJ1509A Efficiency vs. Output Current (Vin=15V, Vout=5V, lo=2A)


TJ1509A Feedback Voltage vs. Temperature (Vin=12V, Vout=5V)


## TYPICAL PERFORMANCE CHARACTERISTICS

TJ1509A Efficiency vs. Temperature
(Vin=12V, Vout=5V, lo=2A)


TJ1509A Saturation Voltage vs. Temperature
( $\mathrm{Vcc}=12 \mathrm{~V}, \mathrm{Vfb}=0 \mathrm{~V}, \mathrm{VSD}=0$ )


TJ1509A Efficiency vs. Temperature
(Vin=12V, Vout=3.3V, Io=2A)


TJ1509A Switch Current Limit vs. Temperature
( $\mathrm{V} \mathrm{cc}=12 \mathrm{~V}, \mathrm{~V}_{\mathrm{fb}}=0 \mathrm{~V}$ )


TJ1509A Supply Current vs. Temperature (Vcc=12V, No Load, Von/off=0V(Switch ON), Von/off=5V(Switch OFF))


## TYPICAL PERFORMANCE CHARACTERISTICS

TJ1509A Threshold Voltage vs. Temperature
(Vcc=12V, $\mathrm{I}_{\mathrm{o}}=100 \mathrm{~mA}$ )


TJ1509A Frequency vs. Temperature (Vcc=12V, lo=500mA, Vout=5V)


TJ1509A Output Voltage vs. Temperature (Vin=12V, lo=2A)


TJ1509A ON/OFF Current vs. ON/OFF Voltage (Vin=12V)


TJ1509A Feedback Current vs. Temperature (Vcc=12V, Vout=5V, Vfb=1.3V)


## FUNCTION DESCRIPTION

## Pin Functions <br> +VIN

This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be present at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

## Ground

Circuit ground.

## Output

Internal switch. The voltage at this pin switches between (+VIN - VSAT) and approximately -0.5 V , with a duty cycle of approximately VOUT / VIN. To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be kept a minimum.

## Feedback(FB)

Senses the regulated output voltage to complete the feedback loop.

## ON/OFF (SD)

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 150 uA . Pulling this pin below a threshold voltage of approximately 1.3 V turns the regulator on, and pulling this pin above 1.3 V (up to a maximum of 18 V ) shuts the regulator down. If this shutdown feature is not needed, the SD pin can be wired to the ground pin or it can be left open, in either case the regulator will be in the ON condition.

## Thermal Considerations

The SOP-8 package needs a heat sink under most conditions. The size of the heatsink depends on the input voltage, the output voltage, the load current and the ambient temperature. The TJ1509A junction temperature rises above ambient temperature for a 3A load and different input and output voltages. The data for these curves was taken with the TJ1509A (SOP-8 package) operating as a buck-switching regulator in an ambient temperature of $25^{\circ} \mathrm{C}$ (still air). These temperature rise numbers are all approximate and there are many factors that can affect these temperatures. Higher ambient temperatures require more heat sinking.

For the best thermal performance, wide copper traces and generous amounts of printed circuit board copper should be used in the board layout. [Once exception to this is the output (switch) pin, which should not have large areas of copper.] Large areas of copper provide the best transfer of heat (lower thermal resistance) to the surrounding air, and moving air lowers the thermal resistance even further.

Package thermal resistance and junction temperature rise numbers are all approximate, and there are many factors that will affect these numbers. Some of these factors include board size, shape, thickness, position, location, and even board temperature. Other factors are, trace width, total printed circuit copper area, copper thickness, single or double-sided, multi-layer board and the amount of solder on the board.

The effectiveness of the PC board to dissipate heat also depends on the size, quantity and spacing of other components on the board, as well as whether the surrounding air is still or moving. Furthermore, some of these components such as the catch diode will add heat to the PC board and the heat can vary as the input voltage changes. For the inductor, depending on the physical size, type of core material and the DC resistance, it could either act as a heat sink taking heat away from the board, or it could add heat to the board.

