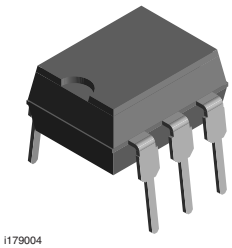
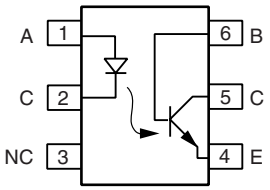
Optocoupler, Phototransistor Output, with Base Connection





FEATURES

- Isolation test voltage (1.0 s), 5300 V_{RMS}
- V_{CEsat} = 0.25 (≤ 0.4) V, I_F = 10 mA, I_C = 2.5 mA
- Built to conform to VDE requirements
- High quality premium device
- Long term stability
- Storage temperature, - 55 °C to + 150 °C
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

Pb-free

e3

RoHS COMPLIANT

DESCRIPTION

The SFH600 is an optocoupler with a GaAs LED emitter which is optically coupled with a silicon planar phototransistor detector. The component is packaged in a plastic plug-in case, 20 AB DIN 41866.
The coupler transmits signals between two electrically isolated circuits. The potential difference between the circuits to be coupled should not exceed the maximum permissible insulating voltage.

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending available with option 1
- CSA 93751
- BSI IEC 60950; IEC 60065

ORDER INFORMATION

PART	REMARKS
SFH600-0	CTR 40 to 80 %, DIP-6
SFH600-1	CTR 63 to 125 %, DIP-6
SFH600-2	CTR 100 to 200 %, DIP-6
SFH600-3	CTR 160 to 320 %, DIP-6
SFH600-0X007	CTR 40 to 80 %, SMD-6 (option 7)
SFH600-1X007	CTR 63 to 125 %, SMD-6 (option 7)
SFH600-1X009	CTR 63 to 125 %, SMD-6 (option 9)
SFH600-2X006	CTR 100 to 200 %, DIP-6 400 mil (option 6)
SFH600-2X007	CTR 100 to 200 %, SMD-6 (option 7)
SFH600-3X006	CTR 160 to 320 %, DIP-6 400 mil (option 6)
SFH600-3X007	CTR 160 to 320 %, SMD-6 (option 7)

Note
For additional information on the available options refer to option information.

ABSOLUTE MAXIMUM RATINGS (1)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V_R	6.0	V
DC forward current		I_F	60	mA
Surge forward current	$t_P \leq 10$ µs	I_{FSM}	2.5	A
Total power dissipation		P_{diss}	100	mW
OUTPUT				
Collector emitter voltage		V_{CE}	70	V
Emitter base voltage		V_{EB}	7.0	V
Collector current		I_C	50	mA
	t = 1.0 ms	I_C	100	mA
Power dissipation		P_{diss}	150	mW

ABSOLUTE MAXIMUM RATINGS (1)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
COUPLER				
Isolation test voltage between emitter and detector, refer to climate DIN 40046, part 2, Nov. 74	t = 1.0 s	V_{ISO}	5300	V_{RMS}
Creepage distance			≥ 7	mm
Clearance distance			≥ 7	mm
Isolation thickness between emitter and detector			≥ 0.4	mm
Comparative tracking index per DIN IEC 112/VDE 0303, part 1			175	
Isolation resistance	V_{IO} = 500 V, T_{amb} = 25 °C	R_{IO}	≥ 10^{12}	Ω
	V_{IO} = 500 V, T_{amb} = 100 °C	R_{IO}	≥ 10^{11}	Ω
Storage temperature range		T_{stg}	- 55 to + 150	°C
Ambient temperature range		T_{amb}	- 55 to + 100	°C
Junction temperature	max. 10 s, dip soldering	T_j	100	°C
Soldering temperature (2)	max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T_{sld}	260	°C

Notes

(1) T_{amb} = 25 °C, unless otherwise specified.
Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

(2) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I_F = 60 mA		V_F		1.25	1.65	V
Breakdown voltage	I_R = 10 µA		V_{BR}	6.0			V
Reverse current	V_R = 6.0 V		I_R		0.01	10	µA
Capacitance	V_F = 0 V, f = 1 MHz		C_O		25		pF
Thermal resistance			R_{thja}		750		K/W
OUTPUT							
Collector emitter capacitance	f = 1.0 mHz, V_{CE} = 5.0 V		C_{CE}		5.2		pF
Collector base capacitance	f = 1.0 mHz, V_{CB} = 5.0 V		C_{CB}		6.5		pF
Emitter base capacitance	f = 1.0 mHz, V_{EB} = 5.0 V		C_{EB}		9.5		pF
Thermal resistance			R_{thja}		500		K/W
Collector emitter leakage current	V_{CE} = 10 V	SFH600-0	I_{CEO}		2.0	35	nA
		SFH600-1	I_{CEO}		2.0	35	nA
		SFH600-2	I_{CEO}		2.0	35	nA
		SFH600-3	I_{CEO}		5.0	70	nA
COUPLER							
Saturation voltage collector emitter voltage	I_F = 10 mA, I_C = 2.5 mA		V_{CEsat}		0.25	0.4	V
Capacitance (input to output)			C_{IO}			0.6	pF

Note

T_{amb} = 25 °C, unless otherwise specified.
Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	**TEST CONDITION**	**PART**	**SYMBOL**	**MIN**	**TYP.**	**MAX**	**UNIT**
I_C/I_F at V_{CE} = 5.0 V	I_F = 10 mA	SFH600-0	CTR	40		80	%
		SFH600-1	CTR	63		125	%
		SFH600-2	CTR	100		200	%
		SFH600-3	CTR	160		320	%
	I_F = 1.0 mA	SFH600-0	CTR	13	30		%
		SFH600-1	CTR	22	45		%
		SFH600-2	CTR	34	70		%
		SFH600-3	CTR	56	90		%

SWITCHING CHARACTERISTICS							
PARAMETER	**TEST CONDITION**	**PART**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
NON-SATURATED							
Current	V_{CC} = 5.0 V, R_L = 75 Ω		I_F		10		mA
Rise time	V_{CC} = 5.0 V, R_L = 75 Ω		t_r		2.0		µs
Fall time	V_{CC} = 5.0 V, R_L = 75 Ω		t_f		2.5		µs
Turn-on time	V_{CC} = 5.0 V, R_L = 75 Ω		t_{on}		3.2		µs
Turn-off time	V_{CC} = 5.0 V, R_L = 75 Ω		t_{off}		3.0		µs
Cut-off frequency	V_{CC} = 5.0 V, R_L = 75 Ω		F_{CO}		250		kHz
SATURATED							
Current		SFH600-0	I_F		20		mA
		SFH600-1	I_F		10		mA
		SFH600-2	I_F		10		mA
		SFH600-3	I_F		5.0		mA
Rise time		SFH600-0	t_r		2.5		µs
		SFH600-1	t_r		3.0		µs
		SFH600-2	t_r		3.0		µs
		SFH600-3	t_r		4.0		µs
Fall time		SFH600-0	t_f		11		µs
		SFH600-1	t_f		12		µs
		SFH600-2	t_f		12		µs
		SFH600-3	t_f		14		µs
Turn-on time		SFH600-0	t_{on}		3.7		µs
		SFH600-1	t_{on}		4.5		µs
		SFH600-2	t_{on}		4.5		µs
		SFH600-3	t_{on}		5.8		µs
Turn-off time		SFH600-0	t_{off}		19		µs
		SFH600-1	t_{off}		21		µs
		SFH600-2	t_{off}		21		µs
		SFH600-3	t_{off}		24		µs

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

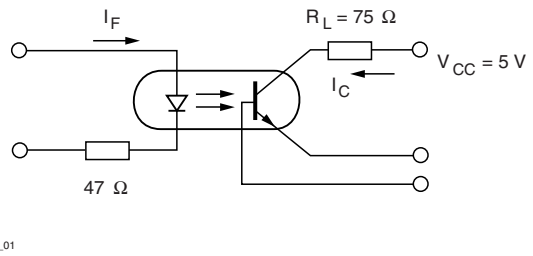


Fig. 1 - Linear Operation (Without Saturation)

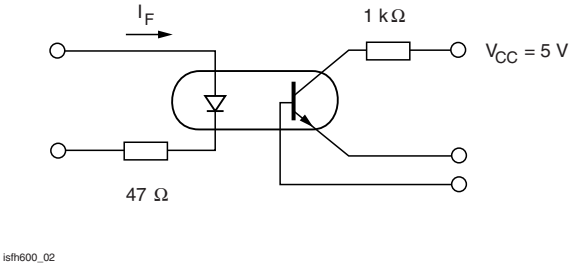


Fig. 2 - Switching Operation (With Saturation)

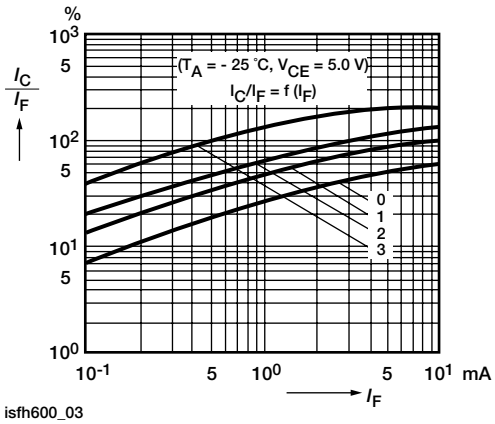


Fig. 3 - Current Transfer Ratio vs. Diode Current

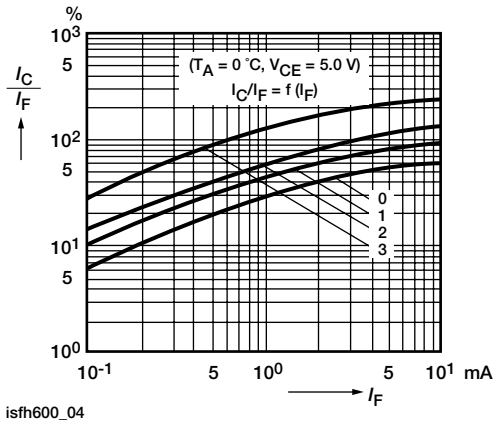


Fig. 4 - Current Transfer Ratio vs. Diode Current

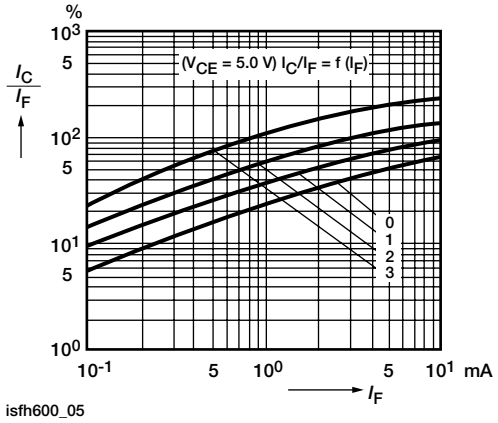


Fig. 5 - Current Transfer Ratio vs. Diode Current

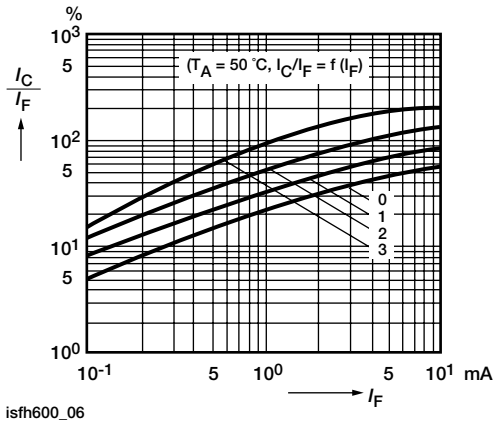


Fig. 6 - Current Transfer Ratio vs. Diode Current

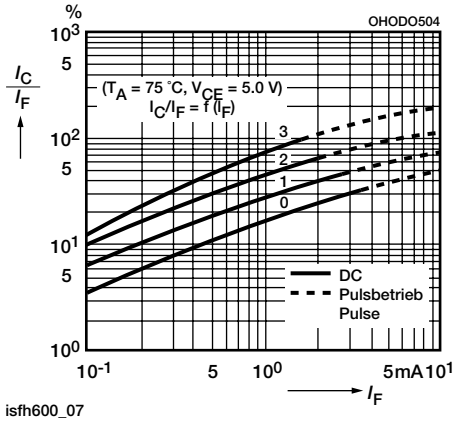


Fig. 7 - Current Transfer Ratio vs. Diode Current

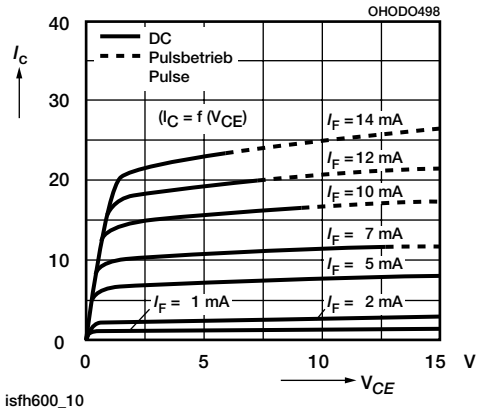


Fig. 10 - Output Characteristics SFH600-2, -3

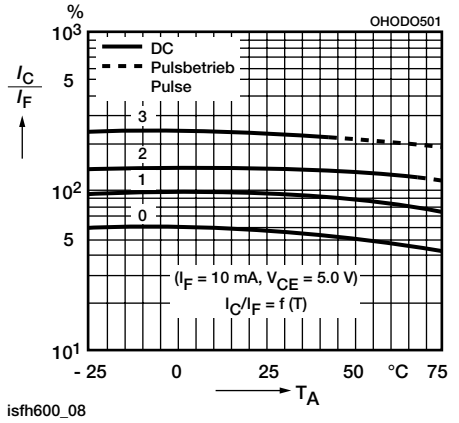


Fig. 8 - Current Transfer Ratio (CTR) vs. Temperature

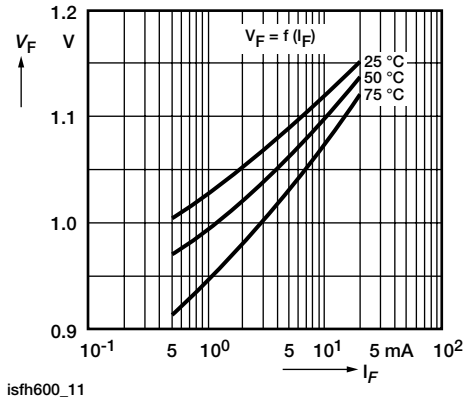


Fig. 11 - Forward Voltage

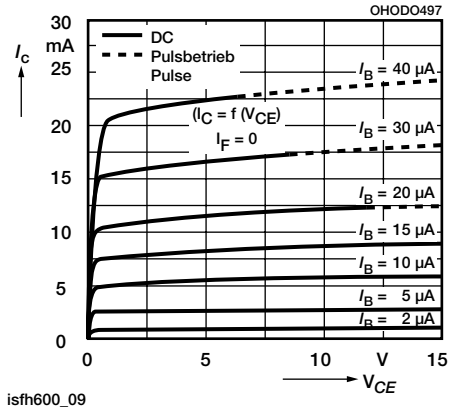


Fig. 9 - Transistor Characteristics SFH600-2, -3

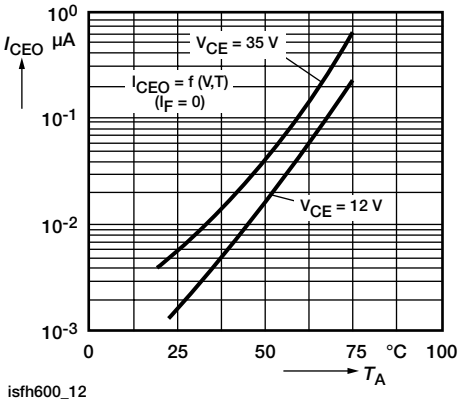


Fig. 12 - Collector Emitter Off-state Current

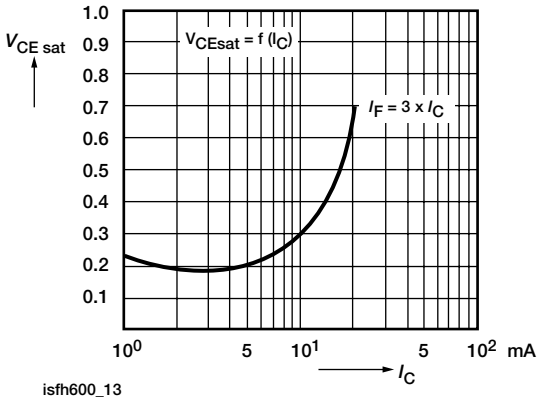


Fig. 13 - Saturation Voltage vs. Collector Current and Modulation Depth SFH600-0

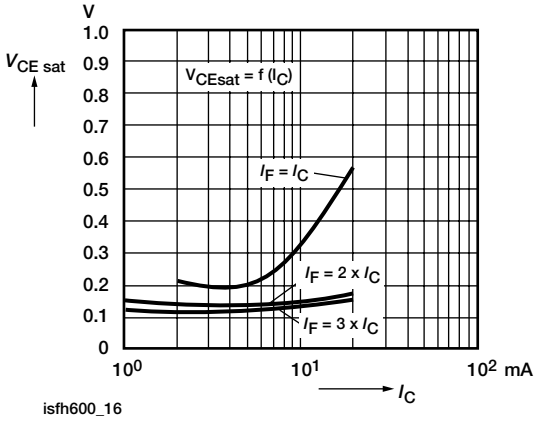


Fig. 16 - Saturation Voltage vs. Collector Current and Modulation Depth SFH600-3

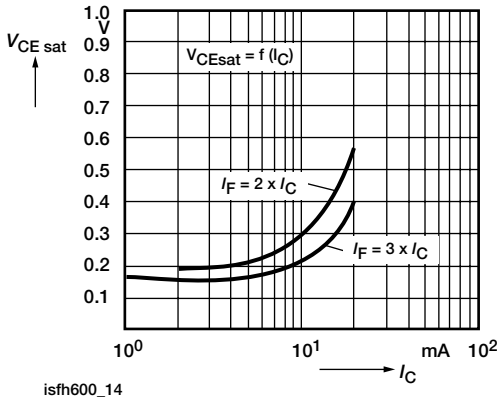


Fig. 14 - Saturation Voltage vs. Collector Current and Modulation Depth SFH600-1

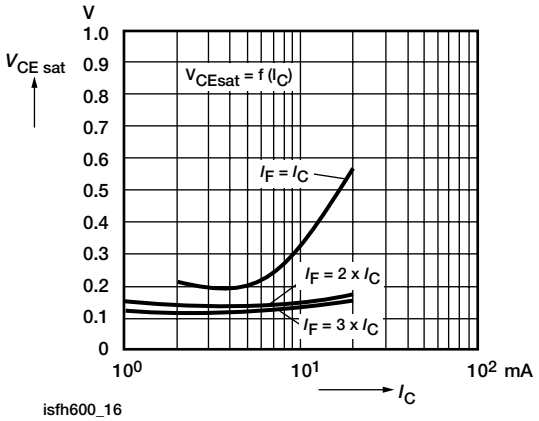


Fig. 17 - Permissible Pulse Load

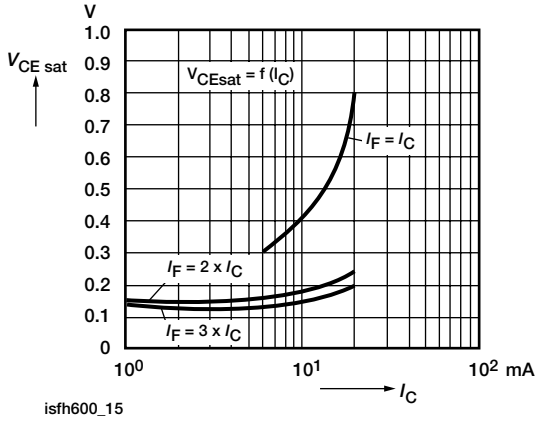


Fig. 15 - Saturation Voltage vs. Collector Current and Modulation Depth SFH600-2

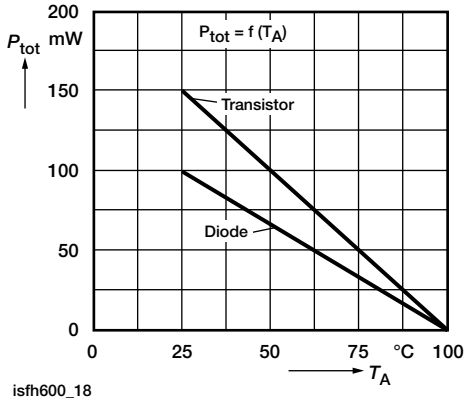


Fig. 18 - Permissible Power Dissipation for Transistor and Diode

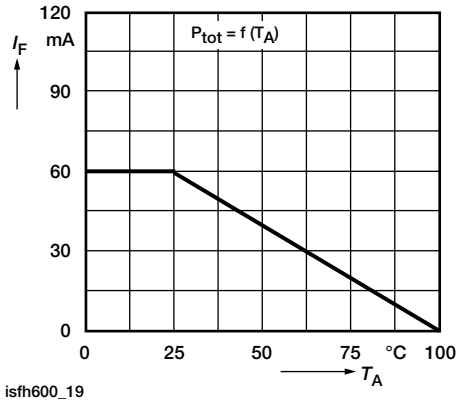


Fig. 19 - Permissible Forward Current Diode

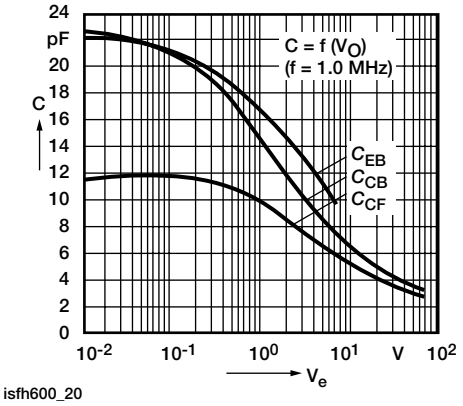
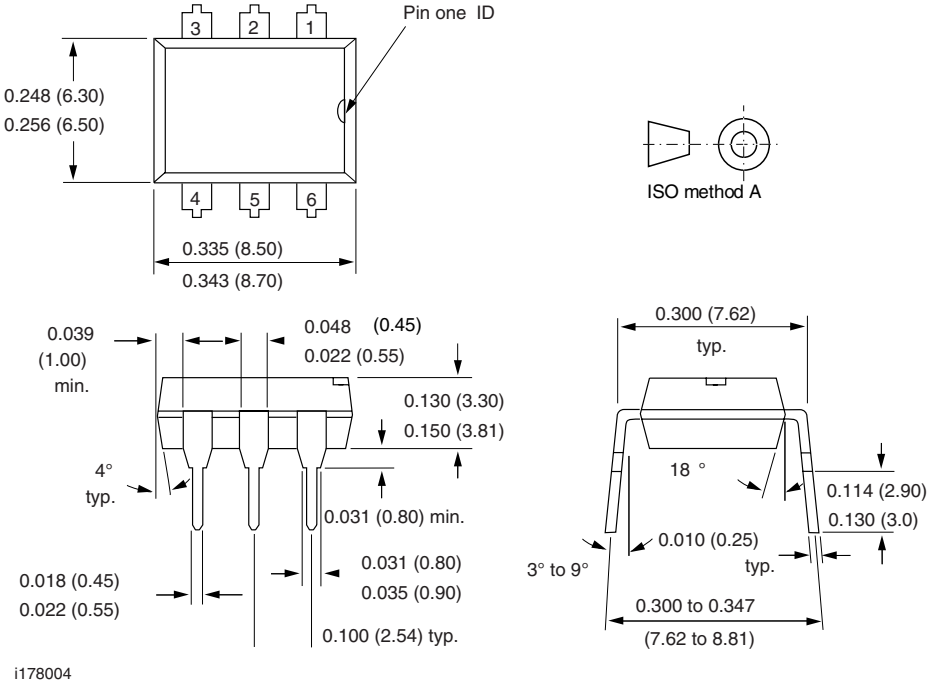
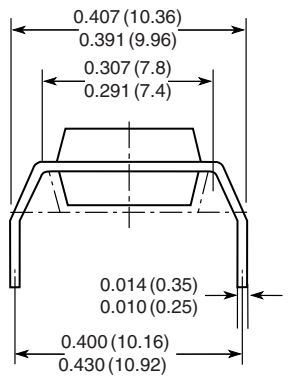


Fig. 20 - Transistor Capacitance

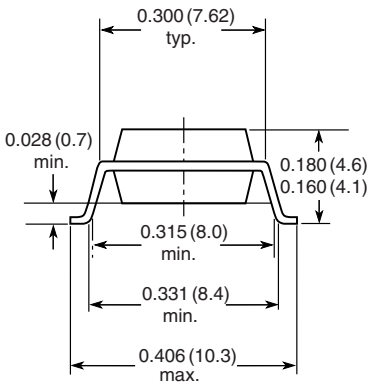
PACKAGE DIMENSIONS in inches (millimeters)



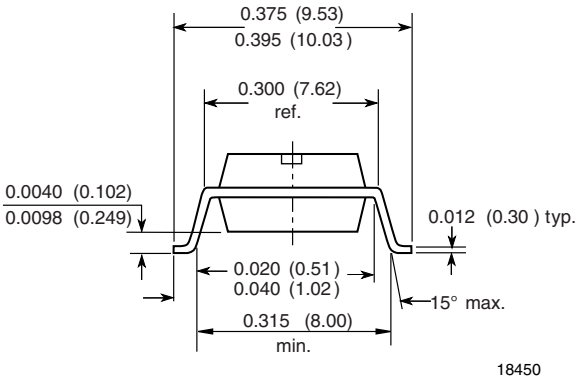
Option 6



Option 7



Option 9



OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

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All product specifications and data are subject to change without notice.

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