

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

ATP404 — General-Purpose Switching Device Applications

Features

- ON-resistance $R_{DS}(on)1=5.5m\Omega$ (typ.)
- Input capacitance Ciss=6400pF (typ.)
- Halogen free compliance

Specifications

• 4.5V drive

Absolute Maximum Ratings at Ta=25°C

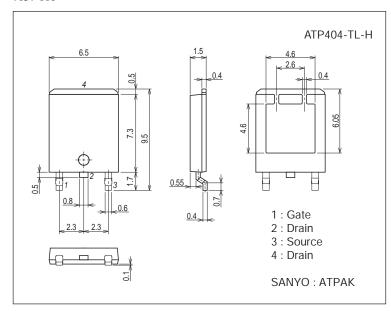
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		95	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	380	А
Allowable Power Dissipation	PD	Tc=25°C	70	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		214	mJ
Avalanche Current *2	IAV		48	А

Note :*1 VDD=30V, L=100µH, IAV=48A

*2 L≤100µH, Single pulse

Package Dimensions

unit : mm (typ) 7057-001

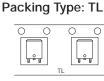


Product & Package Information

Package : ATPAK
JEITA, JEDEC :-

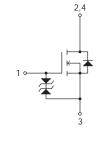
• Minimum Packing Quantity : 3,000 pcs./reel

Marking





Electrical Connection



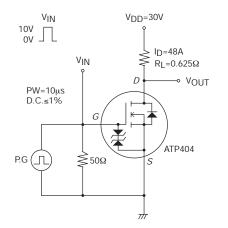
SANYO Semiconductor Co., Ltd. http://semicon.sanyo.com/en/network

61312 TKIM/12109QA MSIM TC-00001833 No. A1405-1/7

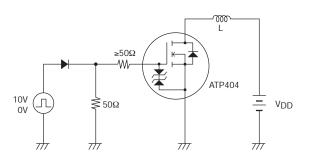
Electrical Characteristics at Ta=25°C

Parameter	Cumhal	Conditions	Ratings			11-11
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0V			10	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =48A		100		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	ID=48A, VGS=10V		5.5	7.2	mΩ
Static Drain-to-Source On-State Resistance	R _{DS} (on)2	ID=48A, VGS=4.5V		7.5	10.5	mΩ
Input Capacitance	Ciss			6400		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		490		pF
Reverse Transfer Capacitance	Crss			380		pF
Turn-ON Delay Time	t _d (on)			53		ns
Rise Time	tr			640		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		380		ns
Fall Time	tf			520		ns
Total Gate Charge	Qg			120		nC
Gate-to-Source Charge	Qgs	V _{DS} =30V, V _{GS} =10V, I _D =95A		25		nC
Gate-to-Drain "Miller" Charge	Qgd	1		25		nC
Diode Forward Voltage	V _{SD}	IS=95A, VGS=0V		0.95	1.2	V

Switching Time Test Circuit



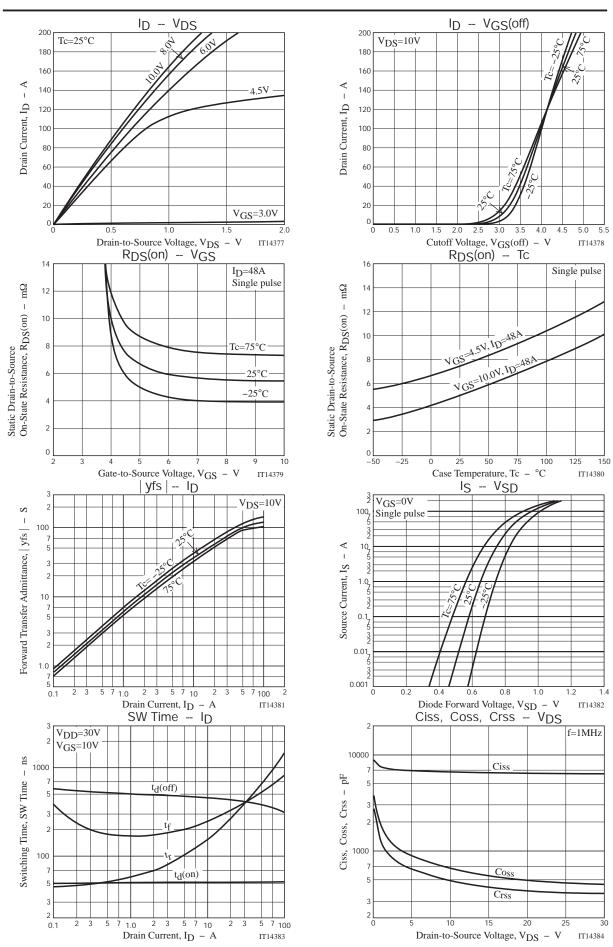
Avalanche Resistance Test Circuit



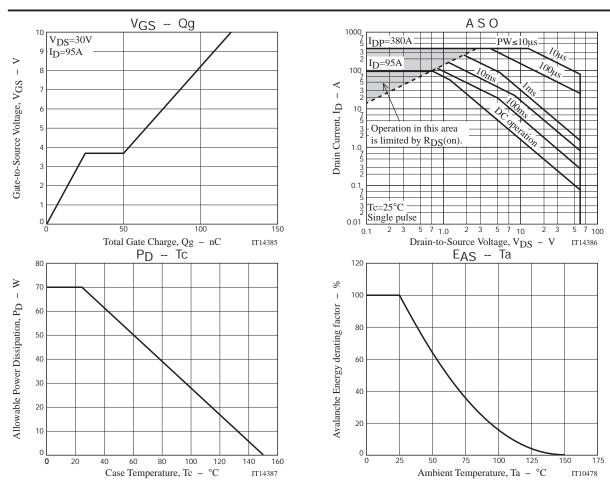
Ordering Information

0				
Device	Package	Shipping	memo	
ATP404-TL-H	ATPAK	3,000pcs./reel	Pb Free and Halogen Free	

ATP404



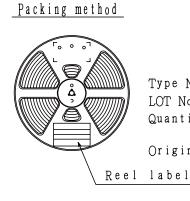
ATP404

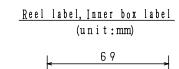


Taping Specification ATP404-TL-H

1. Packing Format (TL)

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format		
Fackage Name	Туре	Reel	Inner box	Outer box	INNER BOX SD-C-18	OUTER BOX SD-A-18	
					1 reels contained	5 inner boxes contained	
ATPAK	ATP	3,0003,	3,000	15,000) Dimensions:mm (external) Dimensions:mm (ext	Dimensions:mm (external)	
					340×340×28	355×355×165	





Outer box label It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

	L 108
	TYPE CODE
	TTPB 00000000
	QTY 0, 000 PCS LEAD FREE #
8 0	
	PACKAGE COCCOCCO
-	SPBCIAL ####################################

NOTE (1) The LEAD FREE 💥 description shows that the surface treatment of the terminal is lead free.

4

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

7-1. Carrier tape size (unit:mm)

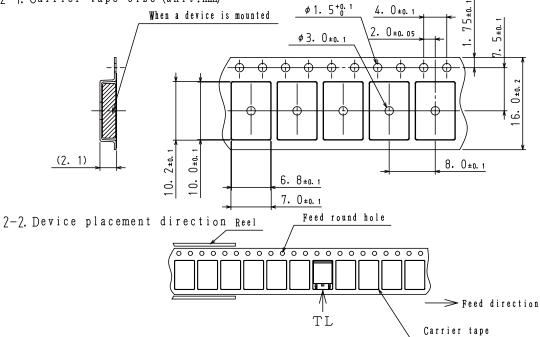
Type No. →

Quantity->

 \rightarrow

LOT No.

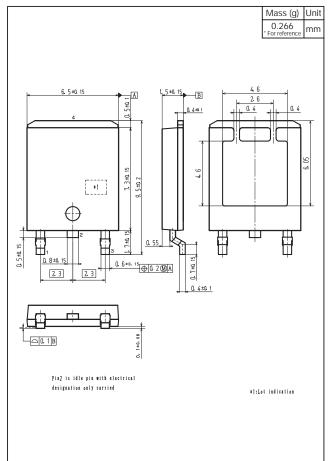
Origin



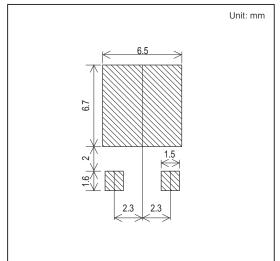
The one erectrode terminals on feed hole side TL

ATP404

Outline Drawing ATP404-TL-H



Land Pattern Example



Note on usage : Since the ATP404 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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