

## SANYO Semiconductors

• 4.5V drive

DATA SHEET

An ON Semiconductor Company

# **ATP201**

N-Channel Silicon MOSFET

## **General-Purpose Switching Device Applications**

· Halogen free compliance

## **Features**

- · Low ON-resistance
- Slim package
- · Protection diode in

## **Specifications**

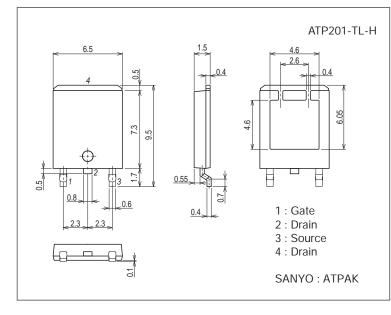
#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		35	А
Drain Current (PW≤10μs)	IDP	PW≤10μs, duty cycle≤1%	105	А
Allowable Power Dissipation	PD	Tc=25°C	30	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		10	mJ
Avalanche Current *2	IAV		18	А

Note :\*1 VDD=10V, L=50µH, IAV=18A \*2 L≤50µH, Single pulse

#### Package Dimensions

unit : mm (typ) 7057-001



### **Product & Package Information**

 Package : ATPAK

• JEITA, JEDEC

• Minimum Packing Quantity : 3,000 pcs./reel

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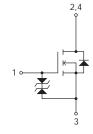
#### Packing Type: TL



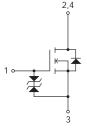


Marking

#### **Electrical Connection**



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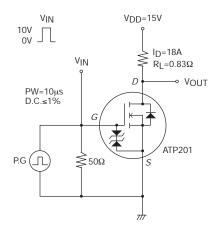
#### SANYO Semiconductor Co., Ltd. http://semicon.sanyo.com/en/network

61312 TKIM/82609PA TKIM TC-00002027 No. A1547-1/7

#### Electrical Characteristics at Ta=25°C

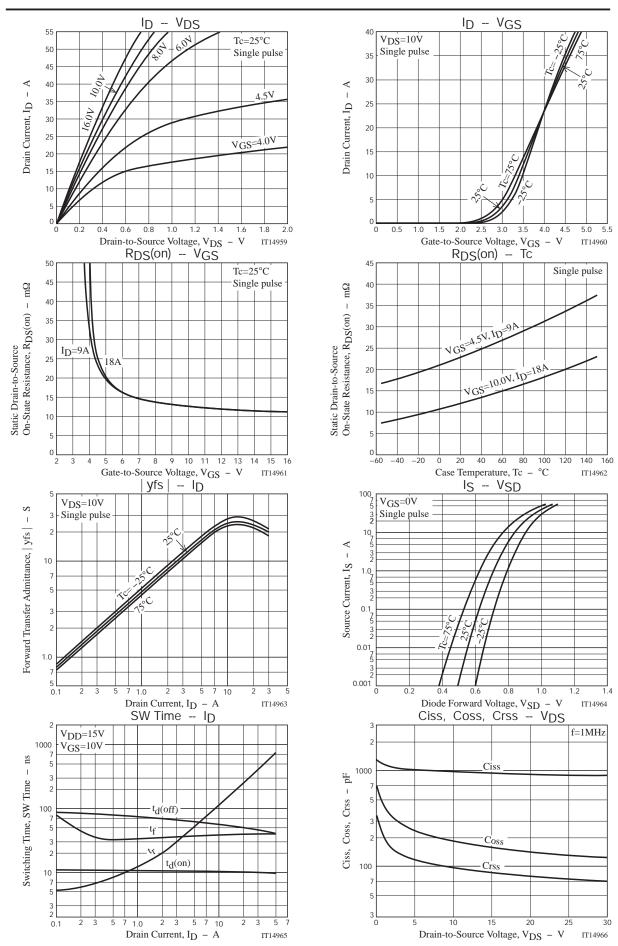
Decemeter	Cumhal	Conditions	Ratings			Linit	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	30			V	
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μΑ	
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.2		2.6	V	
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =18A		24		S	
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=18A, VGS=10V		13	17	mΩ	
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)2	ID=9A, VGS=4.5V		23	33	mΩ	
Input Capacitance	Ciss			985		рF	
Output Capacitance	Coss	V <sub>DS</sub> =10V, f=1MHz		180		pF	
Reverse Transfer Capacitance	Crss			100		pF	
Turn-ON Delay Time	t <sub>d</sub> (on)			10		ns	
Rise Time	tr			230		ns	
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		51		ns	
Fall Time	tf			39		ns	
Total Gate Charge	Qg			17		nC	
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =35A		4.7		nC	
Gate-to-Drain "Miller" Charge	Qgd	1		2.8		nC	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =35A, V <sub>GS</sub> =0V		0.97	1.2	V	

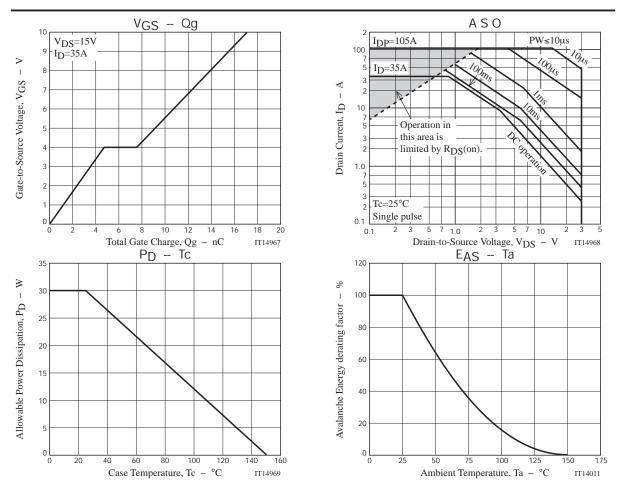
## Switching Time Test Circuit



#### **Ordering Information**

Device	Package	Shipping	memo	
ATP201-TL-H	ATPAK	3,000pcs./reel	Pb Free and Halogen Free	

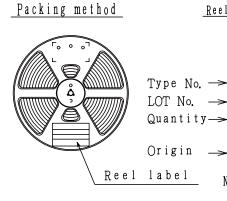




#### **Taping Specification** ATP201-TL-H

	- • •		(
1	Packing	Format	(TTT)
•	Tacrus	LOIMAT	

Package Name	Carrier Tape	Maximum Number devices contained			Packing format		
Fackage Name	Туре	Reel	Inner box	Outer box	INNER BOX SD-C-18	OUTER BOX SD-A-18	
					1 reels contained	5 inner boxes contained	
ATPAK	ATP	3,000	3,000	15,000	Dimensions:mm (external)	Dimensions:mm (external)	
					340×340×28	355×355×165	



Reel	label,	Inner	box	label
	(1	init:	mm)	

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Outer box label It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

	L		108
	Γ		-
		TYPE CODE	*
		TYPB	00000000
		QTY	0, 000 pcs (1) LEAD FREE #
8 0		lot	00000000
		PACKAGE	00000000
-		SPECIAL ASSEMBLY:	*20722005310C* **** (DIFFUSION: *****)

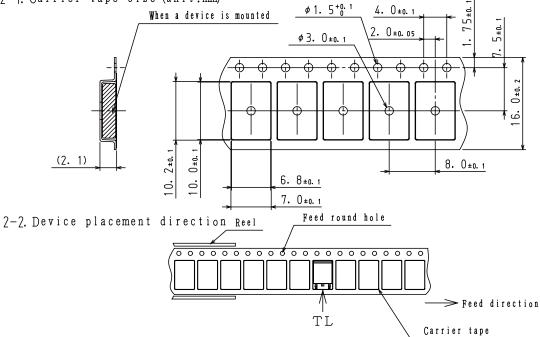
#### NOTE (1) The LEAD FREE 💥 description shows that the surface treatment of the terminal is lead free.

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Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

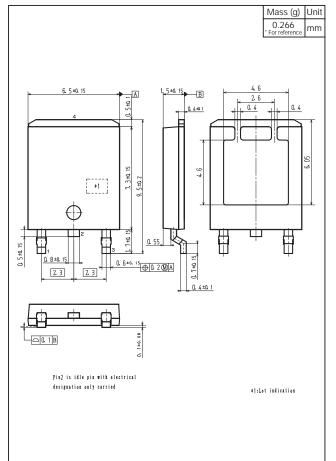
7-1. Carrier tape size (unit:mm)



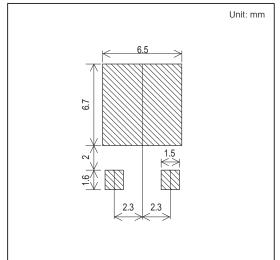
The one erectrode terminals on feed hole side .... TL

## ATP201

### Outline Drawing ATP201-TL-H



#### Land Pattern Example



## Note on usage : Since the ATP201 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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