# RT1N144X SERIES

**(Transistor)** 

UNIT: mm

0.5

Transistor With Resistor

For Switching Application

Silicon NPN Epitaxial Type

# **DESCRIPTION**

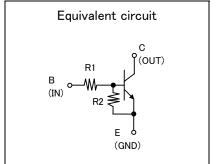
RT1N144X is a one chip transistor with built-in bias resistor, PNP type is RT1P144X.

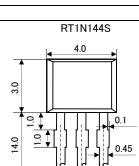
# **FEATURE**

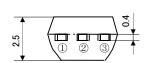
•Built-in bias resistor (R1=10k  $\Omega$  ,R2=47k  $\Omega$  ).

#### **APPLICATION**

Inverted circuit, switching circuit, interface circuit, driver circuit.







1.27 1.27

JEITA: -JEDEC: -

**Terminal Connector** 

- 1: Emitter
- 2: Collector
- 3:Base

# OUTLINE DRAWING

RT1N144C

0.5

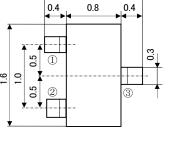
1.90

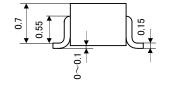
2.5

1.5

1.6 0.4 0.8 0.5 1.6 1.0

RT1N144U

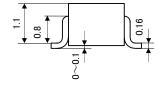




JEITA: -JEDEC: -

Terminal Connector

- 1:Base
- 2: Emitter
- 3: Collector



JEITA: SC-59

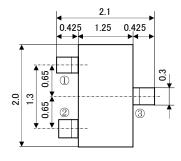
JEDEC: Similar to TO-236

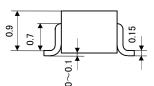
Terminal Connector

- 1:Base
- 2: Emitter
- 3: Collector

RT1N144M

RT1N144T

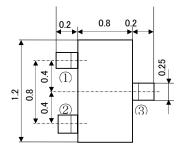


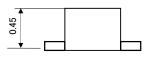


JEITA: SC-70 JEDEC: -

**Terminal Connector** 

- (1):Base
- 2: Emitter
- 3: Collector





JEITA: -, JEDEC: -ISAHAYA: T-USM

**Terminal Connector** 

- 1:Base
- 2: Emitter
- 3: Collector

ISAHAYA ELECTRONICS CORPORATION

# RT1N144X SERIES

**(Transistor)** 

Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

# MAXIMUM RATING (Ta=25°C)

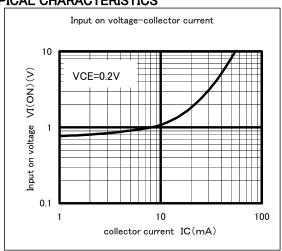
SYMBOL	PARAMETER	RATING					UNIT
		RT1N144T	RT1N144U	RT1N144M	RT1N144C	RT1N144S	UNIT
$V_{CBO}$	Collector to Base voltage	50					
$V_{EBO}$	Emitter to Base voltage	6					٧
V <sub>CEO</sub>	Collector to Emitter voltage	50					
I c	Collector current	100					
I <sub>CM</sub>	Peak Collector current	200					mA
P <sub>c</sub>	Collector dissipation(Ta=25°C)	125(※)	150	20	00	450	mW
Tj	Junction temperature	+125 +150					°C
Tstg	Storage temperature	-55~+125 -55 <b>~</b> +150					°C

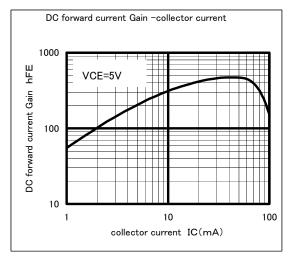
# ELECTRICAL CHARACTERISTICS (Ta=25°C)

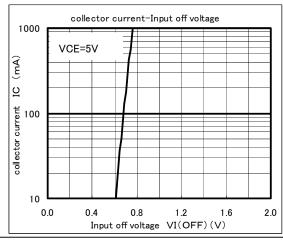
( $\mbox{\@monosphice}\xspace$ ) package mounted on 9mm × 19mm × 1mm glass-epoxy substrate.

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
		TEST CONDITION	MIN	TYP	MAX	UNIT
$V_{(BR)CEO}$	C to E break down voltage	I <sub>C</sub> =100 μ A, R <sub>BE</sub> =∞	50			V
I <sub>CBO</sub>	Collector cut off current	$V_{CB}$ =50V, I $_{E}$ =0			0.1	μΑ
h <sub>FE</sub>	DC forward current gain	$V_{CE}$ =5V, I $_{C}$ =5mA	50			_
$V_{CE(sat)}$	C to E saturation voltage	$I_{C} = 10 \text{mA}, I_{B} = 0.5 \text{mA}$		0.1	0.3	V
$V_{I(ON)}$	Input on voltage	$V_{CE}$ =0.2V, I $_{C}$ =5mA		1.0	1.8	V
$V_{I(OFF)}$	Input off voltage	$V_{CE}$ =5V, I $_{C}$ =100 $\mu$ A	0.4	0.7		V
R <sub>1</sub>	Input resistance		7.0	10	13	kΩ
R <sub>2</sub> /R <sub>1</sub>	Resistance ratio		4.2	4.7	5.1	
f⊤	Gain band width product	$V_{CE} = 6V, I_{E} = -10mA$		200		MHz

TYPICAL CHARACTERISTICS









Marketing division, Marketing planning department

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