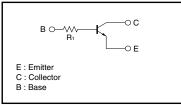
Digital transistors (built-in resistor) DTC343TK / DTC343TS

Features

In addition to the features of regular digital transistors,

- 1) Low $V_{CE(sat)}$ makes these transistors ideal for muting circuits. (Typ. 0.04V at I_O/I_B=50/2.5mA)
- 2) They can be used at high current. (I_{CMax}. =600mA)

Circuit schematic



Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	30	V
Collector-emitter voltage		VCEO	15	V
Emitter-base voltage		VEBO	5	V
Collector current		lc	600	mA
Collector power dissipation	DTC343TK	Pc	200	mW
	DTC343TS	PC	300	
Junction temperature		Tj	150	°C
Storage temperature		Tstg	-55 to +150	°C

•Package, marking, and packaging specifications

Part No.	DTC343TK	DTC343TS
Package	SMT3	SPT
Marking	H03	-
Packaging code	T146	TP
Basic ordering unit (pieces)	3000	5000

•External characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	30	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	15	_	-	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	5	_	-	V	Ιε=50μΑ
Collector cutoff current	Ісво	_	_	0.5	μΑ	V _{CB} =20V
Emitter cutoff current	Іево	_	_	0.5	μΑ	VEB=4V
Collector-emitter saturation voltage	VCE(sat)	_	40	80	mV	Ic=50mA , Iв=2.5mA
DC current transfer ratio	hfe	100	250	600	-	Ic=50mA , Vce=5V
Input resistance	R1	3.29	4.7	6.11	kΩ	_
Transition frequency	f⊤	_	200	-	MHz	Vce=10V , Ie= -50mA , f=100MHz *
Output on resistance	Ron	-	0.95	-	Ω	VI=7V , R=1kΩ , f=1kHz

*Transition frequency of the device.

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Transistors

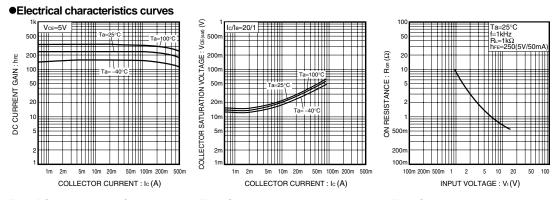


Fig.1 DC current gain vs. Collector current Fig.2 Collector-emitter saturation voltage Fig.3 ON resistance vs. Input voltage vs. Collector current



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