

SURFACE MOUNT CHIP LED LAMP SPECIFICATION

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1.0

REVISION:

●COMMODITY: SURFACE MOUNT CHIP LED LAMP

● DEVICE NUMBER: BL-HJF36A-AV-TRB

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2002.07.12	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0		Initial Released

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SURFACE MOUNT CHIP LED LAMP SPECIFICATION

● COMMODITY: SURFACE MOUNT CHIP LED LAMP

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●ELECTRICAL AND OPTICAL CHARACTERISTICS (Ta=25°C)

-1.6(.063)±0.2-

-0.95(.037)

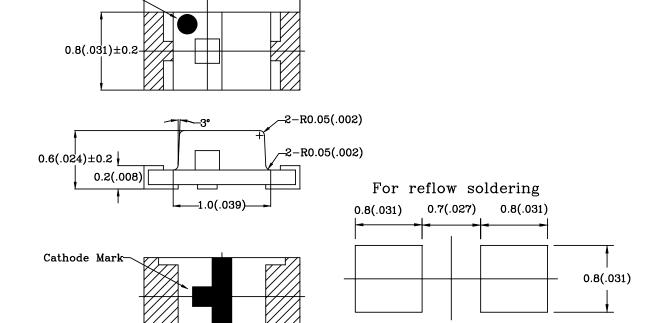
Chip			Absolute Maximum				Electro-optical				Viewing	
	Peak	Dominant	Lens		Rat	ing		Ι	Data (A	t 20mA	.)	Angle
Emitted Color	Wave Length	Wave Length	Appearance	Δλ	Pd	If	Peak	Vf	(V)	Iv(n	ncd)	$2\theta 1/2$ (deg)
	λ p(nm)	λd(nm)		(nm)	(mW)	(mA)	If(mA)	Тур.	Max.	Min	Тур.	(deg)
Super Amber	610	605±5	Water Clear	17	100	30	100	2.1	2.6	28.0	50.0	120

Remark: Viewing angle is the Off-axis angle at which the luminous intensity is half the axial luminous intensity.

lacktriangle ABSOLUTE MAXIMUM RATINGS (Ta=25 $^{\circ}$ C)



Cathode Mark-



-0.35(.014)

NOTES: 1.All dimensions are in millimeters (inches).

2. Tolerance is \pm 0.10mm (0.004) unless otherwise specified.

-0.35(.014)

3. Specifications are subject to change without notice.

Downloaded from Elcons. collision for IFp is pulse of 1/10 duty and 0.1 msec width.

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ELECTRICAL AND OPTICAL CHARACTERISTICS (Ta=25°C) **REVISION:** 1.0

Fig.1 RELATIVE INTENSITY VS. WAVELENGTH

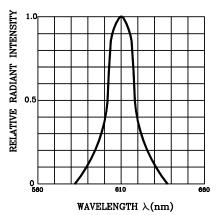


Fig.2 FORWARD CURRENT DERATING CURVE

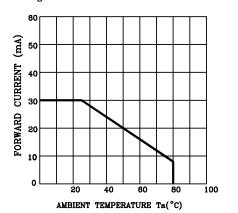


Fig.3 FORWARD CURRENT VS. FORWARD VOLTAGE

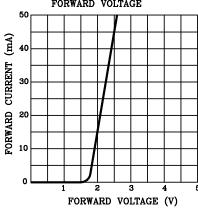


Fig.4 RELATIVE LUMINOUS INTENSITY VS. AMBIENT TEMPERATURE

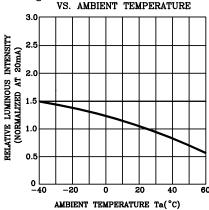


Fig.5 RELATIVE LUMINOUS INTENSITY VS. FORWARD CURRENT

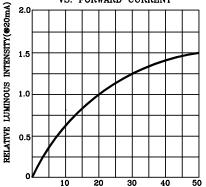
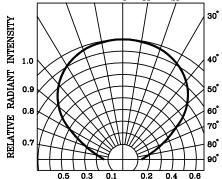


Fig.6 RADIATION DIAGRAM



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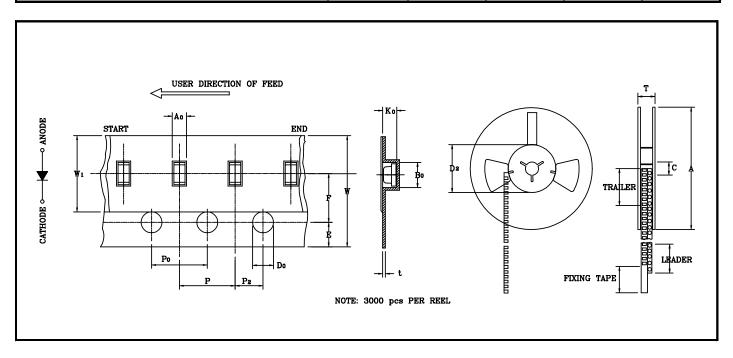
●-TRB: TAPPING & REELING, BLACK CAMRIER TAPE, ESD RESISTED.

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●TAPPING AND PACKAGING SPECIFICATION

●ELECTRICAL AND OPTICAL CHARACTERISTICS (Ta=25°C)

		SPECIFICATION				
ITEM	SYMBOL	Mini	mum	Maximum		
		mm	inch	mm	inch	
Tape Feed Hole Diameter (DIA)	D_0	1.40	0.055	1.60	0.063	
Feed Hole Location	Е	1.65	0.064	1.85	0.073	
Centers Line Dimensions Length Direction	F	3.45	0.135	3.55	0.139	
Compartment Depth	K_0	0.65	0.026	0.75	0.030	
Compartment Pitch	P	3.90	0.153	4.10	0.161	
Sprocket Hole Diameter	P_0	3.90	0.153	4.10	0.161	
Centers Line Dimensions Length Direction	P_2	1.95	0.076	2.05	0.080	
Carrier Tape Thickness	t	_	_	0.30	0.012	
Carrier Tape Width	W	7.70	0.303	8.30	0.326	
Flange Diameter	A	178.0	7.008	180.0	7.087	
Hub Spindle Hole	С	12.50	0.492	13.50	0.531	
Hub Diameter	D_2	70.00	2.755	72.00	2.830	
Fixing Tape Width	\mathbf{W}_1	5.25	0.206	5.35	0.210	
Flange Space Between Flanges	T	12.50	0.492	13.50	0.531	
Compartment Length	A_0	0.85	0.033	0.95	0.037	
Compartment Width	B_0	1.70	0.067	1.80	0.071	



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RELIABILITY TEST

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Classification	Test Item	Reference Standard	Test Conditions	Result
	Operation Life	MIL-STD-750:1026 MIL-STD-883:1005 JIS C 7021 :B-1	Connect with a power If=20mA Ta=Under room temperature Test time=1,000hrs	0/20
Endurance Storage		MIL-STD-202:103B JIS C 7021 :В-11	Ta=+65°C±5°C RH=90%-95% Test time=240hrs	0/20
Test	High Temperature Storage	MIL-STD-883:1008 JIS C 7021 :B-10	High Ta=+85°C±5°C Test time=1,000hrs	0/20
	Low Temperature Storage	JIS-C-7021 :B-12	Low Ta=-35°C±5°C Test time=1,000hrs	0/20
	Temperature Cycling	MIL-STD-202:107D MIL-STD-750:1051 MIL-STD-883:1010 JIS C 7021 :A-4	-35° C $\sim +25^{\circ}$ C $\sim +85^{\circ}$ C $\sim +25^{\circ}$ C 60min 20min 60min 20min Test Time=5cycle	0/20
Environmental Test	Thermal Shock	MIL-STD-202:107D MIL-STD-750:1051 MIL-STD-883:1011	-35°C±5°C ~+85°C±5°C 20min 20min Test Time=10cycle	0/20
	Solder Resistance	MIL-STD-202:201A MIL-STD-750:2031 JIS C 7021 :A-1	IL-STD-750:2031 Operation heating:	

JUDGMENT CRITERIA OF FAILURE FOR THE RELIABILITY

Measuring items	Symbol	Measuring conditions	Judgement criteria for failure
Forward voltage	$V_{F}(V)$	If=20mA	Over Ux1.2
Reverse current	Ir(uA)	Vr=5V	Over Ux2
Luminous intensity	Iv (mcd)	If=20mA	Below SX0.5

Note: 1.U means the upper limit of specified characteristics. S means initial value.

2.Measurment shall be taken between 2 hours and after the test pieces have been returned to normal ambient conditions after completion of each test.

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SOLDERING: 1.

Manual Of Soldering

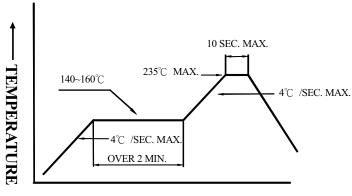
The temperature of the iron tip should not be higher than 300° C (572°F) and Soldering within 3 seconds per solder-land is to be observed.

Reflow Soldering

Preheating: 140° C $\sim 160^{\circ}$ C $\pm 5^{\circ}$ C, within 2 minutes.

Operation heating: 235°C (MAX.) within 10 seconds.(Max)

Gradual Cooling (Avoid quenching).

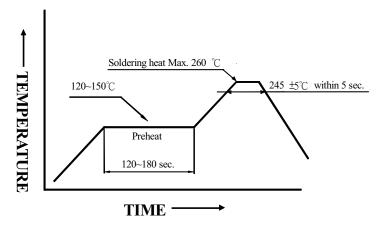


TIME -

DIP soldering (Wave Soldering)

Preheating: 120°C~150°C, within 120~180 sec. Operation heating: 245°C±5°C within 5 sec.260°C (Max)

Gradual Cooling (Avoid quenching).



Handling: 2.

Care must be taken not to cause to the epoxy resin portion of BRIGHT LEDs while it is exposed to high temperature.

Care must be taken not rub the epoxy resin portion of BRIGHT LEDs with hard or sharp article such as the sand blast and the metal hook.

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3. Notes for designing:

Care must be taken to provide the current limiting resistor in the circuit so as to drive the BRIGHT LEDs within the rated figures. Also, caution should be taken not to overload BRIGHT LEDs with instantaneous voltage at the turning ON and OFF of the circuit.

When using the pulse drive care must be taken to keep the average current within the rated figures. Also, the circuit should be designed so as be subjected to reverse voltage when turning off the BRIGHT LEDs.

4. Storage:

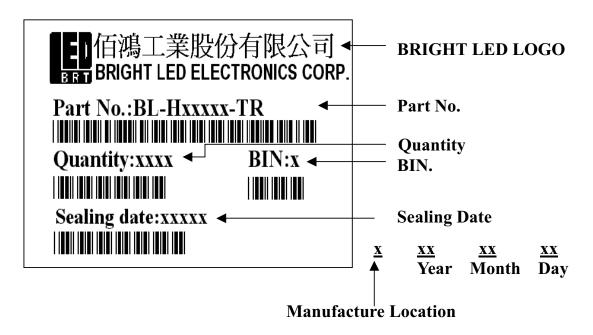
In order to avoid the absorption of moisture, it is recommended to solder BRIGHT LEDs as soon as possible after unpacking the sealed envelope.

If the envelope is still packed, to store it in the environment as following:

- (1) Temperature: $5^{\circ}\text{C}-30^{\circ}\text{C}(41^{\circ}\text{F})$ Humidity: RH 60% Max.
- (2) After this bag is opened, devices that will be applied to infrared reflow, vapor-phase reflow, or equivalent soldering process must be:
- a. Completed within 24 hours.
- b. Stored at less than 30% RH.
- (3) Devices require baking before mounting, if:
 - (2) a or (2) b is not met.
- (4) If baking is required, devices must be baked under below conditions: 12 hours at $60^{\circ}\text{C} \pm 3^{\circ}\text{C}$.

5. Package and Label of Products:

- (1) Package: Products are packed in one bag of 3000 pcs (one taping reel) and a label is attached on each bag.
- (2) Label:



SURFACE MOUNT CHIP LED LAMP SPECIFICATION Intensity And Color Bin Limits

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• Intensity Bin Limits (At 20mA)

BIN CODE	Min. (mcd)	Max. (mcd)
M	28.0	42.0
L	42.0	63.0
N	63.0	94.0

Tolerance for each Bin limit is \pm 15 %

● Color Bin Limits (At 20mA)

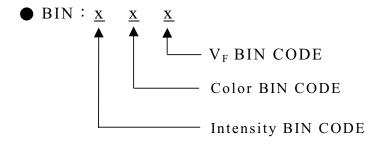
BIN CODE	Min. (nm)	Max. (nm)
1	600	604
2	604	608
3	608	612

Tolerance for each Bin limit is ± 1 nm

• V_F Bin Limits (At 20mA)

BIN CODE	Min.(v)	Max.(v)
С	2.00	2.20
D	2.20	2.40
Е	2.40	2.60

Tolerance for each Bin limit is $\pm 15\%$



Notes:

1. Bin categories are established for classification of products.

Products may not be available in all bin categories.