

P-CHANNEL ENHANCEMENT MODE POWER MOSFET

MTP9575Q8

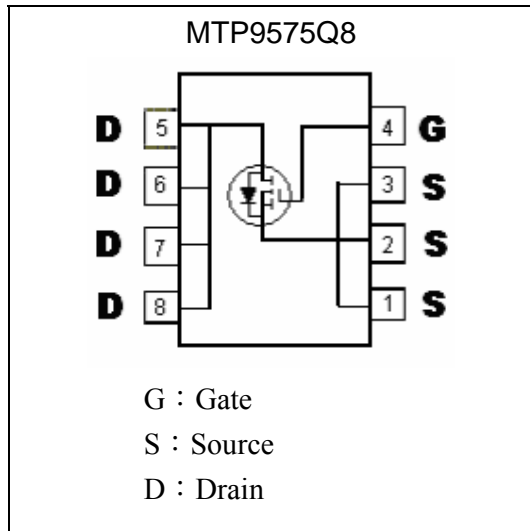
Description

The MTP9575Q8 is a P-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The SOP-8 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

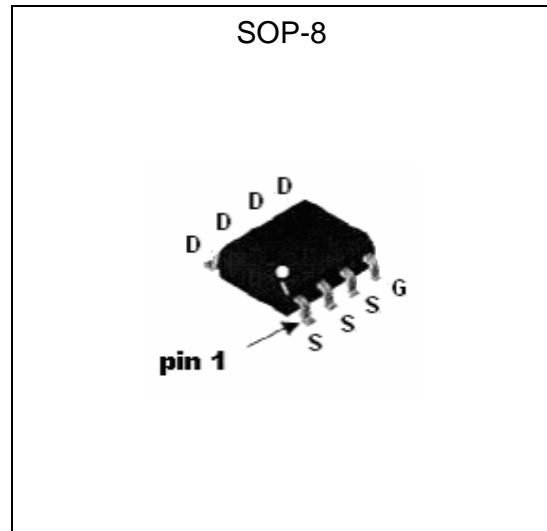
Features

- $R_{DS(ON)}=90m\Omega @V_{GS}=-10V, I_D=-4A$
 $R_{DS(ON)}=120m\Omega @V_{GS}=-4.5V, I_D=-3A$
- Simple drive requirement
- Low on-resistance
- Fast switching speed
- RoHS compliant package

Equivalent Circuit



Outline





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Breakdown Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±25	V
Continuous Drain Current (Note 1)	I _D	-4.0	A
Continuous Drain Current, TA=70°C (Note 1)	I _D	-3.2	A
Pulsed Drain Current (Note 2)	I _{DM}	-20	A
Total Power Dissipation (Note 1)	P _d	2.5	W
Linear Derating Factor		0.02	W / °C
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+150	°C
Thermal Resistance, Junction-to-Ambient (Note 1)	R _{th,j-a}	50	°C/W

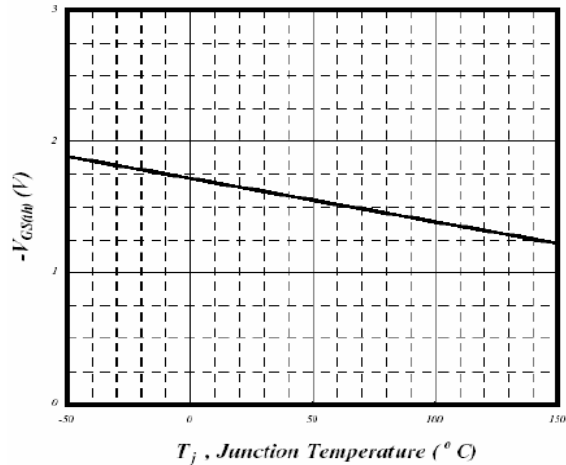
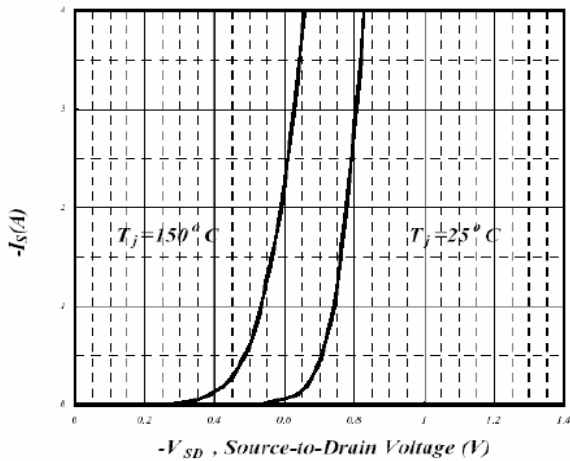
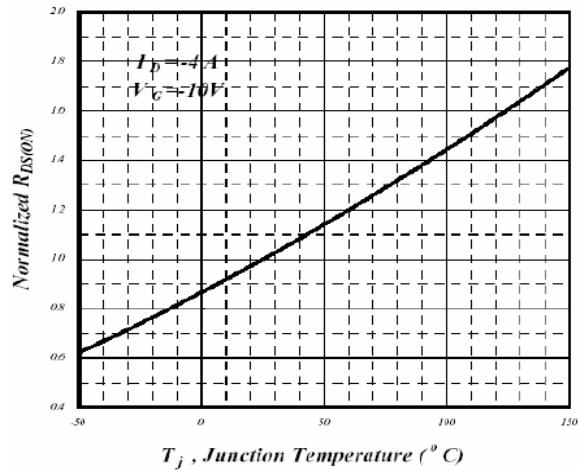
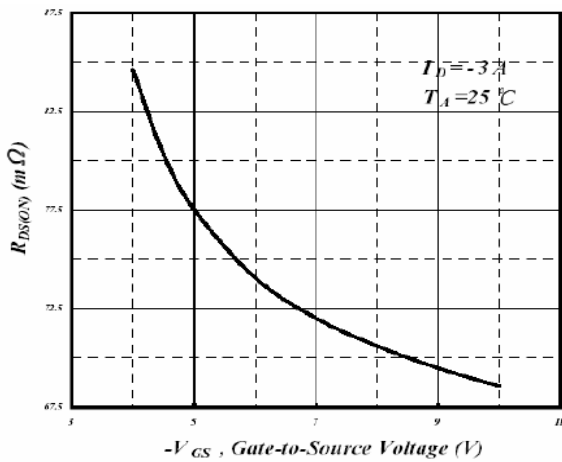
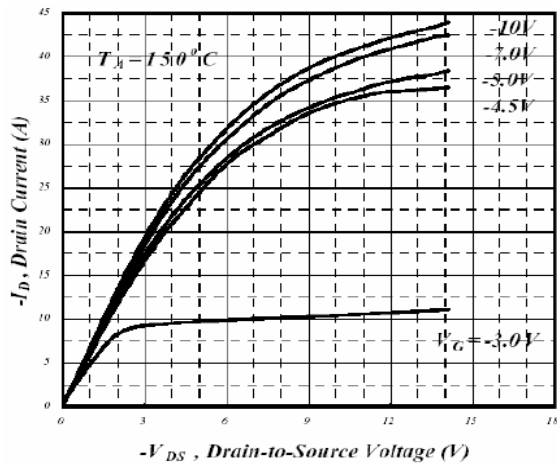
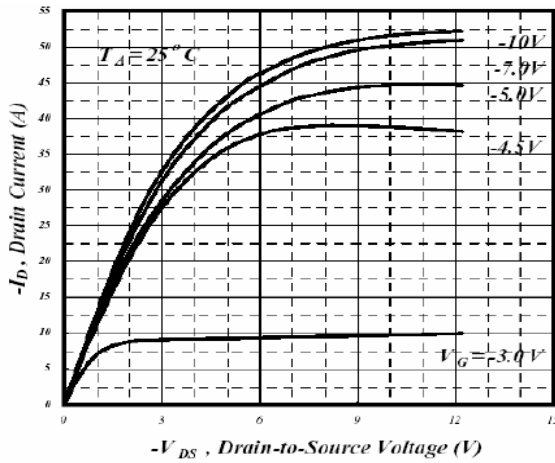
Note : 1.Surface mounted on 1 in² copper pad of FR-4 board; 125 °C/W when mounted on minimum copper pad
 2.Pulse width ≤300μs, Duty Cycle≤2%

Electrical Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-60	-	-	V	V _{GS} =0, I _D =-250μA
ΔBV _{DSS} /ΔT _j	-	-0.04	-	V/°C	Reference to 25°C, I _D =-1mA
V _{GS(th)}	-1.0	-	-3.0	V	V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±25V, V _{DS} =0
I _{DSS}	-	-	-1	μA	V _{DS} =-60V, V _{GS} =0
	-	-	-25		V _{DS} =-48V, V _{GS} =0, T _j =70°C
*R _{DS(ON)}	-	-	90	mΩ	I _D =-4A, V _{GS} =-10V
	-	-	120		I _D =-3A, V _{GS} =-4.5V
*G _{FS}	-	7	-	S	V _{DS} =-10V, I _D =-4A
Dynamic					
C _{iss}	-	1745	2790	pF	V _{DS} =-25V, V _{GS} =0, f=1MHz
C _{oss}	-	165	-		
C _{rss}	-	125	-		
*t _{d(ON)}	-	12	-	ns	V _{DD} =-30V, I _D =-1A, V _{GS} =-10V, R _G =3.3Ω, R _D =30Ω
*t _r	-	5	-		
*t _{d(OFF)}	-	68	-		
*t _f	-	32	-		
*Q _g	-	18	28	nC	V _{DS} =-48V, V _{GS} =-4.5V, I _D =-4A
*Q _{gs}	-	5	-		
*Q _{gd}	-	7	-		
Source Drain Diode					
*V _{SD}	-	-	-1.2	V	V _{GS} =0V, I _S =-2A
*T _{rr}	-	56	-	ns	I _S =-4A, V _{GS} =0, dI/dt=100A/μs
*Q _{rr}	-	146	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Characteristic Curves



Characteristic Curves(Cont.)

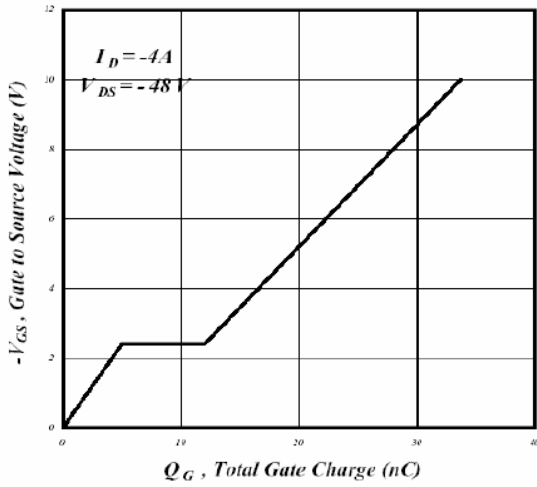


Fig 7. Gate Charge Characteristics

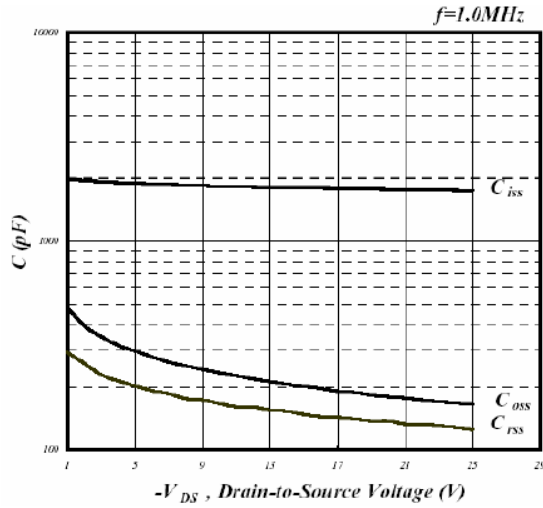


Fig 8. Typical Capacitance Characteristics

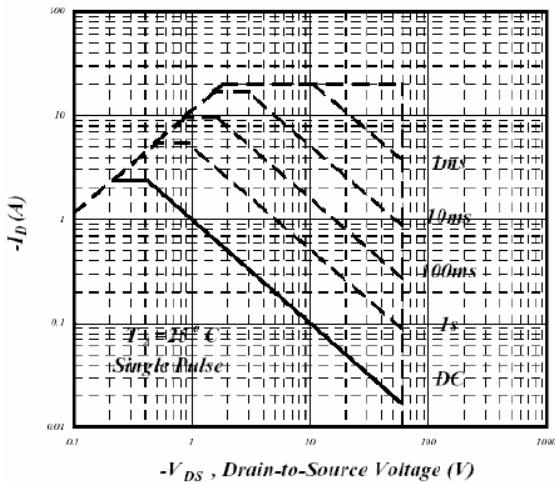


Fig 9. Maximum Safe Operating Area

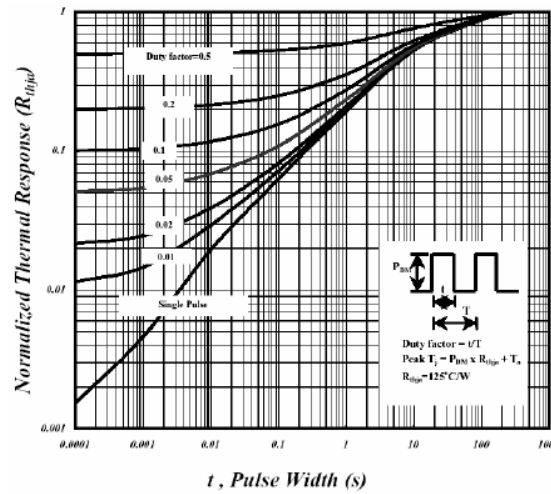


Fig 10. Effective Transient Thermal Impedance

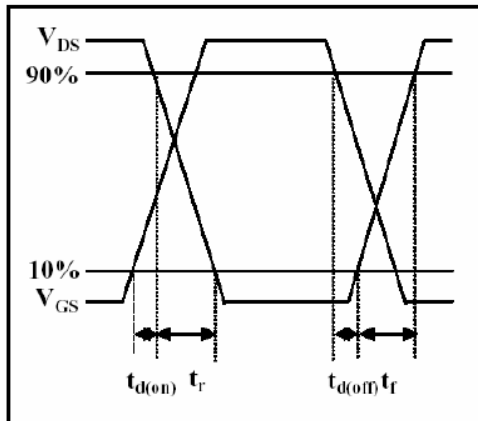


Fig 11. Switching Time Waveform

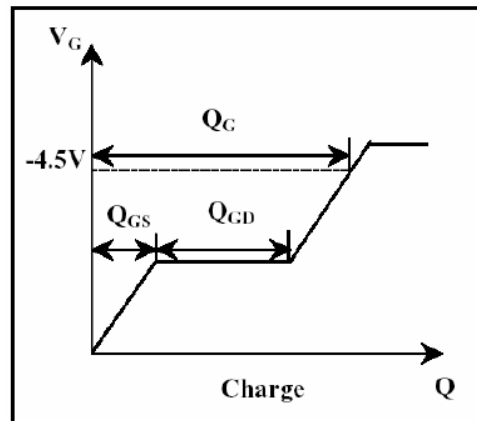
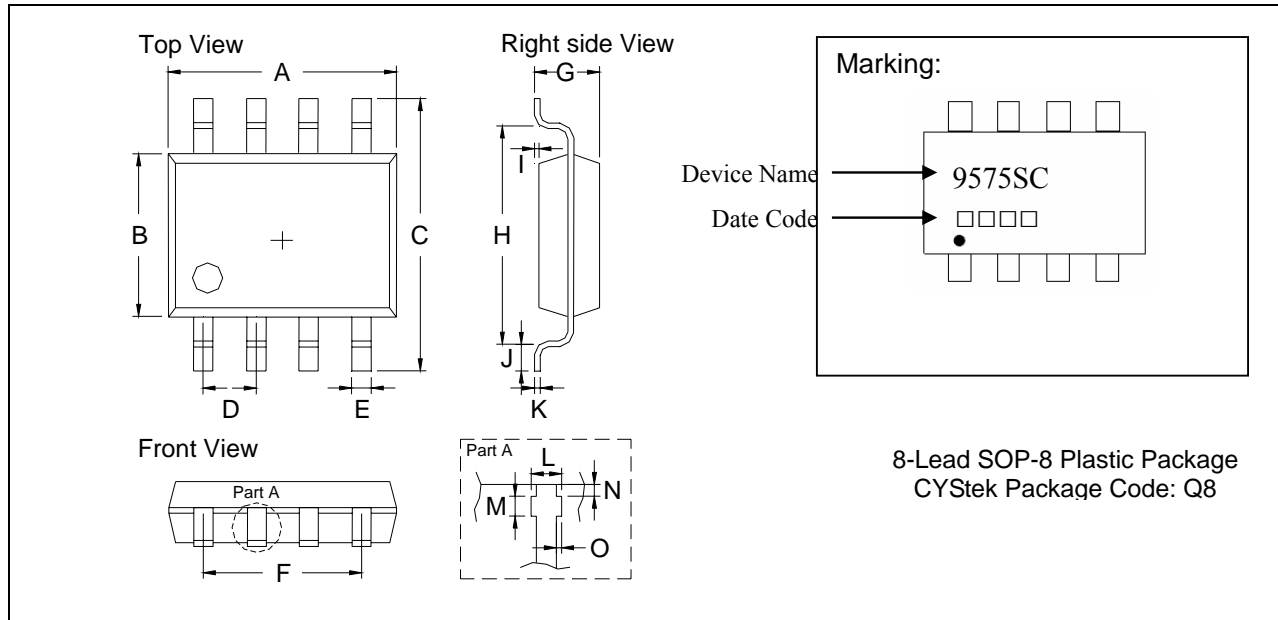


Fig 12. Gate Charge Waveform

SOP-8 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1909	0.2007	4.85	5.10	I	0.0019	0.0078	0.05	0.20
B	0.1515	0.1555	3.85	3.95	J	0.0118	0.0275	0.30	0.70
C	0.2283	0.2441	5.80	6.20	K	0.0074	0.0098	0.19	0.25
D	0.0480	0.0519	1.22	1.32	L	0.0145	0.0204	0.37	0.52
E	0.0145	0.0185	0.37	0.47	M	0.0118	0.0197	0.30	0.50
F	0.1472	0.1527	3.74	3.88	N	0.0031	0.0051	0.08	0.13
G	0.0570	0.0649	1.45	1.65	O	0.0000	0.0059	0.00	0.15
H	0.1889	0.2007	4.80	5.10					

Notes: 1. Controlling dimension: millimeters.
 2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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