

September 2012

FDP032N08

N-Channel PowerTrench[®] MOSFET 75V, 235A, 3.2m Ω

Features

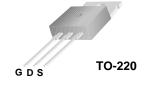
- $R_{DS(on)} = 2.5 m\Omega$ (Typ.)@ $V_{GS} = 10 V$, $I_{D} = 75 A$
- · Fast switching speed
- · Low gate charge
- High performance trench technology for extremely low R_{DS(on)}
- · High power and current handling capability
- · RoHS compliant

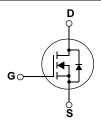
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's adcanced PowerTrench® process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Application

• DC to DC convertors / Synchronous Rectification





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted*

Symbol		Parameter		FDP032N08	Units
V _{DSS}	Drain to Source Voltage	urce Voltage			V
V _{GSS}	Gate to Source Voltage			±20	V
	Drain Current - Con	tinuous (T _C = 25°C, Silicon Limited	d)	235*	Α
I _D	- Con	d)	165*	Α	
	- Con	tinuous (T _C = 25°C, Package Limi	ted)	120	Α
I_{DM}	Drain Current	- Pulsed (Note 1)		940	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		1995	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	6.0	V/ns
D	Danier Diagination	$(T_C = 25^{\circ}C)$		375	W
P _D Power Dissipation		- Derate above 25°C		2.5	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +175	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C

*Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 120A.

Thermal Characteristics

Symbol	Parameter	FDP032N08	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.4	
$R_{\theta CS}$	Thermal Resistance, Case to Sink Typ.	0.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	

Package Marking and Ordering Information $T_C = 25^{\circ}C$ unless otherwise noted

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP032N08	FDP032N08	TO-220	-	-	50

Electrical Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0 V$, $T_C = 25 ^{\circ} C$	75	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$, Referenced to 25°C	-	0.05	-	V/°C
ı	Zero Gate Voltage Drain Current	V _{DS} = 75V, V _{GS} = 0V	-	-	1	^
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 75V, T_{C} = 150^{\circ}C$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\mu A$	2.5	3.5	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 75A$	-	2.5	3.2	mΩ
g _{FS}	Forward Transconductance	$V_{DS} = 10V, I_{D} = 75A$	-	180	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ f = 1MHz		-	11400	15160	pF
C _{oss}	Output Capacitance			-	1360	1810	pF
C _{rss}	Reverse Transfer Capacitance			-	595	800	pF
Q _{g(tot)}	Total Gate Charge at 10V			-	169	220	nC
Q _{gs}	Gate to Source Gate Charge	$V_{DS} = 60V, I_{D} = 75A$		-	60	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	V _{GS} = 10V	ote 4)	-	47	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	230	470	ns
t _r	Turn-On Rise Time	$V_{DD} = 37.5V, I_D = 75A$	-	191	392	ns
t _{d(off)}	Turn-Off Delay Time	$R_{GEN} = 25\Omega$, $V_{GS} = 10V$	-	335	680	ns
t _f	Turn-Off Fall Time	(Note 4)	-	121	252	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current			-	235	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	940	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 75A	-	-	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 75A	-	53	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	-	77	-	nC

Notes:

- Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 0.71mH, I_{AS} = 75A, V_{DD} = 50V, R_{G} = 25 $\!\Omega$, Starting T_{J} = 25 $^{\circ}C$
- 3. $I_{SD} \le 75 A$, di/dt $\le 200 A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$
- 4. Essentially Independent of Operating Temperature Typical Characteristics

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Typical Performance Characteristics

Figure 1. On-Region Characteristics

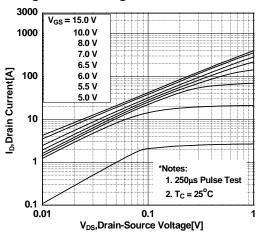


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

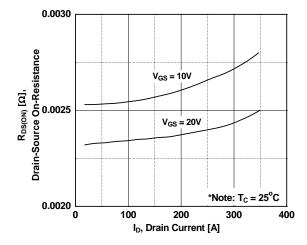


Figure 5. Capacitance Characteristics

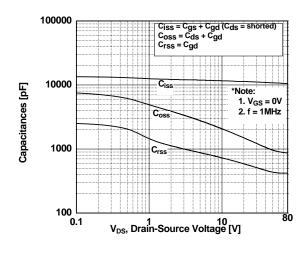


Figure 2. Transfer Characteristics

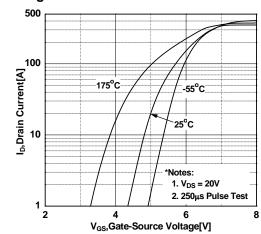


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

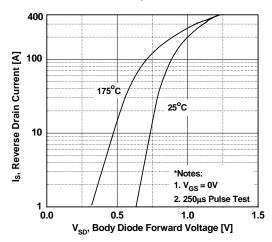
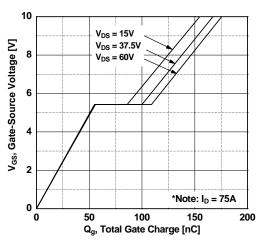


Figure 6. Gate Charge Characteristics



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Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

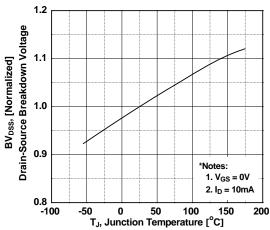


Figure 8. On-Resistance Variation vs. **Temperature**

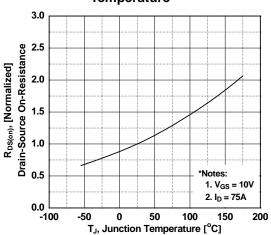
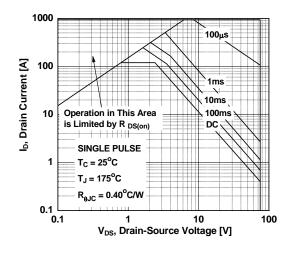


Figure 9. Maximum Safe Operating Area Figure 10. Maximum Drain Current vs. Case Temperature



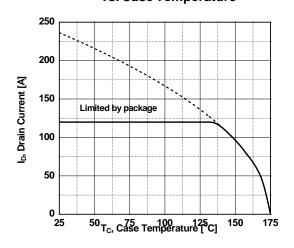
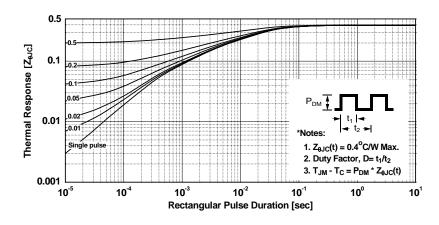
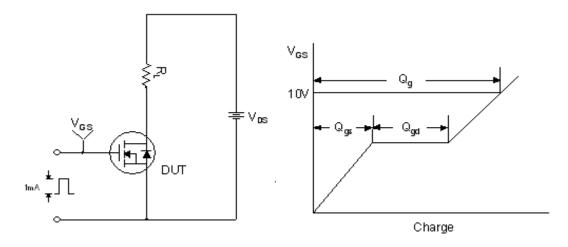


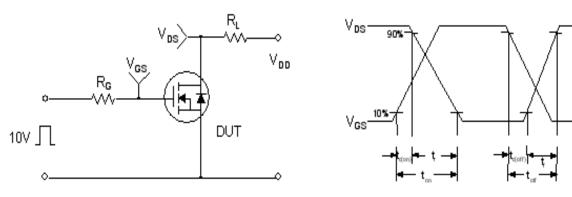
Figure 11. Transient Thermal Response Curve



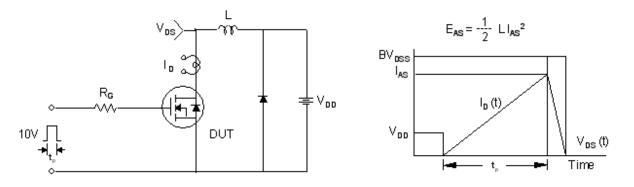
Gate Charge Test Circuit & Waveform



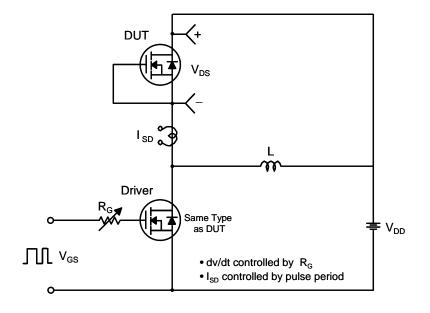
Resistive Switching Test Circuit & Waveforms

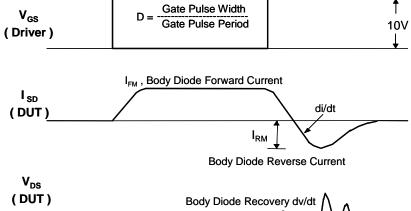


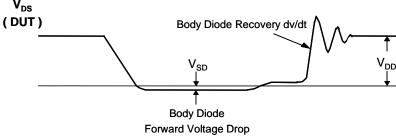
Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms

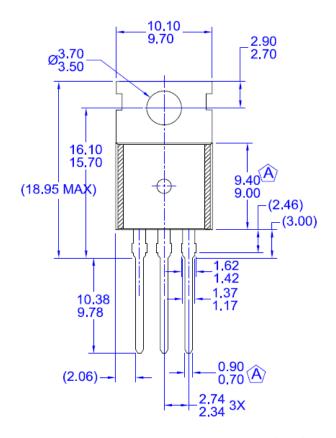


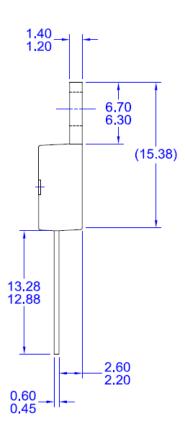


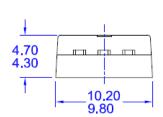


Package Dimensions

TO-220







NOTES:

- (A) CONFORMS TO JEDEC TO-220 VARIATION AB EXCEPT WHERE NOTED
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D) DRAWING FILE/REVISION: MKT-TO220Y03REV1

Dimensions in Millimeters





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