

SGM2013

300mA, Low Power, Low Dropout, 3-Terminal, Linear Regulators

GENERAL DESCRIPTION

The SGM2013 low-power, low-dropout, CMOS linear voltage regulators operate from a 2.5V to 5.5V input and deliver up to 300mA. They are perfect choice for low voltage, low power applications. An ultra low ground current (200 μ A at 300mA output) makes them attractive for battery operated power systems. The SGM2013 series also offer ultra low dropout voltage (300mV at 300mA output) to prolong battery life in portable electronics

The output voltage is preset to voltages in the range of 1.8V to 3.3V. Other features include foldback current limit and thermal shut-down protection.

SGM2013 come in 3-pin SOT23 and 3-pin SOT89 package.

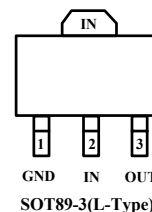
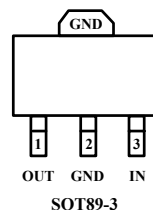
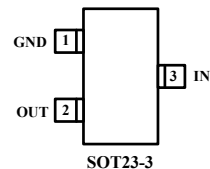
FEATURES

- Ultra-Low Dropout Voltage:
300mV at 300mA output
- Low 77 μ A No-Load Supply Current
- Low 200 μ A Operating Supply Current
at 300mA Output
- High PSRR
- Thermal-Overload Protection
- Output Current Limit
- Output Voltage:
Available in Fixed Outputs 1.8V, 2.5V, 2.7V, 2.8V,
2.9V, 3.0V and 3.3V

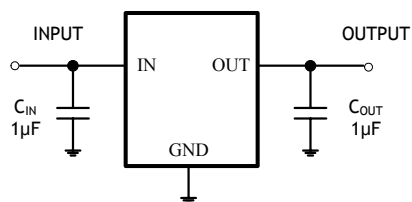
APPLICATIONS

Cellular Telephones
Digital Cameras
MP3, MP4
USB 2.0
Modems
PC Cameras
Hand-Held Instruments
Electronic Dictionaries
Portable/Battery-Powered Equipment

PIN CONFIGURATIONS (TOP VIEW)



TYPICAL OPERATION CIRCUIT



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REV. C

ORDERING INFORMATION

MODEL	V _{OUT} (V)	PIN-PACKAGE	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKAGE OPTION
SGM2013-1.8	1.8V	SOT23-3	- 40°C to +125°C	SGM2013-1.8XN3/TR	XD18	Tape and Reel, 3000
		SOT89-3		SGM2013-1.8XK3/TR	SGM2013-1.8XK3	Tape and Reel, 1000
		SOT89-3(L-Type)		SGM2013-1.8XK3L/TR	SGM2013-1.8XK3L	Tape and Reel, 1000
SGM2013-2.5	2.5V	SOT23-3	- 40°C to +125°C	SGM2013-2.5XN3/TR	XD25	Tape and Reel, 3000
		SOT89-3		SGM2013-2.5XK3/TR	SGM2013-2.5XK3	Tape and Reel, 1000
		SOT89-3(L-Type)		SGM2013-2.5XK3L/TR	SGM2013-2.5XK3L	Tape and Reel, 1000
SGM2013-2.7	2.7V	SOT23-3	- 40°C to +125°C	SGM2013-2.7XN3/TR	XD27	Tape and Reel, 3000
		SOT89-3		SGM2013-2.7XK3/TR	SGM2013-2.7XK3	Tape and Reel, 1000
		SOT89-3(L-Type)		SGM2013-2.7XK3L/TR	SGM2013-2.7XK3L	Tape and Reel, 1000
SGM2013-2.8	2.8V	SOT23-3	- 40°C to +125°C	SGM2013-2.8XN3/TR	XD28	Tape and Reel, 3000
		SOT89-3		SGM2013-2.8XK3/TR	SGM2013-2.8XK3	Tape and Reel, 1000
		SOT89-3(L-Type)		SGM2013-2.8XK3L/TR	SGM2013-2.8XK3L	Tape and Reel, 1000
SGM2013-2.9	2.9V	SOT23-3	- 40°C to +125°C	SGM2013-2.9XN3/TR	XD29	Tape and Reel, 3000
		SOT89-3		SGM2013-2.9XK3/TR	SGM2013-2.9XK3	Tape and Reel, 1000
		SOT89-3(L-Type)		SGM2013-2.9XK3L/TR	SGM2013-2.9XK3L	Tape and Reel, 1000
SGM2013-3.0	3.0V	SOT23-3	- 40°C to +125°C	SGM2013-3.0XN3/TR	XD30	Tape and Reel, 3000
		SOT89-3		SGM2013-3.0XK3/TR	SGM2013-3.0XK3	Tape and Reel, 1000
		SOT89-3(L-Type)		SGM2013-3.0XK3L/TR	SGM2013-3.0XK3L	Tape and Reel, 1000
SGM2013-3.3	3.3V	SOT23-3	- 40°C to +125°C	SGM2013-3.3XN3/TR	XD33	Tape and Reel, 3000
		SOT89-3		SGM2013-3.3XK3/TR	SGM2013-3.3XK3	Tape and Reel, 1000
		SOT89-3(L-Type)		SGM2013-3.3XK3L/TR	SGM2013-3.3XK3L	Tape and Reel, 1000

ABSOLUTE MAXIMUM RATINGS

IN to GND.....- 0.3V to +6V
 Output Short-Circuit Duration.....Infinite
 OUT to GND.....- 0.3V to (V_{IN} + 0.3V)
 Power Dissipation, P_D @ T_A = 25°C
 SOT23-30.4W
 SOT89-30.571W
 Package Thermal Resistance
 SOT23-3, θ_{JA}..... 250°C/W

SOT89-3, θ_{JA}.....175°C/W
 Operating Temperature Range.....- 40°C to +125°C
 Junction Temperature.....+150°C
 Storage Temperature.....- 65°C to +150°C
 Lead Temperature (soldering, 10s).....260°C
 ESD Susceptibility
 HBM.....4000V
 MM.....400V

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

PIN DESCRIPTION

NAME	FUNCTION
IN	Regulator Input. Supply voltage can range from 2.5V to 5.5V.
GND	Ground.
OUT	Regulator Output.

ELECTRICAL CHARACTERISTICS

($V_{IN} = V_{OUT(NOMINAL)} + 0.5V^{(1)}$, $T_A = -40^{\circ}C$ to $+125^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Input Voltage	V_{IN}		2.5		5.5	V	
Output Voltage Accuracy ⁽¹⁾		$I_{OUT} = 1mA$ to $300mA$, $T_A = +25^{\circ}C$ $V_{OUT} + 0.5V \leq V_{IN} \leq 5.5V$	-3		+3	%	
Maximum Output Current			300			mA	
Current Limit	I_{LIM}		310	750		mA	
Ground Pin Current	I_Q	No load,		77	145	μA	
		$I_{OUT} = 300mA$,		200			
Dropout Voltage ⁽²⁾		$I_{OUT} = 1mA$		0.8		mV	
		$I_{OUT} = 300mA$		300	380		
Line Regulation ⁽¹⁾	ΔV_{LNR}	$V_{IN} = 2.5V$ or $(V_{OUT} + 0.5V)$ to $5.5V$, $I_{OUT} = 1mA$		0.03	0.15	%/V	
Load Regulation	ΔV_{LDR}	$I_{OUT} = 0.1mA$ to $300mA$, $C_{OUT} = 1\mu F$		0.0008	0.002	%/mA	
Power Supply Rejection Rate	PSRR	$I_{LOAD} = 50mA$, $C_{OUT} = 1\mu F$	$f = 100Hz$,		75		dB
			$f = 1KHz$,		53		dB
THERMAL PROTECTION							
Thermal Shutdown Temperature	T_{SHDN}			160		$^{\circ}C$	
Thermal Shutdown Hysteresis	ΔT_{SHDN}			15		$^{\circ}C$	

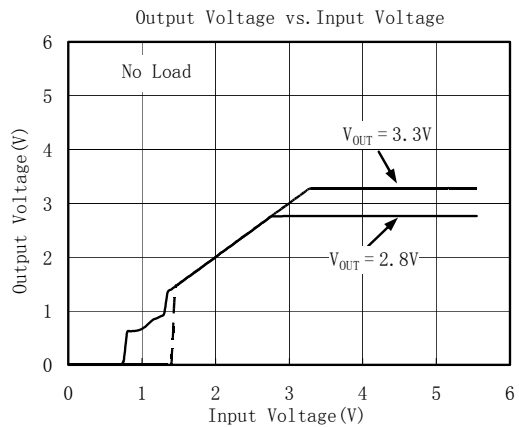
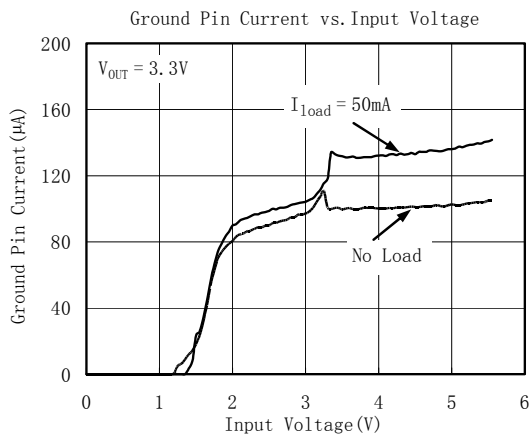
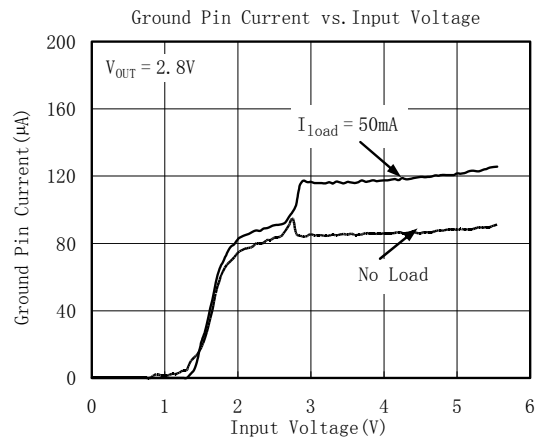
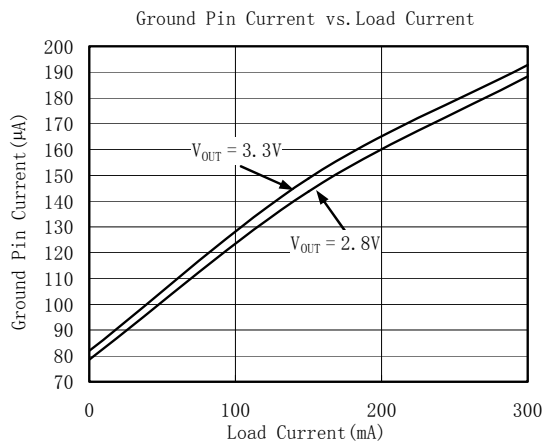
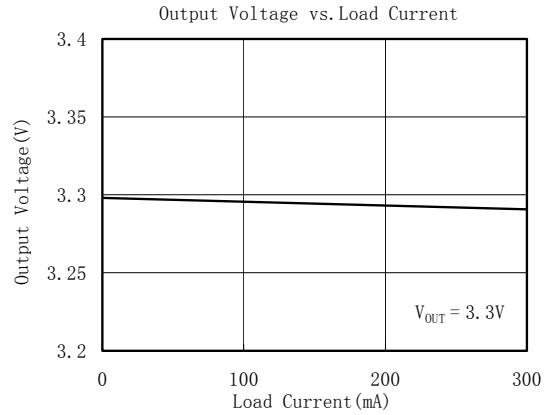
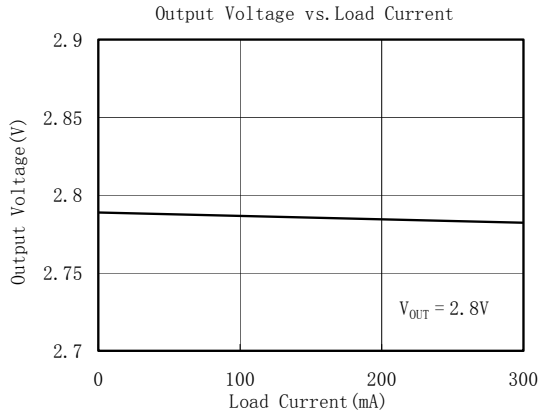
Specifications subject to change without notice.

Note 1: $V_{IN} = V_{OUT(NOMINAL)} + 0.5V$ or $2.5V$, whichever is greater.

Note 2: The dropout voltage is defined as $V_{IN} - V_{OUT}$, when V_{OUT} is 100mV below the value of V_{OUT} for $V_{IN} = V_{OUT} + 0.5V$. (Only applicable for $V_{OUT} = +2.5V$ to $+5.0V$.)

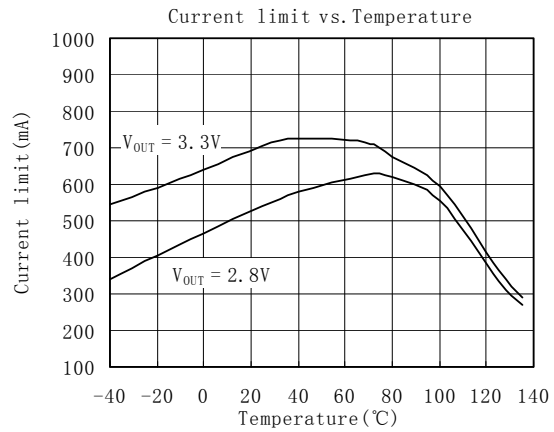
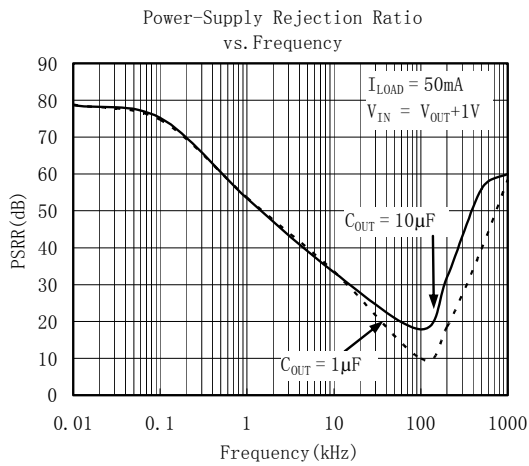
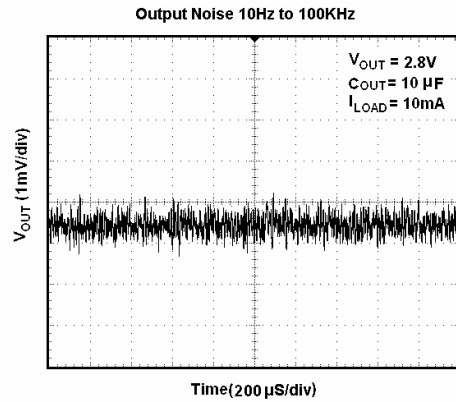
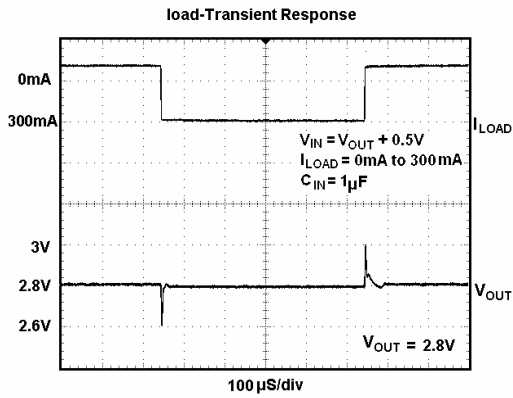
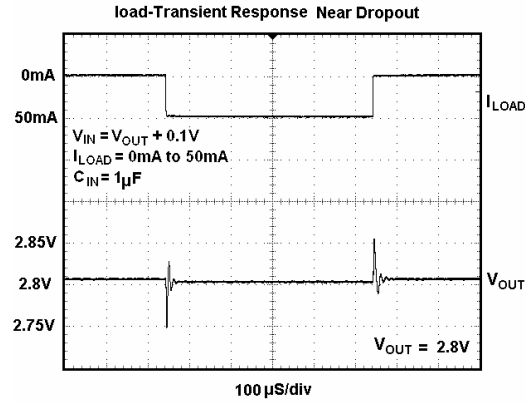
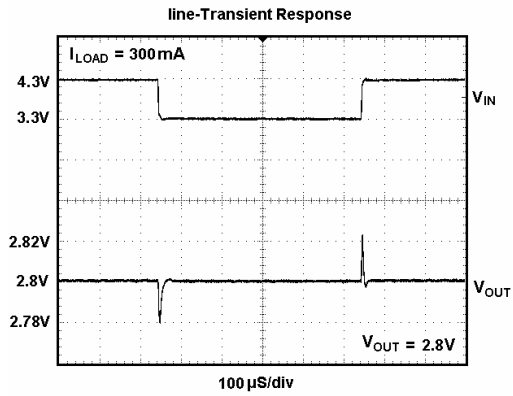
TYPICAL OPERATING CHARACTERISTICS

$V_{IN} = V_{OUT(NOMINAL)} + 0.5V$ or $2.5V$ (whichever is greater), $C_{IN} = 1\mu F$, $C_{OUT} = 1\mu F$, $T_A = +25^\circ C$, unless otherwise noted.



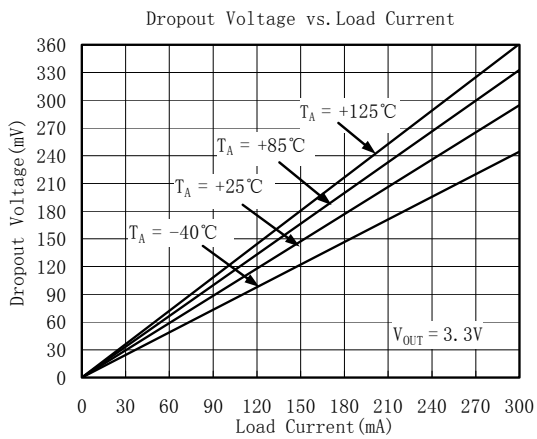
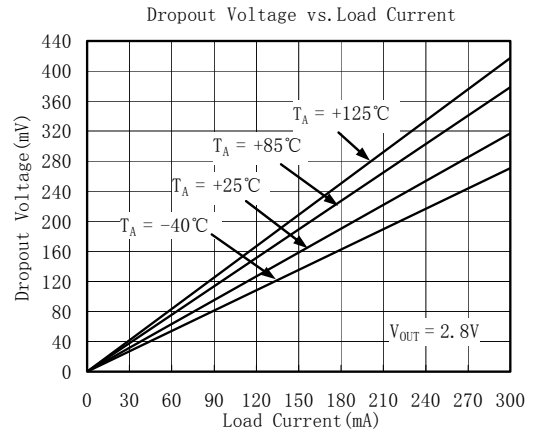
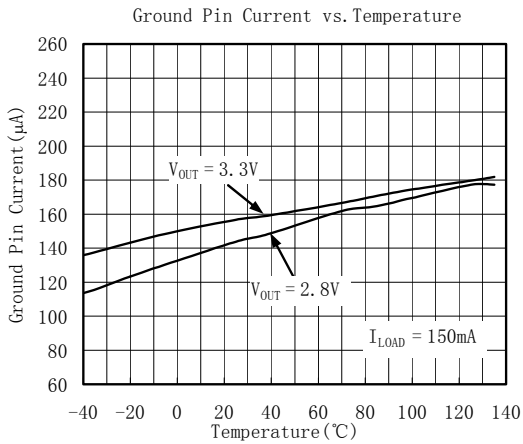
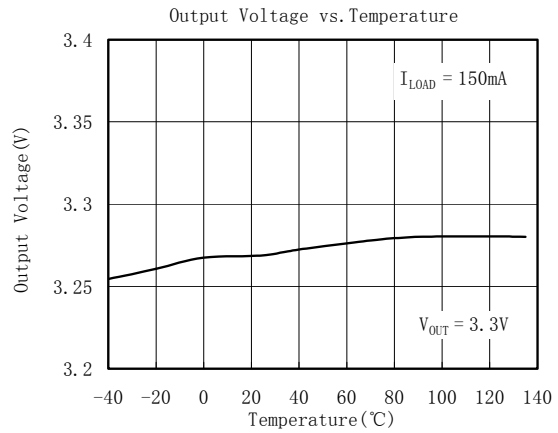
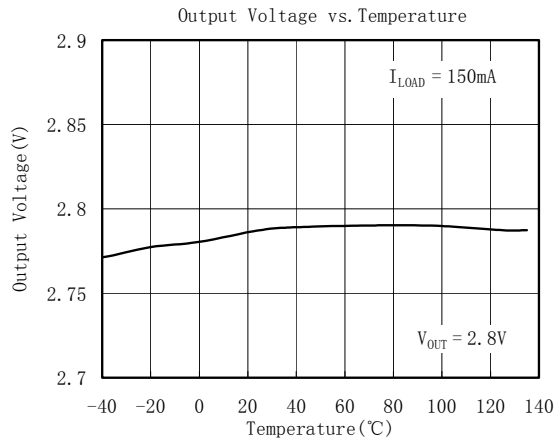
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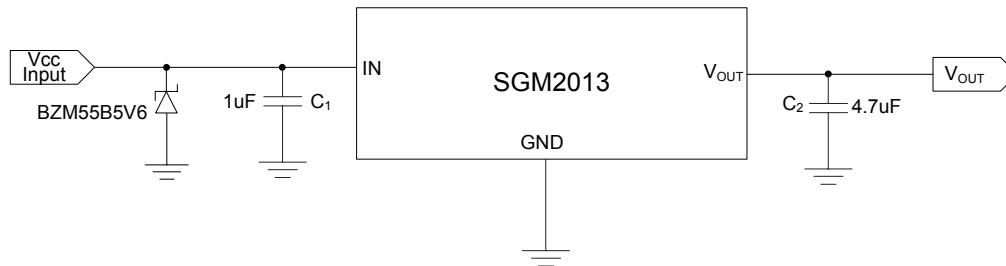
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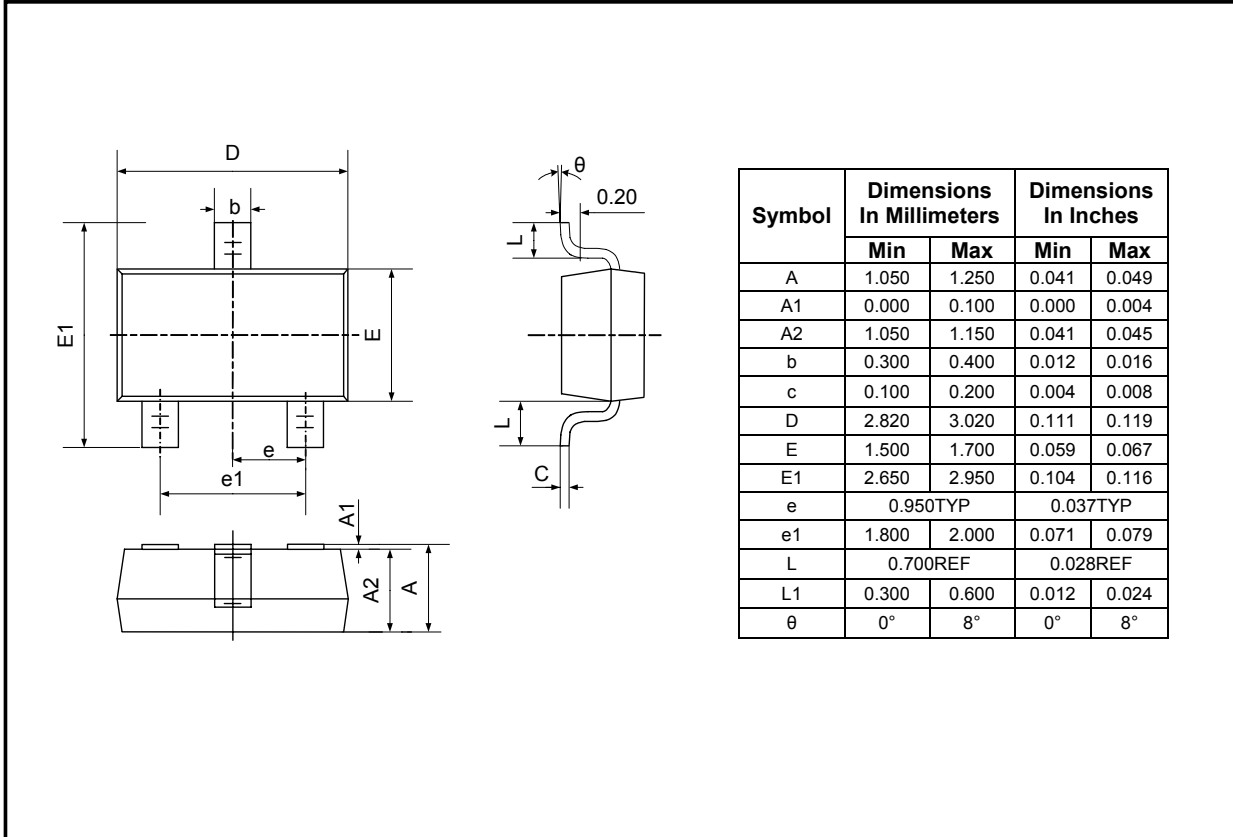
Application Notes

When LDO is used in handheld products, Attention must be paid to voltage spike which would damage SGM2013. In such applications, voltage spike will be generated at charger interface and V_{BUS} pin of USB interface when charger adapters and USB equipments are hot-inserted. Besides this, handheld products will be tested on the production line on the condition of no battery. Test Engineer will apply power from the connector pin which connects with positive pole of the battery. When external power supply is turned on suddenly, the voltage spike will be generated at the battery connector. The voltage spike will be very high, it always exceeds the absolute maximum input voltage (6.0V) of LDO. In order to get robust design. Design Engineer needs to clear up this voltage spike. Zener diode is a cheap and effective solution to eliminate such voltage spike. For example, BZM55B5V6 is a 5.6V small package Zener diode which can be used to remove voltage spike in cell phone design. The schematic is shown in below:



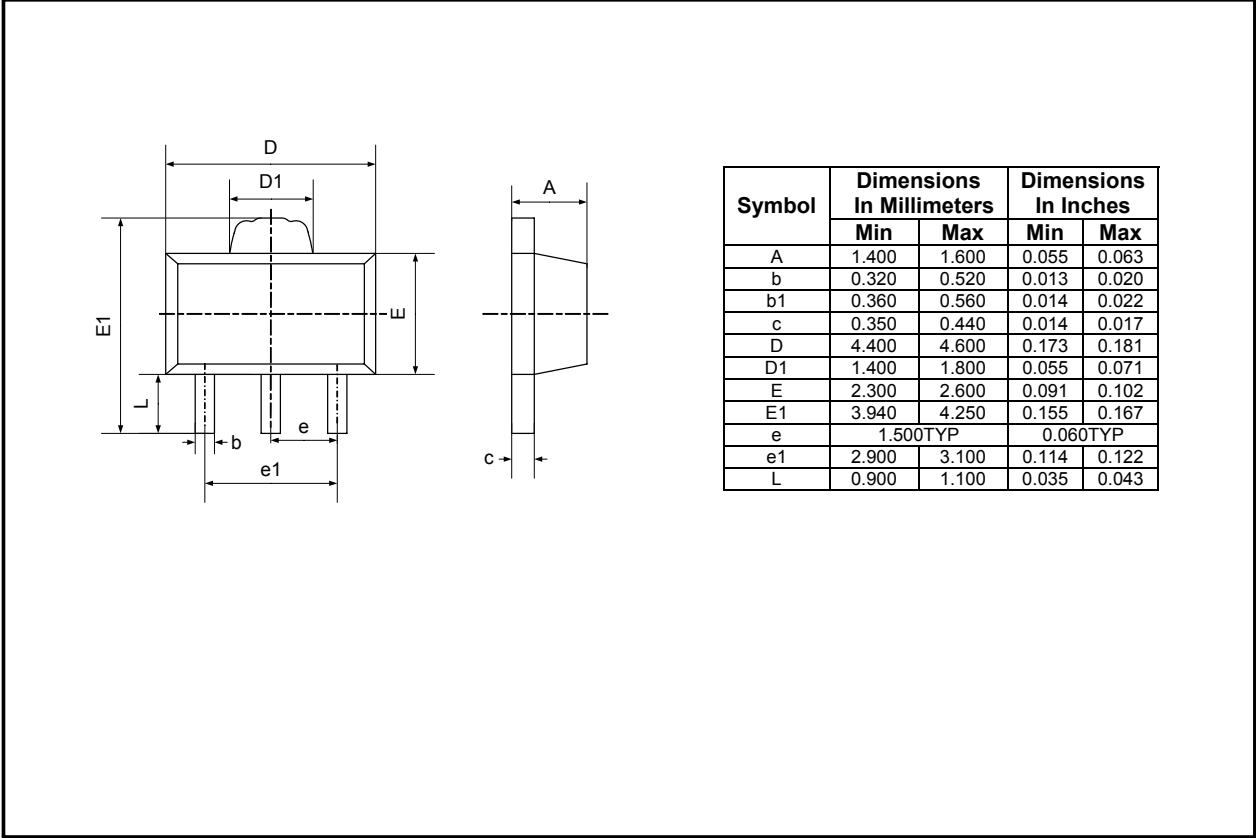
PACKAGE OUTLINE DIMENSIONS

SOT23-3



PACKAGE OUTLINE DIMENSIONS

SOT89-3



REVISION HISTORY

Location	Page
12/06— Data Sheet changed from REV. A to REV. B	
Changed to ABSOLUTE MAXIMUM RATINGS	2
Added Application Notes	7
03/07— Data Sheet changed from REV. B to REV. C	
Changed to TYPICAL OPERATING CHARACTERISTICS	5

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