

## SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

# TIG062E8 - N-Channel IGBT

## N-Channel IGBT Light-Controlling Flash Applications

#### Features

- Low-saturation voltage.
- Low voltage drive (3V).
- Enhansment type.
- Built-in Gate-to-Emitter protection diode.
- Mounting Height 0.9mm, Mounting Area 8.12mm<sup>2</sup>.
- dv / dt guarantee\*.
- Halogen free compliance.

#### Specifications

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Emitter Voltage	VCES		400	V
Gate-to-Emitter Voltage (DC)	VGES		±6	V
Gate-to-Emitter Voltage (Pulse)	VGES	PW≤1ms	±8	V
Collector Current (Pulse)	ICP1	С <sub>М</sub> =150µF, V <sub>GE</sub> =3V	100	А
	I <sub>CP</sub> 2	С <sub>М</sub> =100μF, V <sub>GE</sub> =3.3V	130	А
	ICP3	С <sub>М</sub> =100µF, V <sub>GE</sub> =4V	150	А
Maximum Collector-to-Emitter dv / dt	dV <sub>CE</sub> / dt	V <sub>CE</sub> ≤320V, starting Tch=25°C	400	V/μs
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-40 to +150	°C

Marking : ZC

\*: Concerning dv / dt (slope of Collector Voltage at the time of Turn-OFF), dv / dt > 400V / µs will be 100% screen-detected in the circuit shown as Fig. 1.

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#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I <sub>C</sub> =2mA, V <sub>GE</sub> =0V	400			V
Collector-to-Emitter Cutoff Current	ICES	V <sub>CE</sub> =320V, V <sub>GE</sub> =0V			10	μA
Gate-to-Emitter Leakage Current	IGES	VGE=±6V, VCE=0V			±10	μA
Gate-to-Emitter Threshold Voltage	V <sub>GE</sub> (off)	V <sub>CE</sub> =10V, I <sub>C</sub> =1mA	0.4		0.9	V
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=100A, VGE=3V		5	8	V
Input Capacitance	Cies	V <sub>CE</sub> =10V, f=1MHz		2400		pF
Output Capacitance	Coes	V <sub>CE</sub> =10V, f=1MHz		32		pF
Reverse Transfer Capacitance	Cres	V <sub>CE</sub> =10V, f=1MHz		24		pF

#### Package Dimensions

unit : mm (typ)

7011A-004

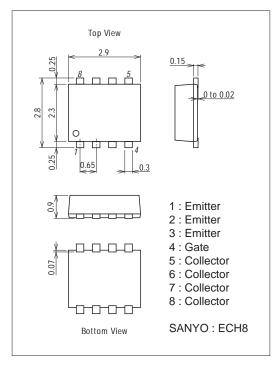
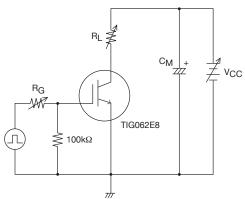


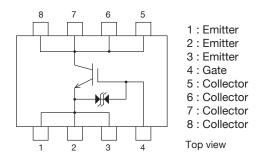
Fig.1 Large Current R Load Switching Circuit

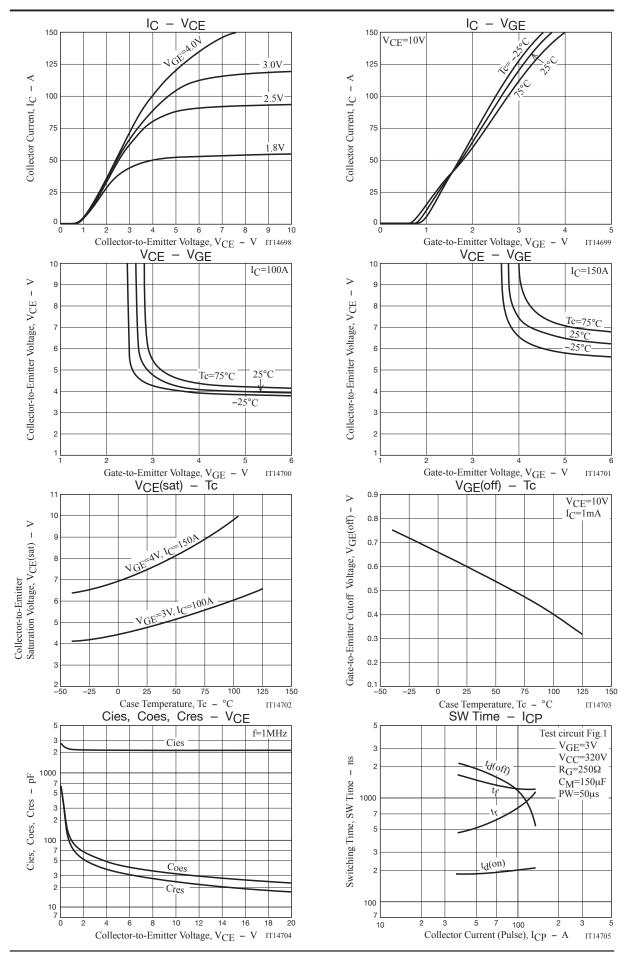


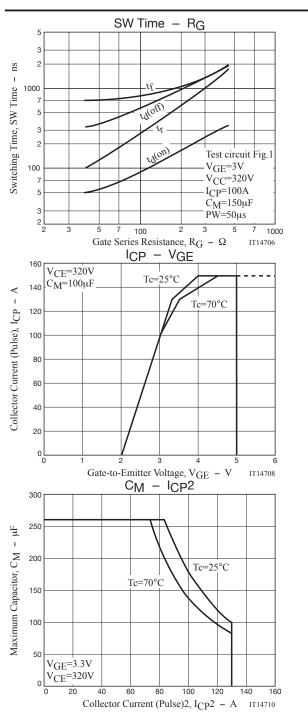
# $\begin{array}{c|c} C_{M} + & \neq \\ ZZ & \neq \\ VCC \end{array}$

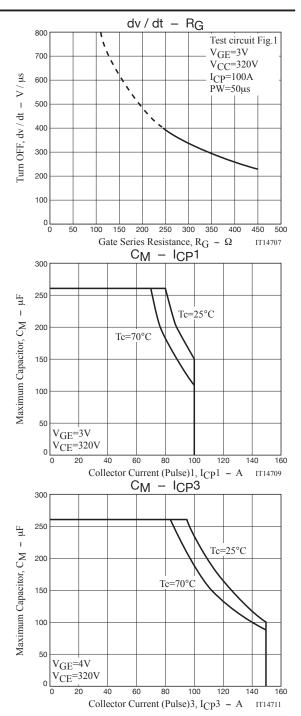
Note1. Gate Series Resistance  $R_G \ge 250\Omega$  is recommended for protection purpose at the time of turn OFF. However, if  $dv / dt \le 400V / \mu s$  is satisfied at customer's actual set evaluation,  $R_G < 250\Omega$  can also be used. Note2. The collector voltage gradient dv / dt must be smaller than  $400V / \mu s$  to protect the device when it is turned off.

#### **Electrical Connection**









- Note : TIG062E8 has protection diode between gate and emitter but handling it requires sufficient care to be taken.
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