

Dwg. No. A-11,407

ABSOLUTE MAXIMUM RATINGS

at 25°C Free-Air Temperature for any one driver (unless otherwise noted)

Developed for use with low-voltage LED and incandescent displays requiring low output saturation voltage, the UDN2595A and A2595SLW meet many interface needs, including those exceeding the capabilities of standard logic buffers. The eight non-Darlington outputs of each driver can continuously and simultaneously sink load currents of 100 mA at ambient temperatures of up to +75°C.

The eight-channel driver's active-low inputs can be driven directly from TTL, Schottky TTL, DTL, 5 to 16 V CMOS, and NMOS logic. All input connections are on one side of the package, output connections on the other, for simplified printed wiring board layouts.

These drivers are packaged in plastic DIPs (suffix A) or surface-mountable wide-body SOlCs (suffix LW), and are rated for operation over the temperature range of -20°C to +85°C. The A2595SLW is also available for operation to -40°C. To order, change the suffix from 'SLW' to 'ELW'.

FEATURES

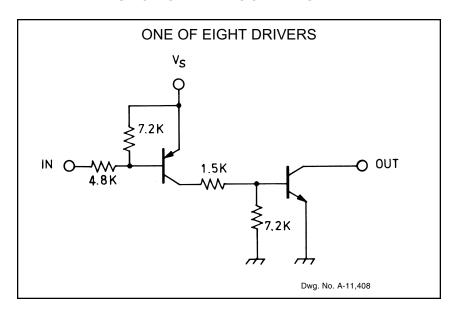
- Non-Inverting Function (Input Low = Output ON)
- 200 mA Current Rating
- 100 mA Continuous and Simultaneous (All outputs) to +85°C
- Low Saturation Voltage
- TTL, CMOS, NMOS Compatible
- Efficient Input/Output Pin Format
- DIP or SOIC Packaging

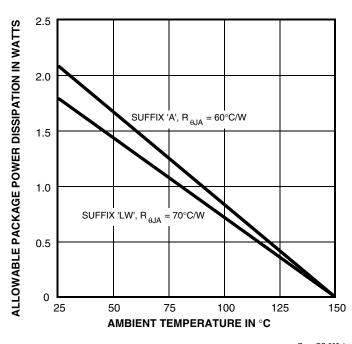
Always order by complete part number:

Part Number	Package
UDN2595A	18-Pin DIP
A2595SLW	20-Lead Wide-Body SOIC

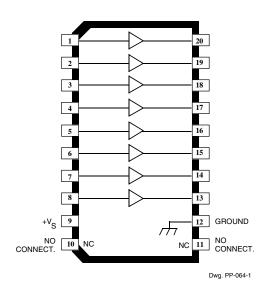


FUNCTIONAL BLOCK DIAGRAM





A2595SLW







ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$, $V_S = 5.0 \text{ V}$ (unless otherwise noted).

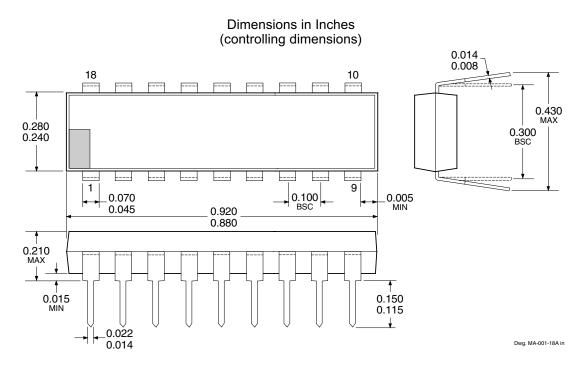
			Limits		
Characteristic	Symbol	Test Conditions	Min.	Max.	Units
Output Leakage	I _{CEX}	V _{IN} ≥ 4.5 V, V _{OUT} = 20 V, T _A = 25°C	_	50	μΑ
Current		V _{IN} ≥ 4.6 V, V _{OUT} = 20 V, T _A = 70°C	_	100	μΑ
Output Saturation	V _{CE(SAT)}	V _{IN} = 0.4 V, I _{OUT} = 50 mA	_	0.5	V
Voltage	, ,	V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	0.6	V
Input Current	I _{IN(ON)}	V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	-1.6	mA
		V_{IN} = 0.4 V, I_{OUT} = 100 mA, V_{S} = 15 V	_	-5.0	mA
Input Voltage	V _{IN(ON)}	I _{OUT} = 100 mA, V _{OUT} ≤ 0.6 V	_	0.4	V
	V _{IN(OFF)}	I _{OUT} = 100 μA, T _A = 70°C	4.6	_	V
Input Capacitance	C _{IN}		_	25	pF
Supply Current	I _S	V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	6.0	mA
		V _{IN} = 0.4 V, I _{OUT} = 100 mA, V _S = 15 V	_	20	mA

NOTES: 1. Negative current is defined as coming out of the specified device pin.

2. The $V_{\text{IN}(\text{ON})}$ voltage limit guarantees a minimum output sink current per the specified conditions.

3. l_S is measured with any one of eight drivers turned ON.

UDN2595A



(for reference only) 0.355 0.204 18 10 10.92 MAX 7.11 6.10 7.62 BSC 9 2.54 BSC 0.13 MIN 1.77 1.15 23.37 22.35 5.33 MAX 0.39 3.81 2.93 _0.558 0.356 Dwg. MA-001-18A mm

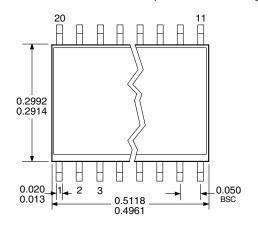
Dimensions in Millimeters

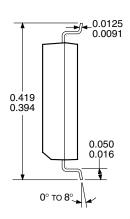
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
 - 2. Lead spacing tolerance is non-cumulative.
 - 3. Lead thickness is measured at seating plane or below.

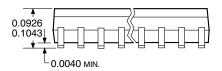


A2595SLW

Dimensions in Inches (for reference only)

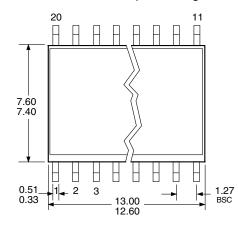


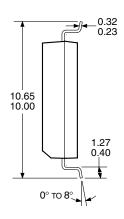


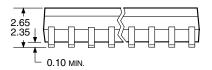


Dwg. MA-008-20 in

Dimensions in Millimeters (controlling dimensions)







Dwg. MA-008-20 mm

NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

2. Lead spacing tolerance is non-cumulative.

2595 8-CHANNEL SATURATED SINK DRIVER

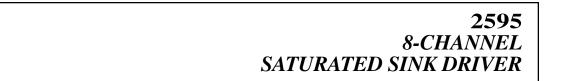
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2595 8-CHANNEL SATURATED SINK DRIVER

POWER SINK DRIVERS

IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

Output Ratings *			Features					
		Serial	Latched	Diode		Internal	_	
mA	V	#	Input	Drivers	Clamp	Outputs	Protection	Part Number [†]
75	17	8	X X	X X		constant current		6275 6276
100	17 20	16 8			_ (constant current		2595
100	30	32	X	X	_	saturated –	_	5833
	40	32	X	X	_	saturated	_	5832
	50	8	addre	ssable deco	der/driver	DMOS	_	6B259
	50 50	8 8	X	X X	_	DMOS DMOS	_	6B273 6B595
250	50	8		ssable deco	der/driver	DMOS	_	6259
	50	8	_	X	_	DMOS	_	6273
	50 135	8 7	X -	Х	X	DMOS -		6595 7003
300	45	1		all sensor/driv		<u>_</u>	X	5140
300	50	7	_ 11c		Χ	_	_	2003
	50	8	_	_	X	_	_	2803
	50	8	_	_	X X	saturated	X	2596
	60 95	4 7	_	_	X	saturated –	X -	2557 2023
	95	8	_	_	X	_	_	2823
350	50	4	_	X	Х	_	_	5800
	50 50	7	_	_	X X X	_	_	2004
	50 50	8 8	_	X	X	_	_	2804 5801
	50	8	X	x	_	_	_	5821
	50	8	X	X	. X	_	_	5841
	50 50	8 8	addre X	ssable decod	der/driver	DMOS DMOS	_	6A259 6A595
	80	8	X	X X	_		_	5822
	80	8	X	X	X X	_	_	5842
	95	7	_	_		-	_	2024
450	95 30	8 	- dual /	- 1- to 14-line o	X	<u> </u>	<u> </u>	2824 6817
600	60	4	uuai ²	+- to 14-iiie c	iecouei/unv	saturated	X	2547
000	60	4	_	_	X	saturated	x	2549
700	60	4	_	_	Х	saturated	Χ	2543 and 2559
750	50	8	_	_	Х	saturated	_	2597
900	14	2		II sensor/driv		saturated	X	3625
4000	26	2		all sensor/driv		saturated	X	3626
1000	46	4		er motor con				7024 and 7029
1200	46	4		stepping con			X	7042 5804
1250	50 50	4 4	stepp –	er motor tran –	isiator/drive X	r – –	X -	2064 and 2068
1500	80	4	_	_	X	_	_	2065 and 2069
1800	50	4	_	_	Х	_	_	2544
	50	4	_	_	X	_	_	2540
3000	46	4		er motor con			_	7026
4000	46	4	micro	stepping con			_	7044
4000	50 80	4 4	_	_	X X	<u> </u>	_ _	2878 2879
	- 50	•			/\			_0.0

^{*} Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

[†] Complete part number includes additional characters to indicate operating temperature range and package style.

